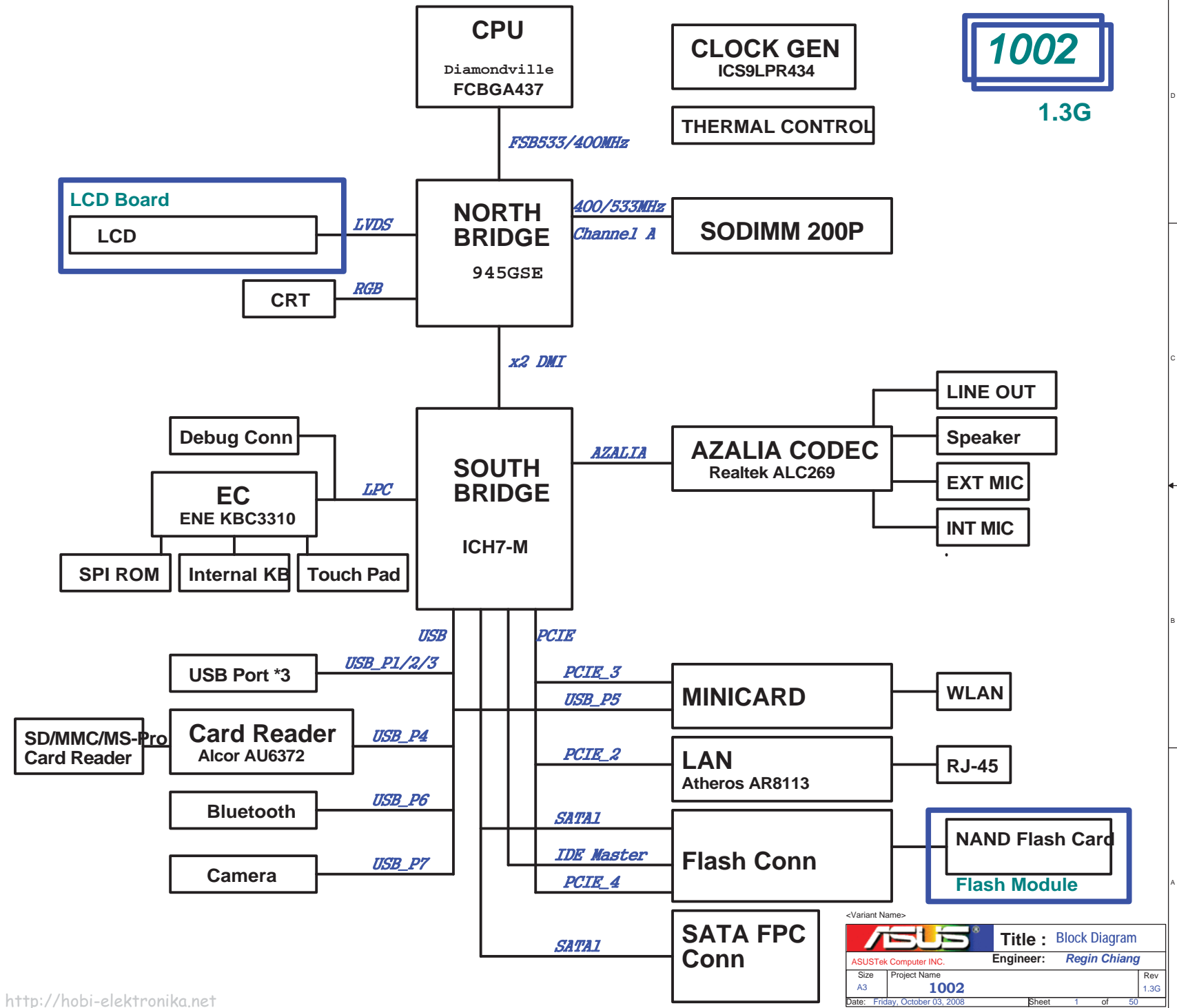


- 01\_Block Diagram
- 02\_System Setting
- 03\_Power Sequence
- 04\_Clock Gen\_ICS9LPR434
- 05\_Diamondville\_BUS
- 06\_Diamondville\_PWR
- 07\_NB-945GMS(HOST)
- 08\_NB-945GMS(DMI)
- 09\_NB-945GMS(GRAPHIC)
- 10\_NB-945GMS(DDR2)
- 11\_NB-945GMS(PWR)
- 12\_NB-945GMS(PWR2)
- 13\_NB-945GMS(GND)
- 14\_SB-ICH7M(PWR)
- 15\_SB-ICH7M(1)
- 16\_SB-ICH7M(2)
- 17\_SB-ICH7M(3)
- 18\_DDR2 SODIMM
- 19\_DDR2 Termination
- 20\_Onboard VGA
- 21\_LCD Conn\_LID
- 22\_Blank
- 23\_Mini WIFI+ BT
- 24\_LAN\_Atheros AR8113
- 25\_RJ45
- 26\_Flash Conn
- 27\_USB Port
- 28\_Camera Conn
- 29\_Card Reader\_AU6372A51
- 30\_Codec\_ALC269
- 31\_Audio\_AMP\_Jack
- 32\_EC\_ENE KB3310
- 33\_EC
- 34\_Switch\_SPI ROM\_Debug Conn
- 35\_Thermal Sensor\_FAN
- 36\_KB\_Touch Pad
- 37\_LED\_THERMTRIP
- 38\_Discharge
- 39\_PWR Jack
- 40\_Srew Hole
- 41\_EMI
- 42\_POWER FLOW
- 43\_Vcore
- 44\_Power System
- 45\_Power\_+1.8V & VTTDDR
- 46\_Power\_VCCP
- 47\_Power\_+1.5V & +2.5V
- 48\_Power\_Charger
- 49\_EC Pin Define
- 49\_History



<Variant Name>

<b>ASUS</b>		<b>Title : Block Diagram</b>	
ASUSTek Computer INC.		Engineer: <i>Regin Chiang</i>	
Size	Project Name	Rev	
A3	<b>1002</b>	1.3G	
Date: Friday, October 03, 2008	Sheet 1 of 50		

**EEE PC 701 PCB version**

GPI37	GPI38	GPI39	PCB version
0	0	0	
0	0	0	
0	0	1	
0	0	1	
0	1	0	
0	1	0	
0	1	1	
0	1	1	
1	0	0	
1	0	0	
1	0	1	
1	0	1	
1	1	0	
1	1	0	
1	1	1	
1	1	1	

**USB**

USB 0	NC
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	Minicard
USB 6	Bluetooth
USB 7	Camera


**PCIE**

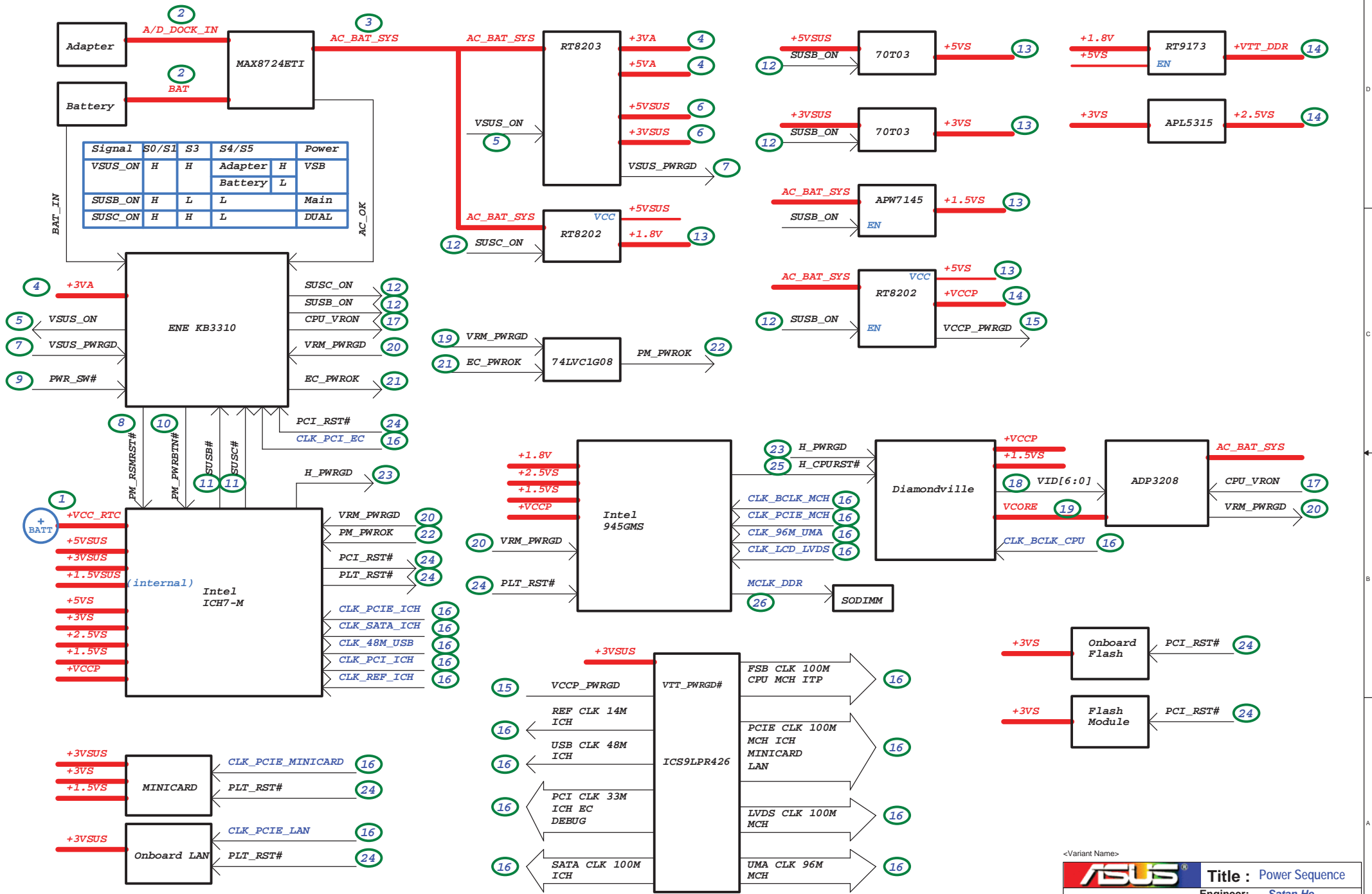
PCIE 1	NC
PCIE 2	LAN
PCIE 3	Minicard
PCIE 4	SSD

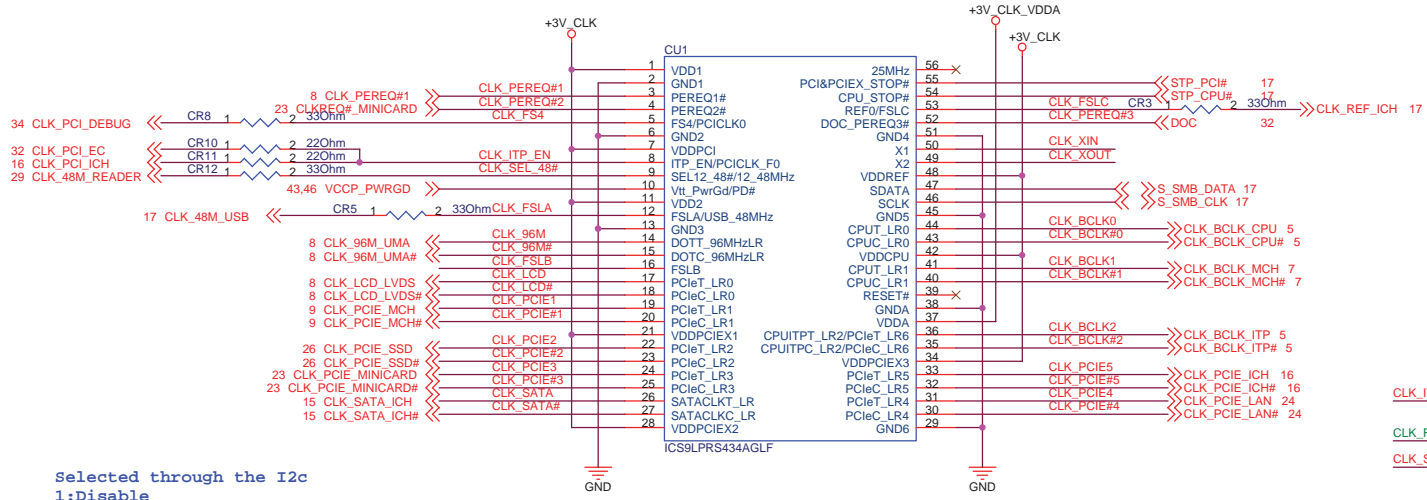
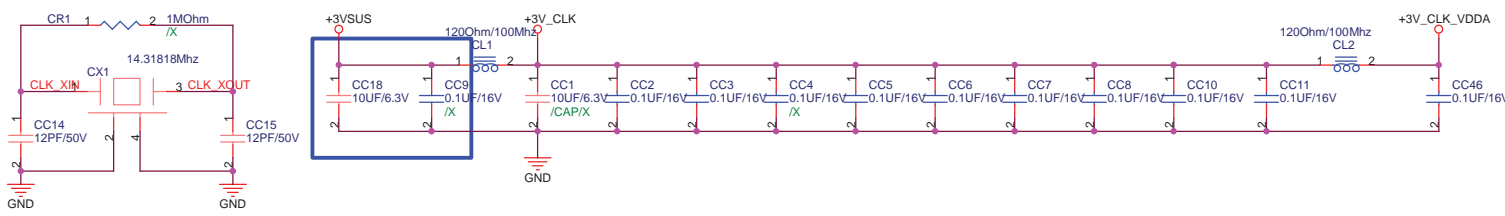
**Azalia**

ACZ_SDIN0	CODEC
ACZ_SDIN1	NC
ACZ_SDIN2	NC

<Variant Name>

		<b>Title : System Setting</b>	
ASUSTek Computer INC.		Engineer: <i>Satan_He</i>	
Size	Project Name		Rev
A3	<b>S101</b>		1.3G
Date: Friday, October 03, 2008		Sheet	2 of 50

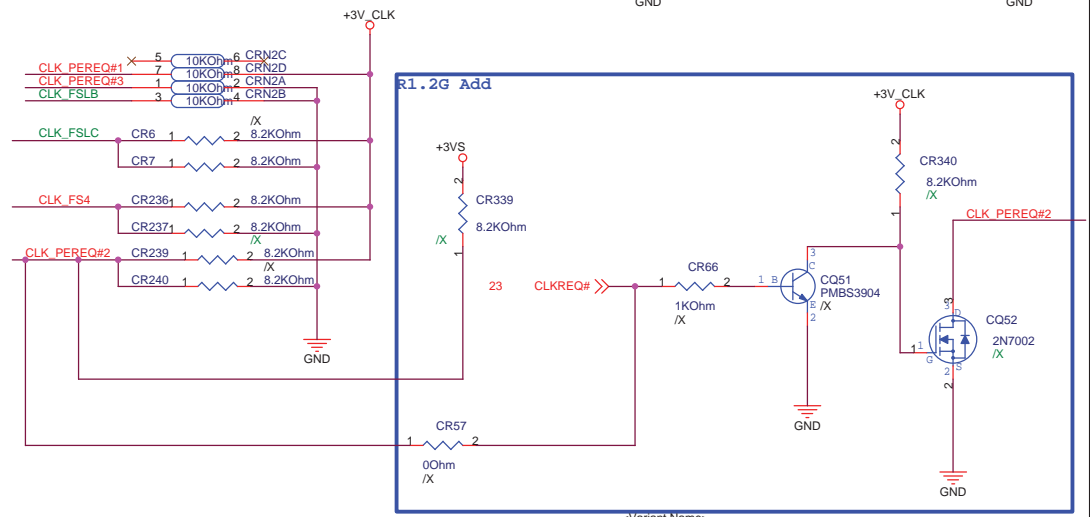
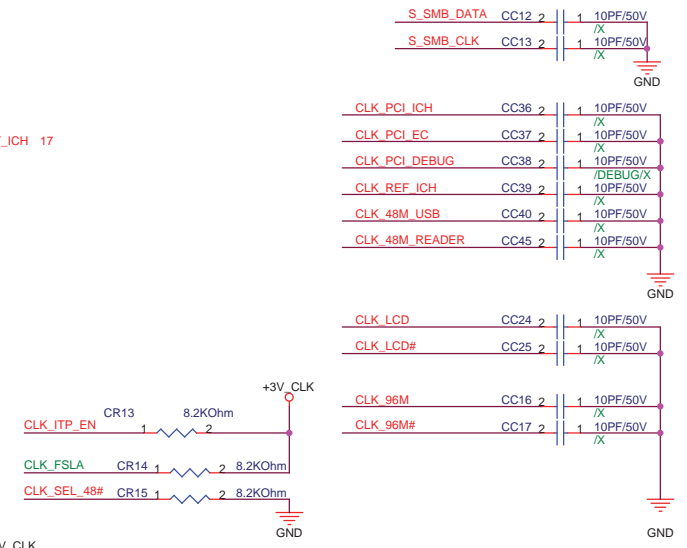
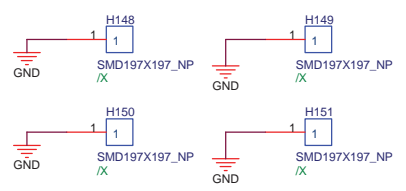




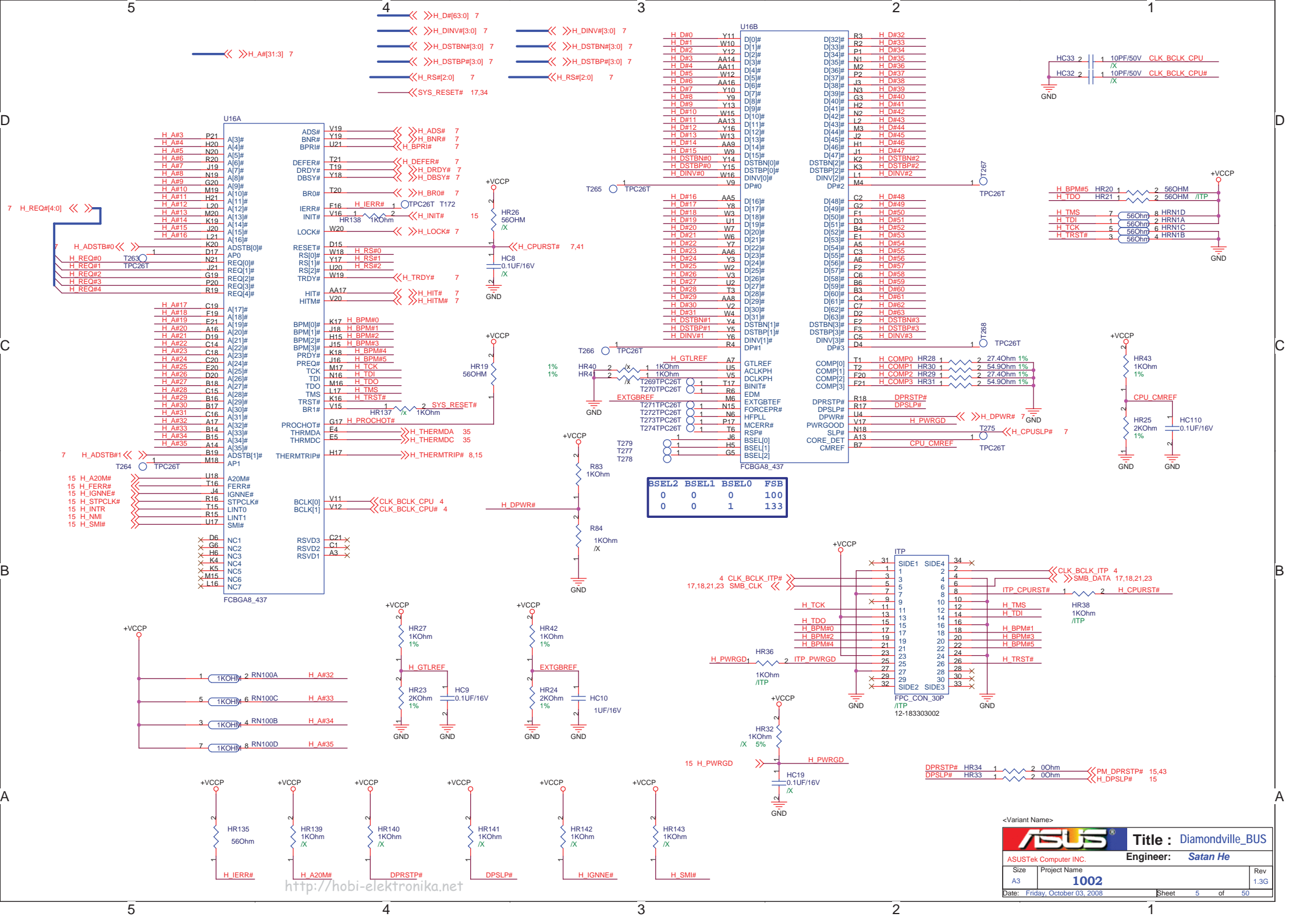
Selected through the I2c  
 1:Disable  
 0:Enable

PEREQ1:PCIEx0 & PCIEx1  
 PEREQ2:PCIEx2 & PCIEx3 & SATA  
 PEREQ3:PCIEx4 & PCIEx5 & PCIEx6

H148-H151 reserve to place GASKET for EMI



ASUS® Title : Clock\_Gen\_ICS9LPRS434  
 ASUSTek Computer INC. Engineer: **Satan He**  
 Size: A3 Project Name: **1002** Rev: 1.3G  
 Date: Friday, October 03, 2008 Sheet: 4 of 50

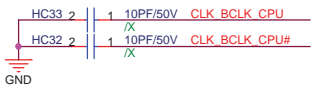


<<<H\_A#[31:3] 7

<<<H\_D#[63:0] 7  
<<<H\_DIN#[3:0] 7  
<<<H\_DSTBN#[3:0] 7  
<<<H\_DSTBP#[3:0] 7  
<<<H\_RS#[2:0] 7  
<<<H\_RS#[2:0] 7  
<<<SYS\_RESET# 17,34

U16B  
H D#0 Y11  
H D#1 W10  
H D#2 Y12  
H D#3 AA14  
H D#4 AA11  
H D#5 W12  
H D#6 AA16  
H D#7 Y10  
H D#8 Y9  
H D#9 Y13  
H D#10 W15  
H D#11 AA13  
H D#12 W13  
H D#14 AA9  
H D#15 W9  
H DSTBN#0 Y14  
H DSTBP#0 Y15  
H DIN#0 W16  
D#0#  
D[0]#  
D[1]#  
D[2]#  
D[3]#  
D[4]#  
D[5]#  
D[6]#  
D[7]#  
D[8]#  
D[9]#  
D[10]#  
D[11]#  
D[12]#  
D[13]#  
D[14]#  
D[15]#  
DSTBN[0]#  
DSTBP[0]#  
DIN[0]#  
DP#0

R3 H D#32  
R2 H D#33  
P1 H D#34  
N1 H D#35  
M2 H D#36  
P2 H D#37  
J3 H D#38  
N3 H D#39  
G3 H D#40  
H2 H D#41  
L2 H D#42  
M3 H D#43  
J2 H D#44  
H1 H D#45  
J1 H D#46  
K2 H DSTBN#2  
K3 H DSTBP#2  
L1 H DIN#2  
M4  
C2 H D#48  
G2 H D#49  
F1 H D#50  
D3 H D#51  
B4 H D#52  
E1 H D#53  
A5 H D#54  
C3 H D#55  
A6 H D#56  
F2 H D#57  
C6 H D#58  
B6 H D#59  
B3 H D#60  
C4 H D#61  
C7 H D#62  
D2 H D#63  
E2 H DSTBN#3  
F3 H DSTBP#3  
C5 H DIN#3  
DP#1  
T1 H COMP0 HR28 1  
T2 H COMP1 HR30 1  
F20 H COMP2 HR29 1  
F21 H COMP3 HR31 1  
R18 DPRSTP#  
R17 DPSLP#  
U4  
V17 H\_PWRGD  
V18 H\_PWRGD  
V19 H\_PWRGD  
A13 CORE\_DET  
B7 CMREF



7 H\_REQ#[4:0] <<<  
7 H\_ADSTB#0 <<<  
H\_REQ#0 T263  
H\_REQ#1 TPC26T  
H\_REQ#2 G19  
H\_REQ#3 P20  
H\_REQ#4 R19

7 H\_ADSTB#1 <<<  
15 H\_A20M#  
15 H\_FERR#  
15 H\_IGNNE#  
15 H\_STPCLK#  
15 H\_INTR#  
15 H\_NMI#  
15 H\_SMI#

U16A  
H A#3 P21  
H A#4 H20  
H A#5 N20  
H A#6 R20  
H A#7 J19  
H A#8 N19  
H A#9 G20  
H A#10 M19  
H A#11 H21  
H A#12 L20  
H A#13 M20  
H A#14 K19  
H A#15 J21  
H A#16 L20  
A[3]#  
A[4]#  
A[5]#  
A[6]#  
A[7]#  
A[8]#  
A[9]#  
A[10]#  
A[11]#  
A[12]#  
A[13]#  
A[14]#  
A[15]#  
A[16]#  
K20  
ADSTB[0]#  
D17  
N21  
REQ[0]#  
REQ[1]#  
REQ[2]#  
REQ[3]#  
REQ[4]#  
R19

H A#17 C19  
H A#18 E19  
H A#19 E21  
H A#20 A16  
H A#21 D19  
H A#22 C14  
H A#23 C18  
H A#24 C18  
H A#25 E20  
H A#26 D20  
H A#27 B18  
H A#28 C15  
H A#29 B16  
H A#30 B17  
H A#31 C16  
H A#32 A17  
H A#33 B14  
H A#34 B15  
H A#35 A14  
A[17]#  
A[18]#  
A[19]#  
A[20]#  
A[21]#  
A[22]#  
A[23]#  
A[24]#  
A[25]#  
A[26]#  
A[27]#  
A[28]#  
A[29]#  
A[30]#  
A[31]#  
A[32]#  
A[33]#  
A[34]#  
A[35]#  
ADSTB[1]#  
AP1

ADS# V19  
BNR# Y19  
BPR# U21  
DEFER# T21  
DRDY# T19  
DBSY# Y18  
BR#0 T20  
IERR# F16  
INIT# HR138  
LOCK# W20  
RESET# D15  
RS[0]# W18  
RS[1]# U20  
RS[2]# W19  
TRDY# W19  
HIT# AA17  
HITM# V20  
V19 <<<H\_ADS# 7  
Y19 <<<H\_BNR# 7  
U21 <<<H\_BPR# 7  
T21 <<<H\_DEFER# 7  
T19 <<<H\_DRDY# 7  
Y18 <<<H\_DBSY# 7  
T20 <<<H\_BR# 7  
F16 <<<H\_IERR# 7  
HR138 <<<H\_INIT# 7  
W20 <<<H\_LOCK# 7  
D15 <<<H\_RESET# 7  
W18 <<<H\_RS#0 7  
U20 <<<H\_RS#1 7  
W19 <<<H\_RS#2 7  
W19 <<<H\_TRDY# 7  
AA17 <<<H\_HIT# 7  
V20 <<<H\_HITM# 7

K17 H\_BPM#0  
J18 H\_BPM#1  
H15 H\_BPM#2  
J15 H\_BPM#3  
K18 H\_BPM#4  
M17 H\_BPM#5  
L16 H\_TCK  
N16 H\_TDI  
M16 H\_TDO  
L17 H\_TMS  
K16 H\_TRST#  
V15  
G17 H\_PROCHOT#  
E4  
E5  
H17  
K17 H\_BPM#0  
J18 H\_BPM#1  
H15 H\_BPM#2  
J15 H\_BPM#3  
K18 H\_BPM#4  
M17 H\_BPM#5  
L16 H\_TCK  
N16 H\_TDI  
M16 H\_TDO  
L17 H\_TMS  
K16 H\_TRST#  
V15  
G17 H\_PROCHOT#  
E4  
E5  
H17

PROCHOT# E4  
THRMDA THRMDC  
H17  
H\_THERMDA 35  
H\_THERMDC 35  
H\_THERMTRIP# 8,15  
V11  
V12  
C21  
C17  
A3  
C21 X  
C17 X  
A3 X  
BCLK[0] V11  
BCLK[1] V12  
RSVD3  
RSVD2  
RSVD1

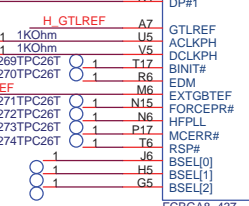
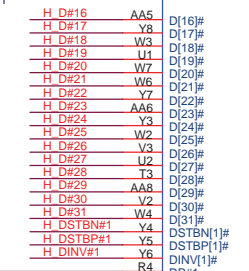
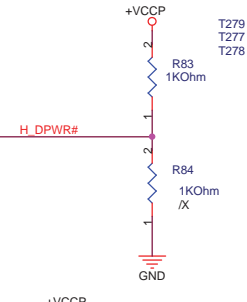
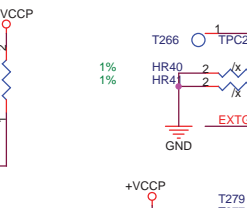
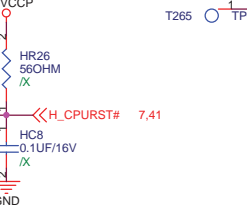
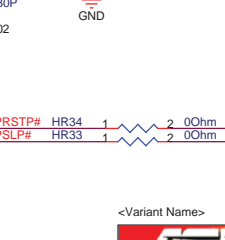
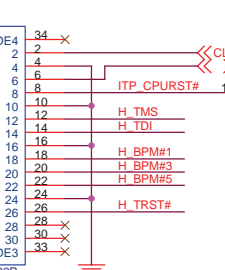
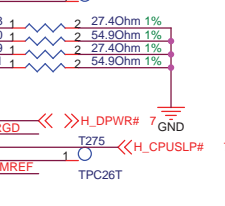
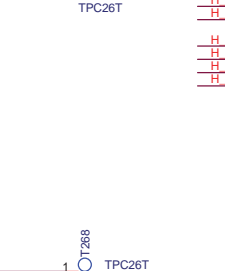
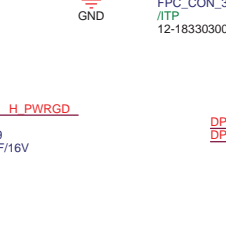
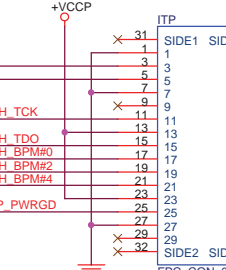
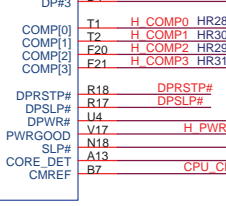
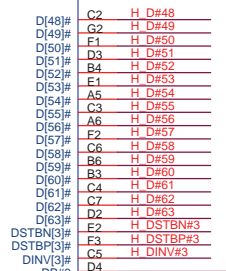
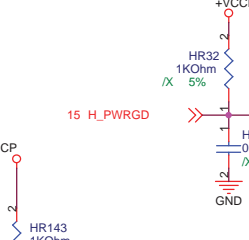
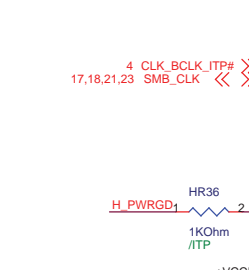
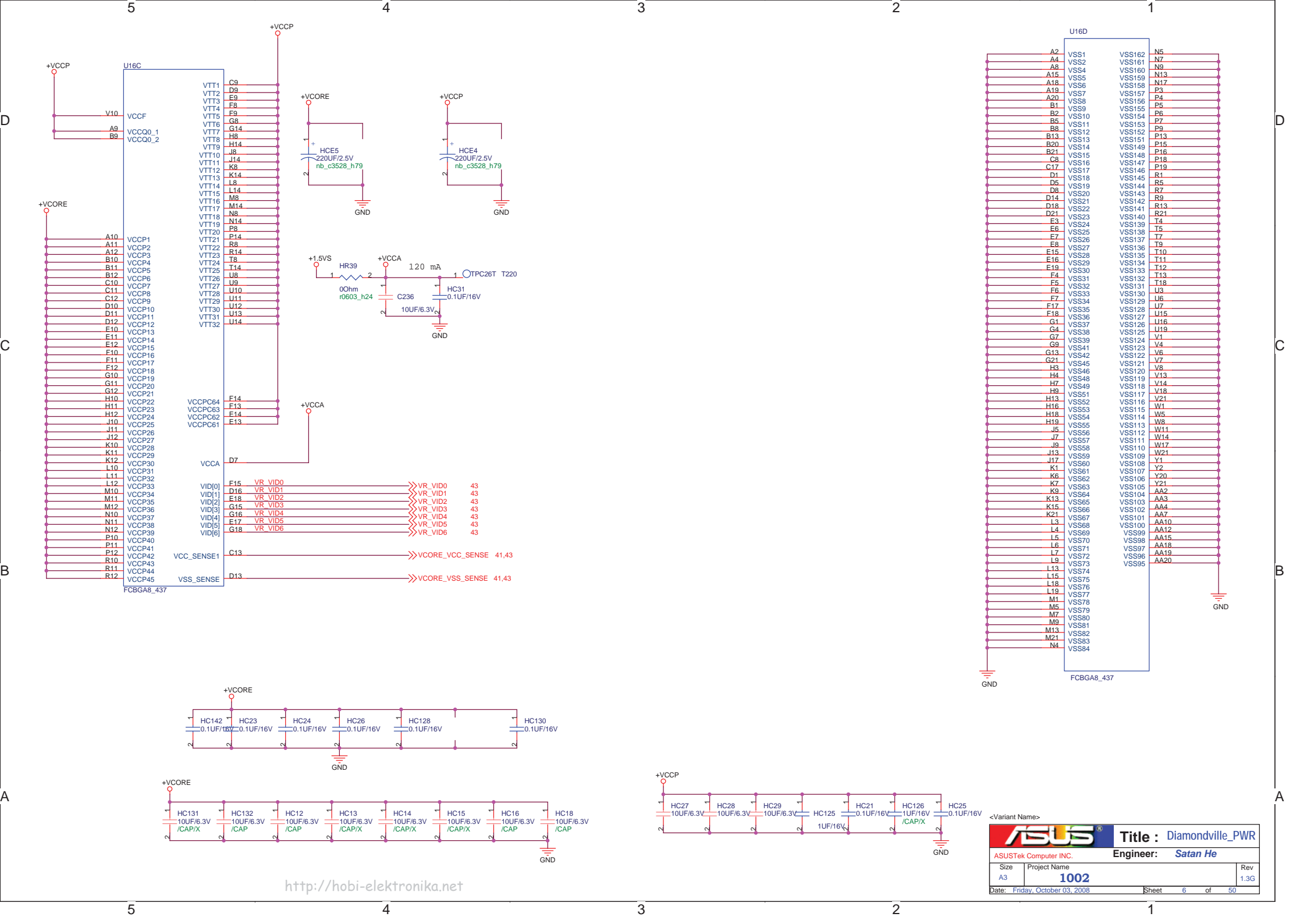


Table with 4 columns: BSEL2, BSEL1, BSEL0, FSB. Values are 0, 0, 0, 1.00 and 0, 0, 1, 1.33.



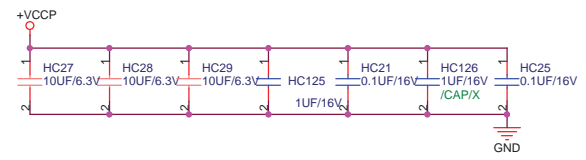
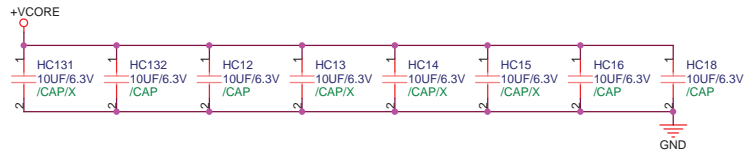
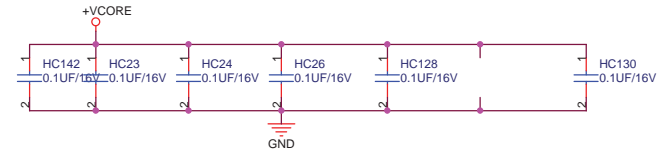
ASUS logo, Title: Diamondville\_BUS, Engineer: Satan He, Size A3, Project Name 1002, Rev 1.3G, Date: Friday, October 03, 2008, Sheet 5 of 50



U16D

A2	VSS1	VSS162	N5
A4	VSS2	VSS161	N7
A8	VSS4	VSS160	N9
A15	VSS5	VSS159	N13
A18	VSS6	VSS158	P3
A19	VSS7	VSS157	P4
A20	VSS8	VSS156	P5
B1	VSS9	VSS155	P6
B2	VSS10	VSS154	P7
B5	VSS11	VSS153	P9
B8	VSS12	VSS152	P13
B13	VSS13	VSS151	P15
B20	VSS14	VSS149	P16
B21	VSS15	VSS148	P18
C8	VSS16	VSS147	P19
C17	VSS17	VSS146	R5
D1	VSS18	VSS145	R7
D5	VSS19	VSS144	R9
D8	VSS20	VSS143	R13
D14	VSS21	VSS142	R21
D18	VSS22	VSS141	T4
D21	VSS23	VSS140	T5
E3	VSS24	VSS139	T7
E6	VSS25	VSS138	T9
E7	VSS26	VSS137	T10
E8	VSS27	VSS136	T11
E15	VSS28	VSS135	T12
E16	VSS29	VSS134	T13
E19	VSS30	VSS133	T18
F4	VSS31	VSS132	U3
F5	VSS31	VSS132	U6
F6	VSS33	VSS130	U7
F7	VSS34	VSS129	U15
F17	VSS35	VSS128	U16
F18	VSS35	VSS128	U19
G4	VSS38	VSS125	V1
G7	VSS39	VSS124	V4
G9	VSS41	VSS123	V6
G13	VSS42	VSS122	V8
G21	VSS45	VSS121	V13
H3	VSS46	VSS120	V14
H4	VSS48	VSS119	V18
H7	VSS49	VSS118	V21
H9	VSS51	VSS117	V5
H13	VSS52	VSS116	V8
H16	VSS53	VSS115	W1
H18	VSS54	VSS114	W5
H19	VSS55	VSS113	W8
J5	VSS56	VSS112	W11
J7	VSS57	VSS111	W14
J9	VSS58	VSS110	W17
J13	VSS59	VSS109	W21
J17	VSS60	VSS108	Y1
K1	VSS61	VSS107	Y2
K6	VSS62	VSS106	Y20
K7	VSS63	VSS105	Y21
K9	VSS64	VSS104	AA2
K13	VSS65	VSS103	AA3
K15	VSS66	VSS102	AA4
K21	VSS67	VSS101	AA7
L3	VSS68	VSS100	AA10
L4	VSS69	VSS99	AA12
L5	VSS70	VSS98	AA15
L6	VSS71	VSS97	AA18
L7	VSS72	VSS96	AA19
L9	VSS73	VSS95	AA20
L13	VSS74		
L15	VSS75		
L18	VSS76		
L19	VSS77		
M1	VSS78		
M5	VSS79		
M7	VSS80		
M9	VSS81		
M13	VSS82		
M21	VSS83		
N4	VSS84		

FCBGA8\_437



<http://hobi-elektronika.net>

<Variant Name>

**Title : Diamondville\_PWR**

ASUSTek Computer INC. **Engineer: Satan He**

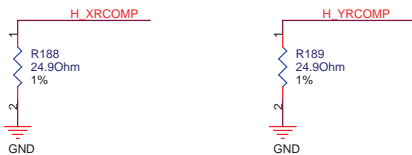
Size	Project Name	Rev
A3	1002	1.3G

Date: Friday, October 03, 2008 Sheet 6 of 50

Power:  
+VCCP

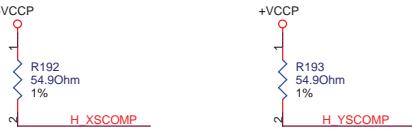
### RCOMP

For Calibrating the FSB I/O Buffer



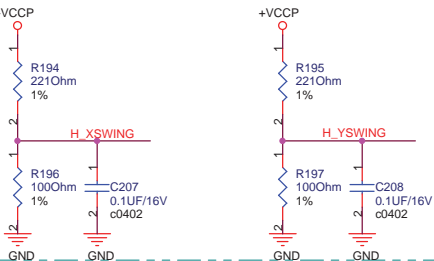
### SCOMP

For Slow Rate Compensation on the FSB

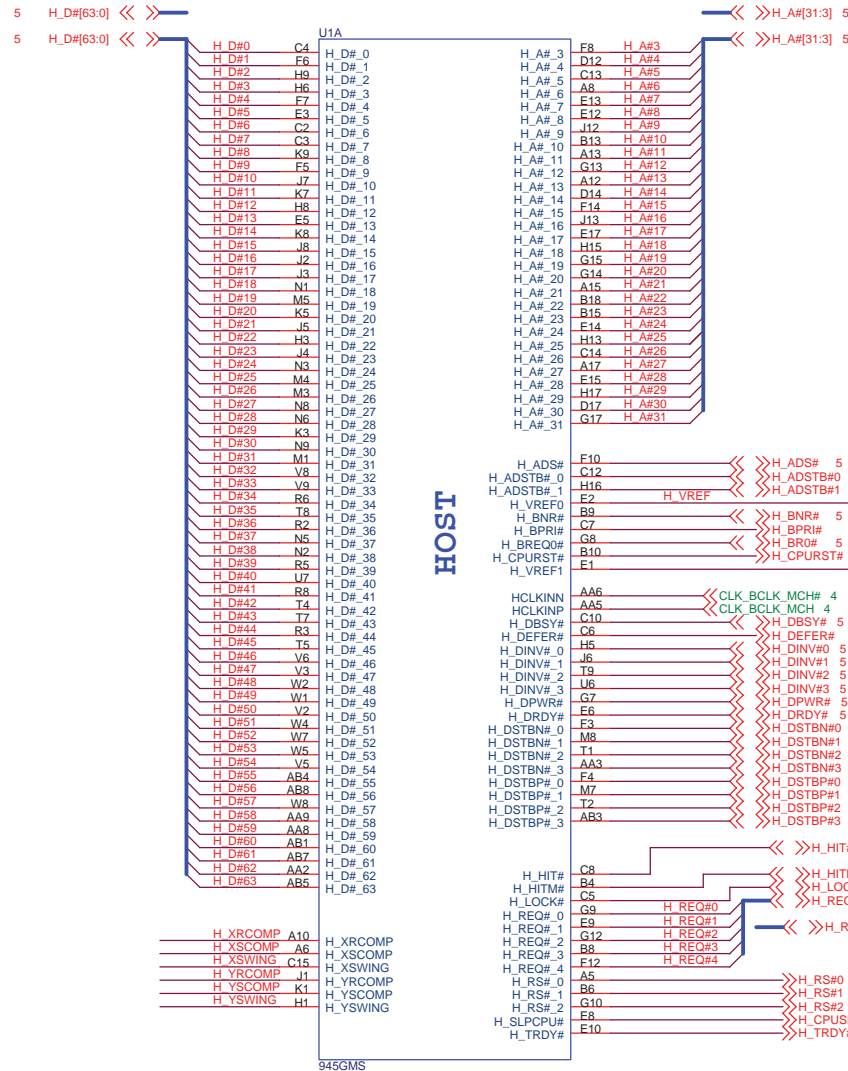


### Voltage Swing

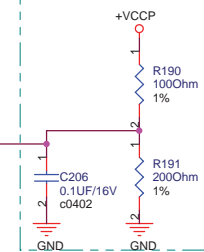
For Providing a Reference Voltage to The FSB RCOMP circuits



Signal voltage level =  
0.3125\*VCCP  
Trace should be 10 mil wide  
with 20 mil spacing



### AGTL+ I/O Voltage Reference

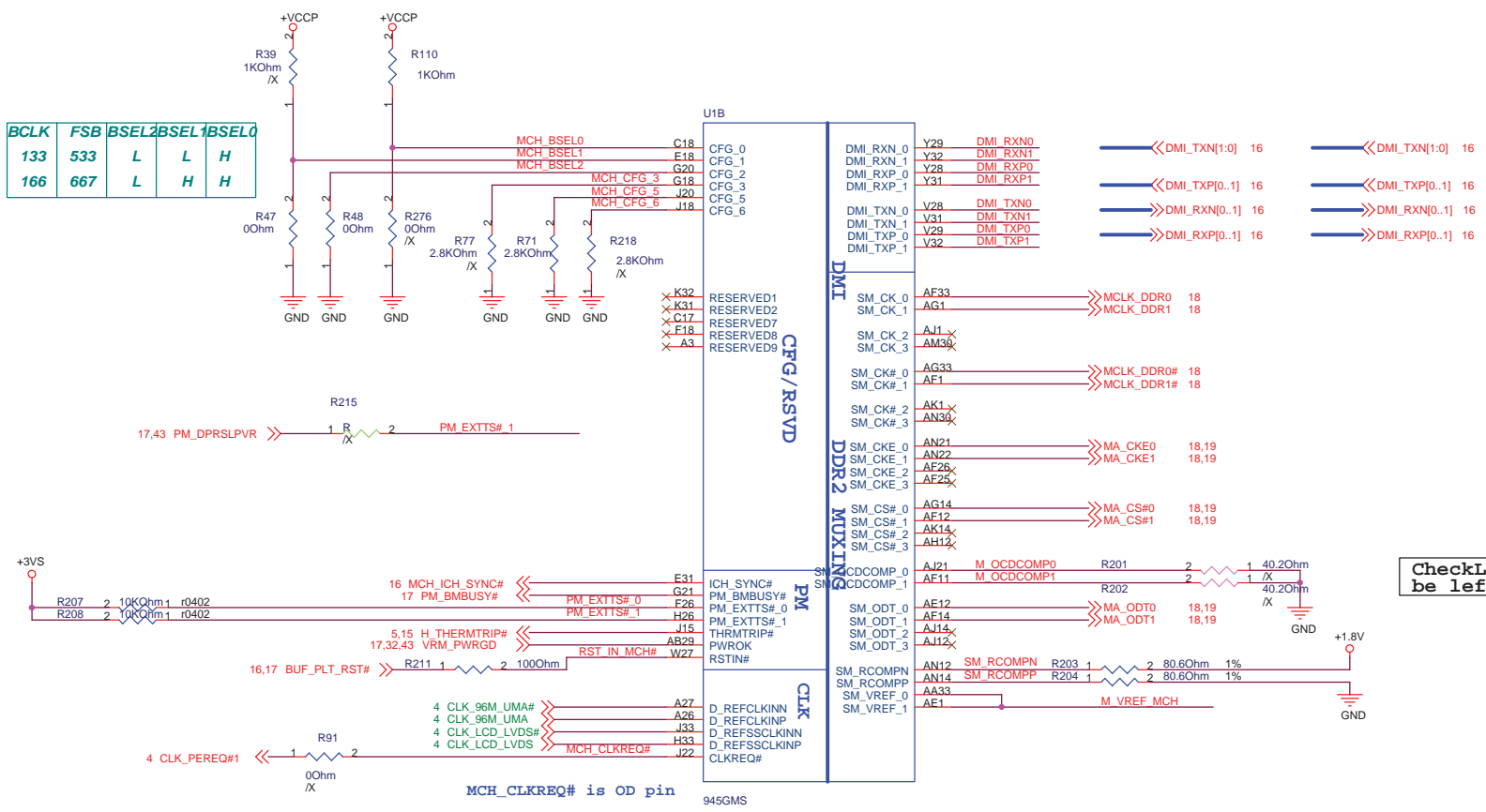


Layout Note:  
0.1uF should be placed 100mils or less from GMCH pin.

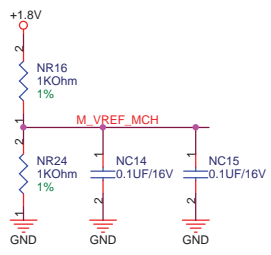
<Variant Name>

<b>ASUS</b>		<b>Title : NB-945GMS(HOST)</b>	
ASUSTek COMPUTER INC.		Engineer: <b>Satan He</b>	
Size A3	Project Name <b>1002</b>	Date Friday, October 03, 2008	Rev 1.3G
Sheet 7 of 50			

BCLK	FSB	BSEL2	BSEL1	BSEL0
133	533	L	L	H
166	667	L	H	H



CheckList notes :Can be left as NC

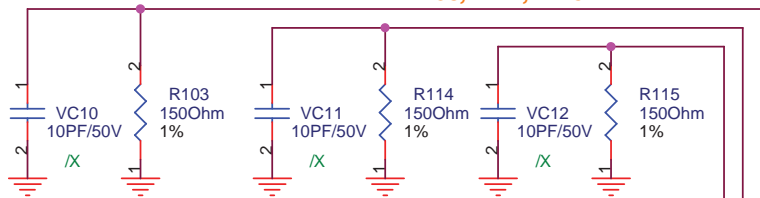


<Variant Name>

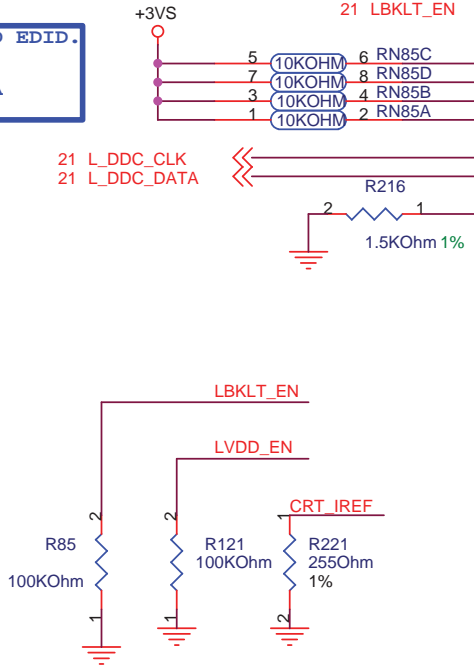
<b>ASUS</b>		<b>Title :</b> NB-945GMS(DMI & CFG)
ASUSTeK COMPUTER INC.		<b>Engineer:</b> <i>Satan He</i>
Size A3	Project Name <b>1002</b>	Rev 1.3G
Date: Friday, October 03, 2008	Sheet	8 of 50



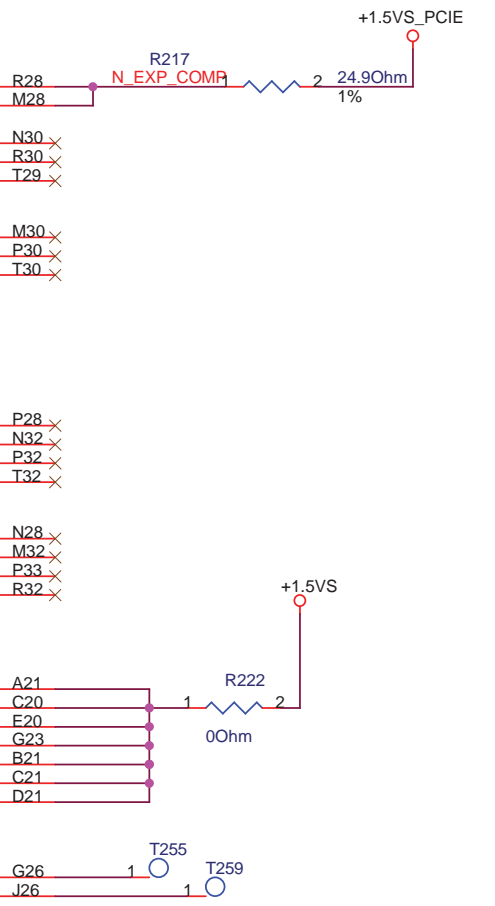
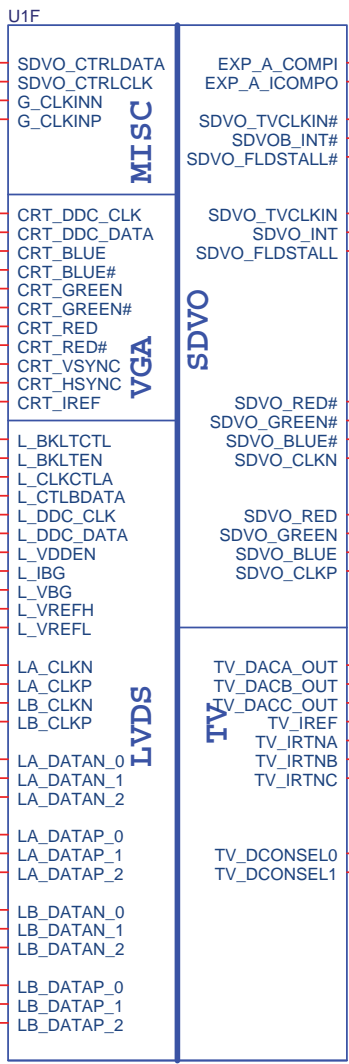
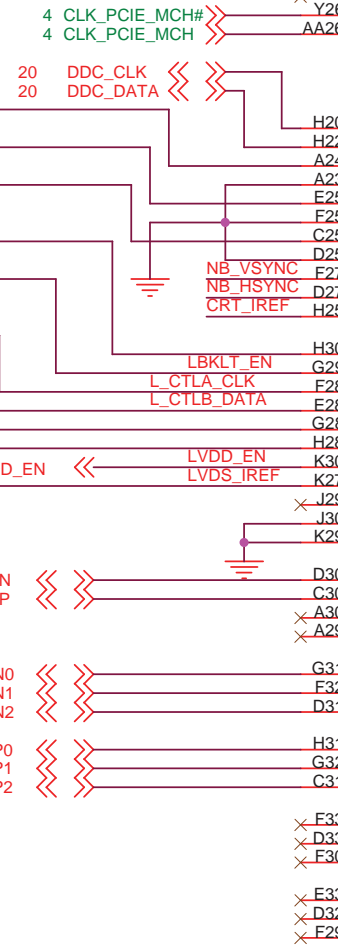
**Close to GMCH**  
R103,R114,R115



**IF USE NB READ EDID.  
MUST CONNECT  
L\_DDC\_CLK&DATA**



**Close to GMCH**



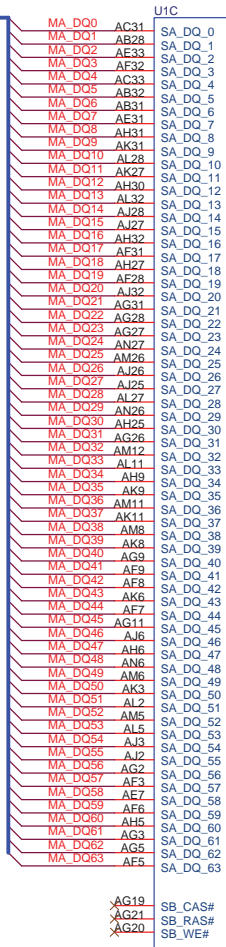
945GMS

<Variant Name>

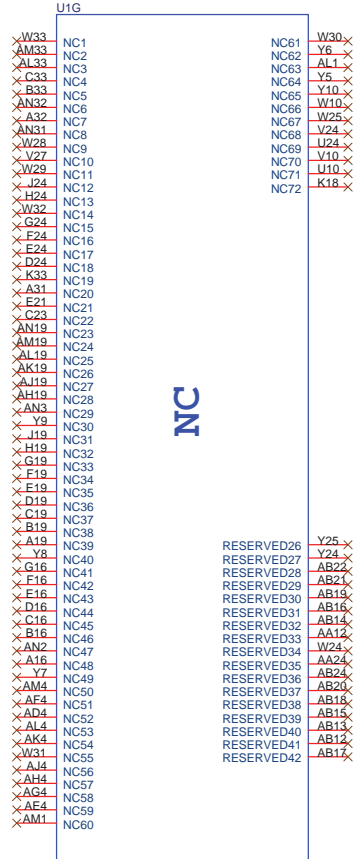
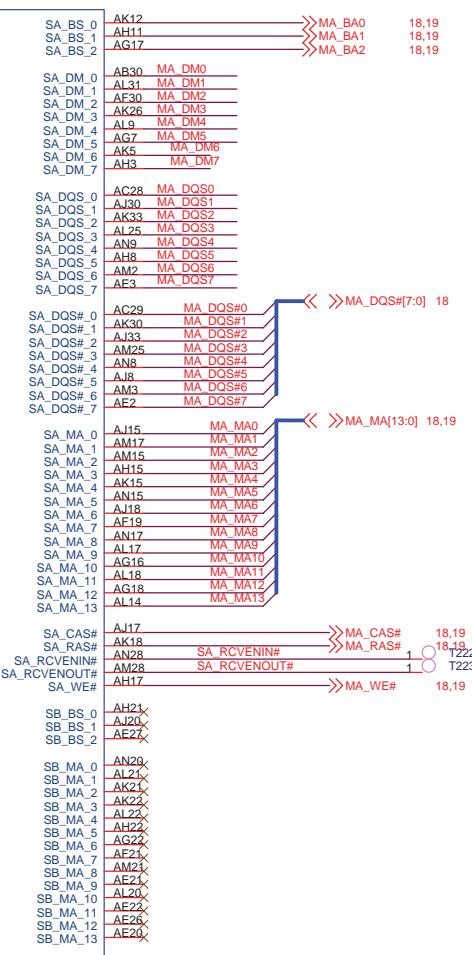
		<b>Title :</b> NB-945GMS(GRAPHIC)
ASUSTeK COMPUTER INC.		<b>Engineer:</b> Satan_He
Size A4	Project Name <b>1002</b>	Rev 1.3G
Date: Friday, October 03, 2008	Sheet 9 of 50	

18 MA\_DQ[63:0] << >>  
 18 MA\_DQ[63:0] << >>

<< >> MA\_DQS[7:0] 18  
 << >> MA\_DM[7:0] 18



DDR2 SYSTEM MEMORY

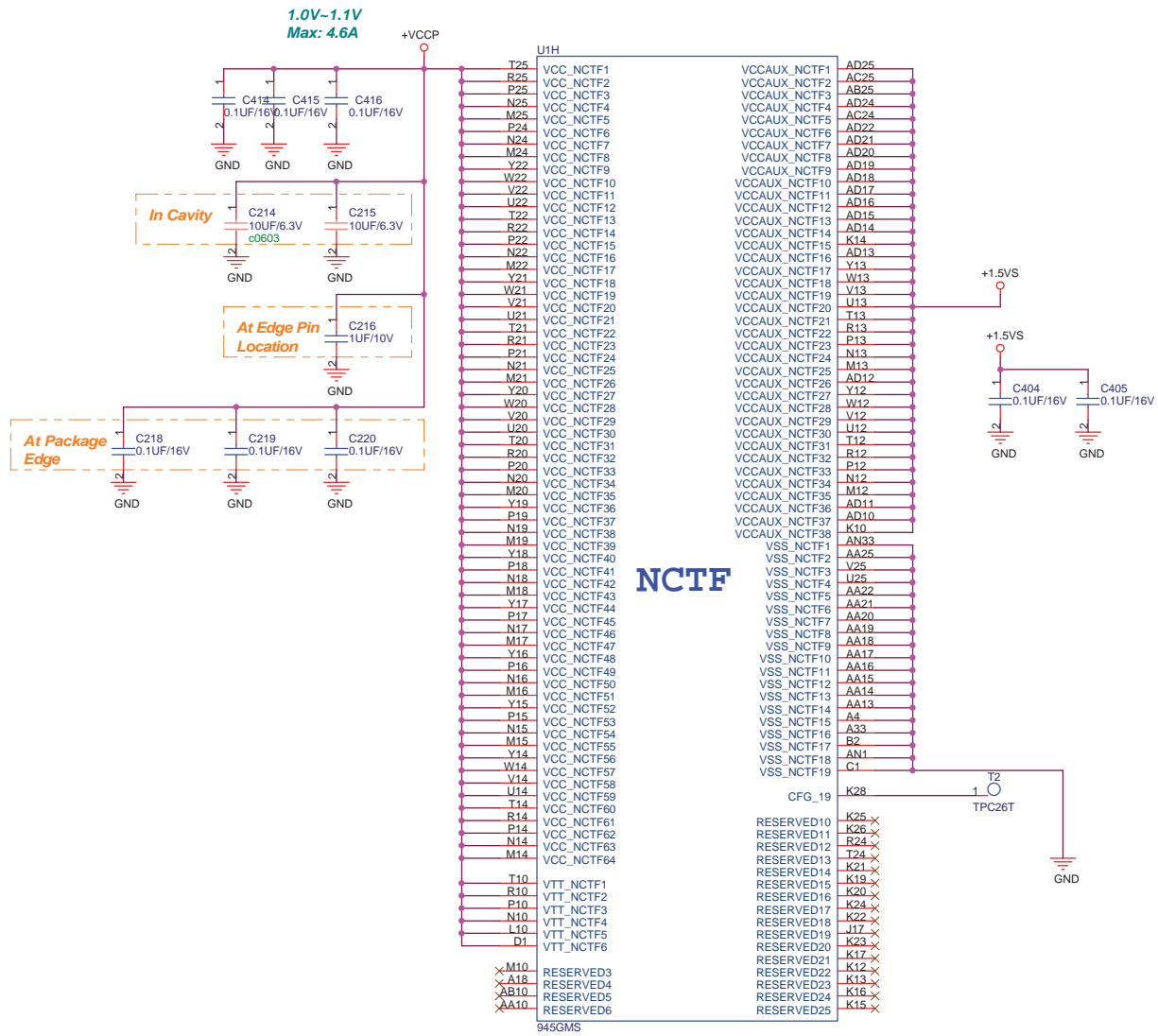


945GMS

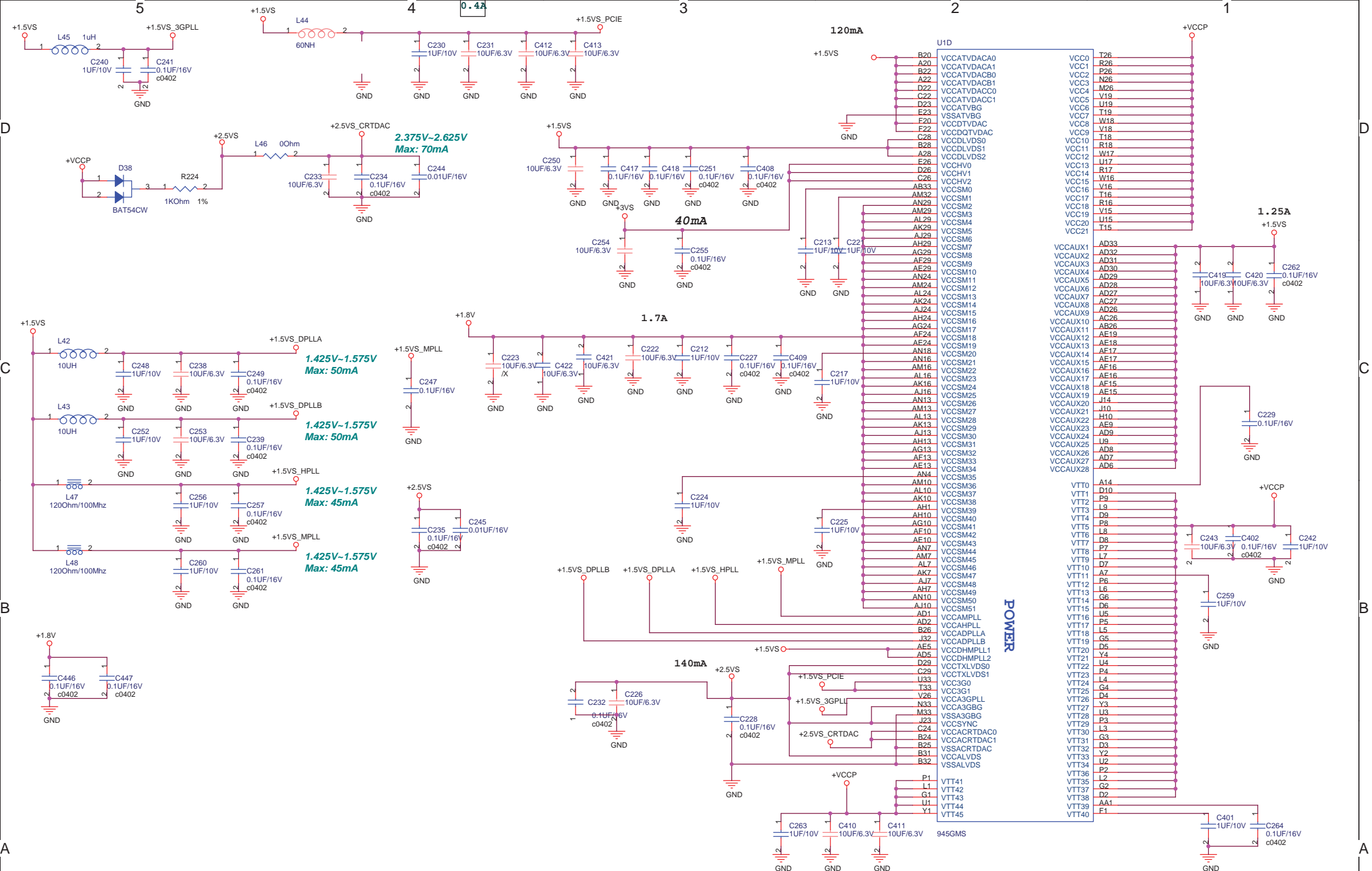
945GMS

<Variant Name>

<b>ASUS</b>		<b>Title : NB-945GMS(DDR2)</b>	
ASUSTek COMPUTER INC.		Engineer: <b>Satan He</b>	
Size A3	Project Name <b>1002</b>	Rev 1.3G	
Date: Friday, October 03, 2008		Sheet 10 of 50	



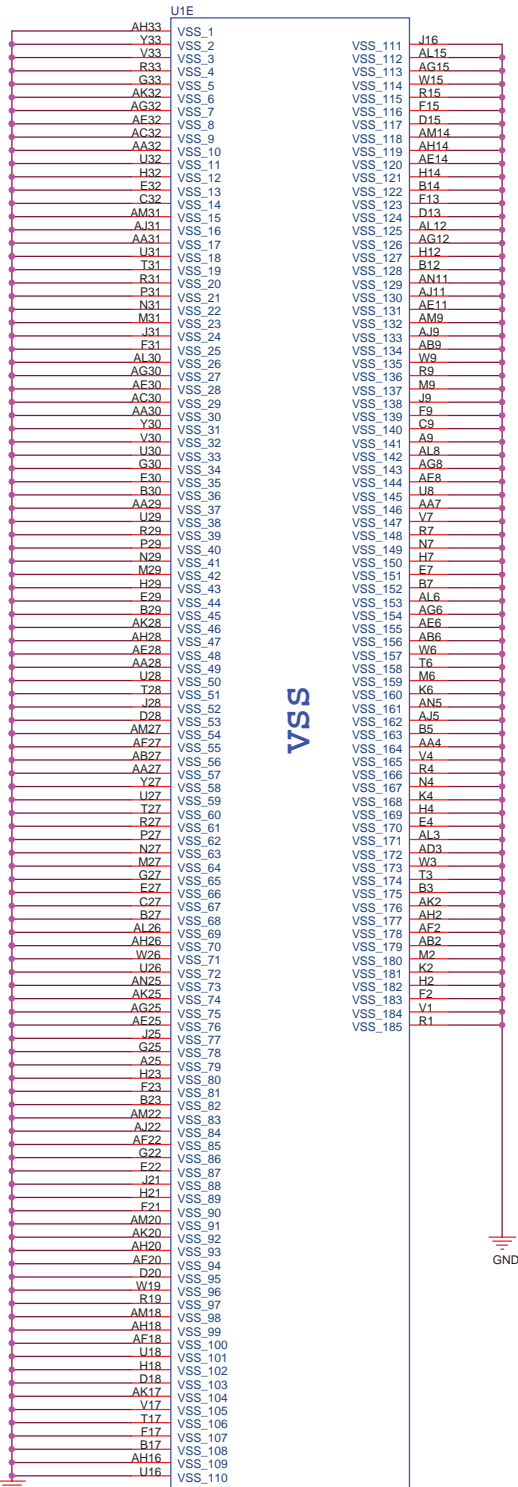
**CFG\_19(K28) Strapping :**  
**DMI LANE Reversal:**  
 0:Normal Operation (Default)  
 1.:Reversal Lanes, 3->0,2->1..etc  
 Note:945GMS doesn't support DMI Lane Reversal



<http://hobi-elektronika.net>

ASUS® Title : NB-945GMS(PWR2)  
 ASUSTeK COMPUTER INC. Engineer: *Satan He*

Size	Project Name	Rev
A3	1002	1.3G
Date: Friday, October 03, 2008	Sheet 12 of 50	



- | Component Pin | Signal Name | Component Pin | Signal Name |
|---------------|-------------|---------------|-------------|
| AH33          | VSS_1       | VSS_111       | J16         |
| Y33           | VSS_2       | VSS_112       | AL15        |
| V33           | VSS_3       | VSS_113       | AG15        |
| R33           | VSS_4       | VSS_114       | W15         |
| G33           | VSS_5       | VSS_115       | R15         |
| AK32          | VSS_6       | VSS_116       | F15         |
| AG32          | VSS_7       | VSS_117       | D15         |
| AE32          | VSS_8       | VSS_118       | AM14        |
| AC32          | VSS_9       | VSS_119       | AH14        |
| AA32          | VSS_10      | VSS_120       | AE14        |
| U32           | VSS_11      | VSS_121       | H14         |
| H32           | VSS_12      | VSS_122       | B14         |
| E32           | VSS_13      | VSS_123       | F13         |
| C32           | VSS_14      | VSS_124       | D13         |
| AM31          | VSS_15      | VSS_125       | AI12        |
| AJ31          | VSS_16      | VSS_126       | AG12        |
| AA31          | VSS_17      | VSS_127       | H12         |
| U31           | VSS_18      | VSS_128       | B12         |
| T31           | VSS_19      | VSS_129       | AM11        |
| R31           | VSS_20      | VSS_130       | AJ11        |
| P31           | VSS_21      | VSS_131       | AE11        |
| N31           | VSS_22      | VSS_132       | AM9         |
| M31           | VSS_23      | VSS_133       | AJ9         |
| J31           | VSS_24      | VSS_134       | AB9         |
| F31           | VSS_25      | VSS_135       | W9          |
| AL30          | VSS_26      | VSS_136       | R9          |
| AG30          | VSS_27      | VSS_137       | M9          |
| AE30          | VSS_28      | VSS_138       | J9          |
| AC30          | VSS_29      | VSS_139       | F9          |
| AA30          | VSS_30      | VSS_140       | CG          |
| Y30           | VSS_31      | VSS_141       | AL8         |
| V30           | VSS_32      | VSS_142       | AG8         |
| U30           | VSS_33      | VSS_143       | AE8         |
| G30           | VSS_34      | VSS_144       | U8          |
| E30           | VSS_35      | VSS_145       | AA7         |
| B30           | VSS_36      | VSS_146       | VZ7         |
| AA29          | VSS_37      | VSS_147       | R7          |
| U29           | VSS_38      | VSS_148       | N7          |
| R29           | VSS_39      | VSS_149       | H7          |
| P29           | VSS_40      | VSS_150       | E7          |
| N29           | VSS_41      | VSS_151       | B7          |
| M29           | VSS_42      | VSS_152       | AL6         |
| H29           | VSS_43      | VSS_153       | AG6         |
| E29           | VSS_44      | VSS_154       | AE6         |
| B29           | VSS_45      | VSS_155       | AB6         |
| AK28          | VSS_46      | VSS_156       | W6          |
| AH28          | VSS_47      | VSS_157       | T6          |
| AE28          | VSS_48      | VSS_158       | M6          |
| AA28          | VSS_49      | VSS_159       | K6          |
| U28           | VSS_50      | VSS_160       | AN5         |
| T28           | VSS_51      | VSS_161       | AJ5         |
| J28           | VSS_52      | VSS_162       | B5          |
| D28           | VSS_53      | VSS_163       | AA4         |
| AM27          | VSS_54      | VSS_164       | V4          |
| AE27          | VSS_55      | VSS_165       | R4          |
| AB27          | VSS_56      | VSS_166       | N4          |
| AA27          | VSS_57      | VSS_167       | K4          |
| Y27           | VSS_58      | VSS_168       | H4          |
| U27           | VSS_59      | VSS_169       | E4          |
| T27           | VSS_60      | VSS_170       | AL3         |
| R27           | VSS_61      | VSS_171       | AD3         |
| P27           | VSS_62      | VSS_172       | W3          |
| N27           | VSS_63      | VSS_173       | T3          |
| M27           | VSS_64      | VSS_174       | B3          |
| G27           | VSS_65      | VSS_175       | AK2         |
| E27           | VSS_66      | VSS_176       | AH2         |
| C27           | VSS_67      | VSS_177       | AF2         |
| B27           | VSS_68      | VSS_178       | AB2         |
| AL26          | VSS_69      | VSS_179       | M2          |
| AH26          | VSS_70      | VSS_180       | K2          |
| W26           | VSS_71      | VSS_181       | H2          |
| U26           | VSS_72      | VSS_182       | F2          |
| AN25          | VSS_73      | VSS_183       | V1          |
| AK25          | VSS_74      | VSS_184       | R1          |
| AG25          | VSS_75      | VSS_185       |             |
| AE25          | VSS_76      |               |             |
| J25           | VSS_77      |               |             |
| G25           | VSS_78      |               |             |
| A25           | VSS_79      |               |             |
| H23           | VSS_80      |               |             |
| F23           | VSS_81      |               |             |
| B23           | VSS_82      |               |             |
| AM22          | VSS_83      |               |             |
| AJ22          | VSS_84      |               |             |
| AF22          | VSS_85      |               |             |
| G22           | VSS_86      |               |             |
| E22           | VSS_87      |               |             |
| J21           | VSS_88      |               |             |
| H21           | VSS_89      |               |             |
| F21           | VSS_90      |               |             |
| AM20          | VSS_91      |               |             |
| AK20          | VSS_92      |               |             |
| AH20          | VSS_93      |               |             |
| AE20          | VSS_94      |               |             |
| D20           | VSS_95      |               |             |
| W19           | VSS_96      |               |             |
| R19           | VSS_97      |               |             |
| AM18          | VSS_98      |               |             |
| AH18          | VSS_99      |               |             |
| AF18          | VSS_100     |               |             |
| U18           | VSS_101     |               |             |
| H18           | VSS_102     |               |             |
| D18           | VSS_103     |               |             |
| AK17          | VSS_104     |               |             |
| V17           | VSS_105     |               |             |
| T17           | VSS_106     |               |             |
| F17           | VSS_107     |               |             |
| B17           | VSS_108     |               |             |
| AH16          | VSS_109     |               |             |
| U16           | VSS_110     |               |             |

VSS



GND

945GMS

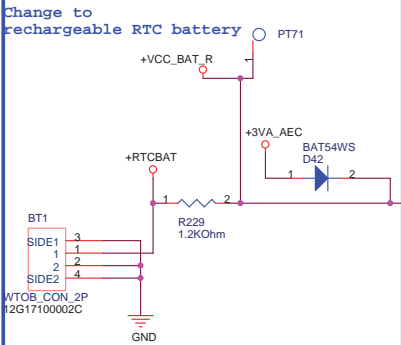
<http://hobi-elektronika.net>

<Variant Name>

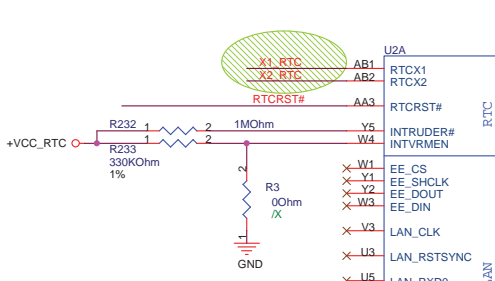
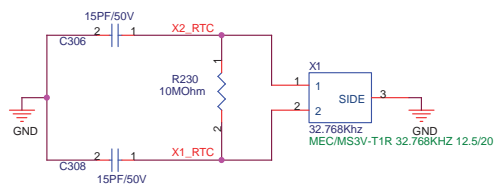
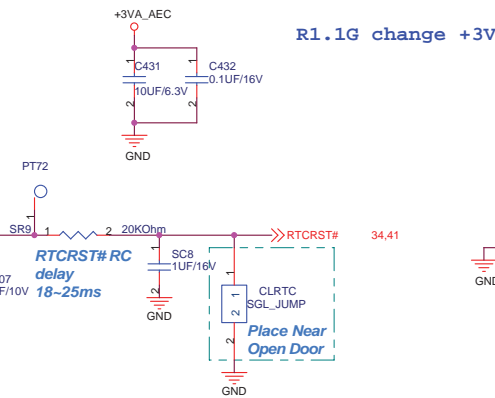
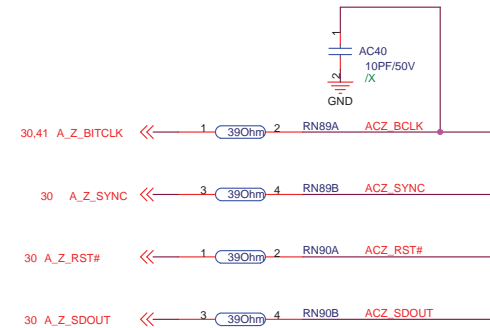
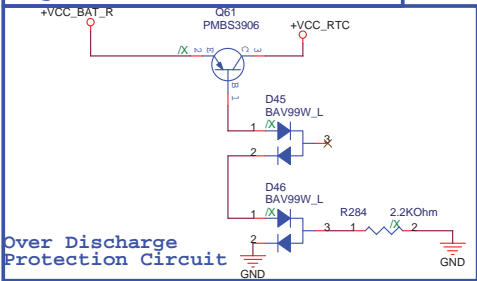
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ASUSTeK COMPUTER INC.		<b>Engineer:</b> Satan_He	
Size	Project Name	Rev	
A3	1002		1.3G
Date: Friday, October 03, 2008		Sheet	13 of 50



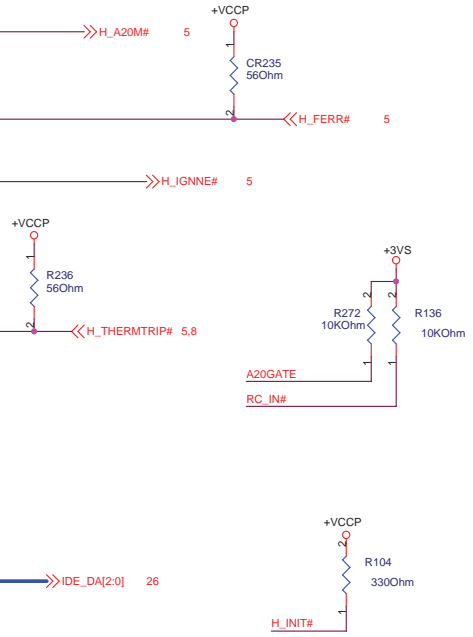
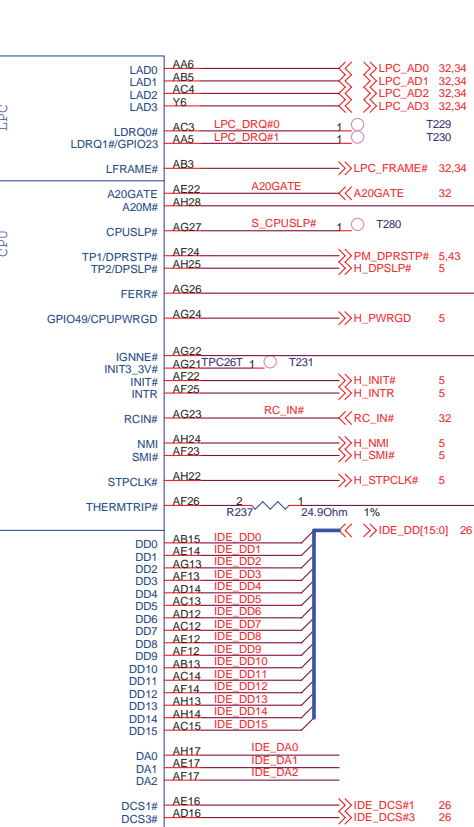
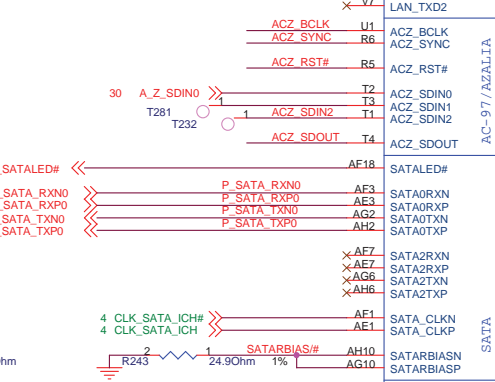
R1.1G change +3VA net to +3VA\_AEC

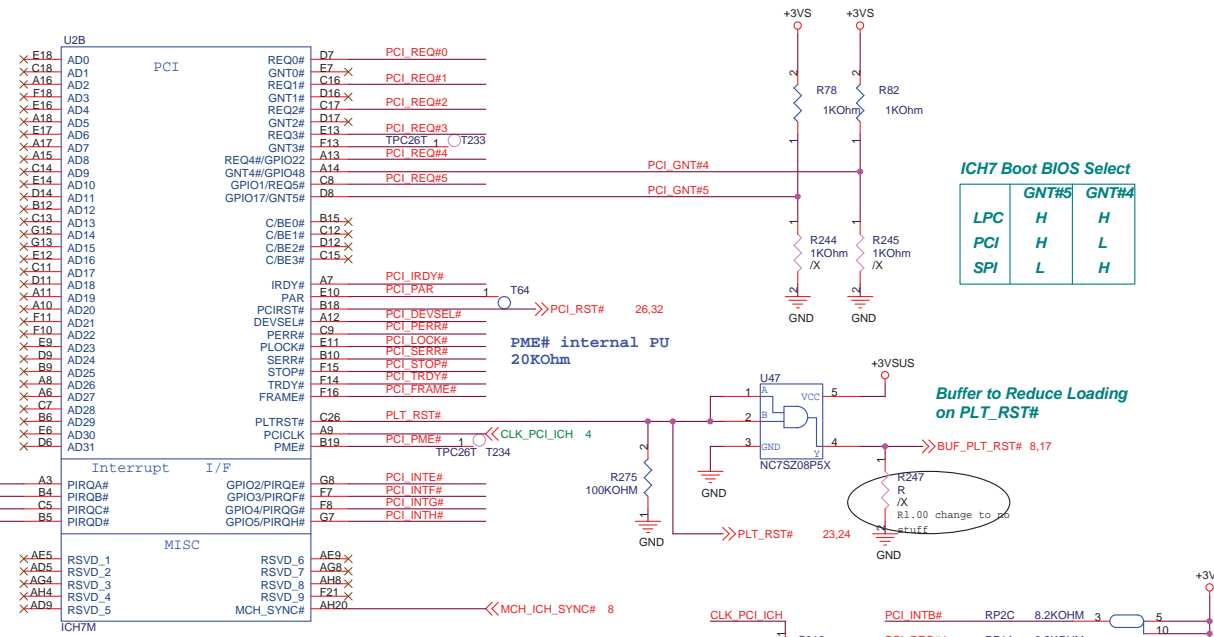


Height : 3.4 mm



ACZ_SDIN0	CODEC
ACZ_SDIN1	NA

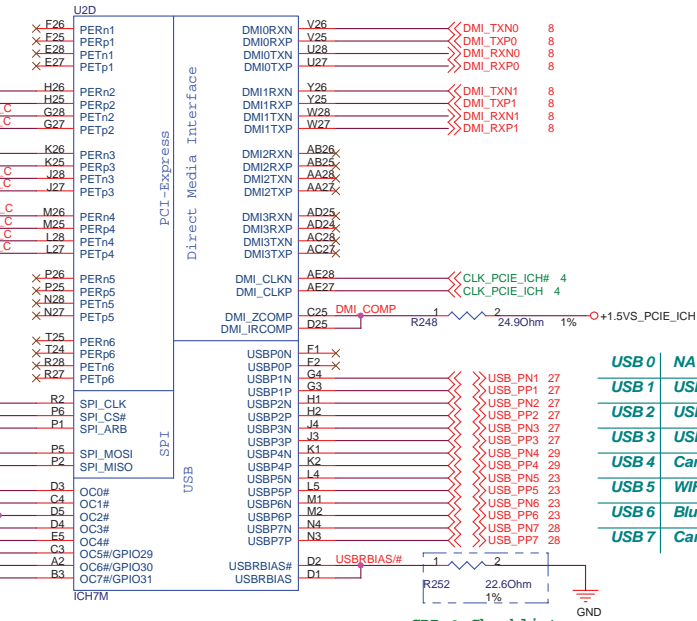
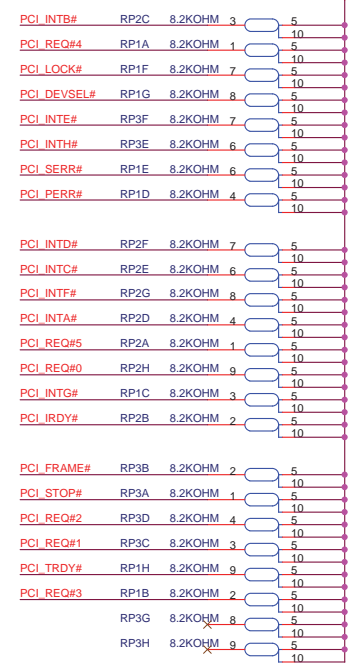




**ICH7 Boot BIOS Select**

	GNT#5	GNT#4
LPC	H	H
PCI	H	L
SPI	L	H

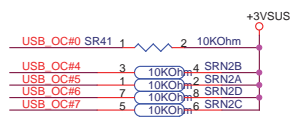
Buffer to Reduce Loading on PLT\_RST#



USB 0	NA
USB 1	USB Conn
USB 2	USB Conn
USB 3	USB Conn
USB 4	Card Reader
USB 5	WIFI
USB 6	Bluetooth
USB 7	Camera

CRB & Checklist

LAN AR8113 IC  
WIFI PCIExpress Card  
PCIe Interface SSD



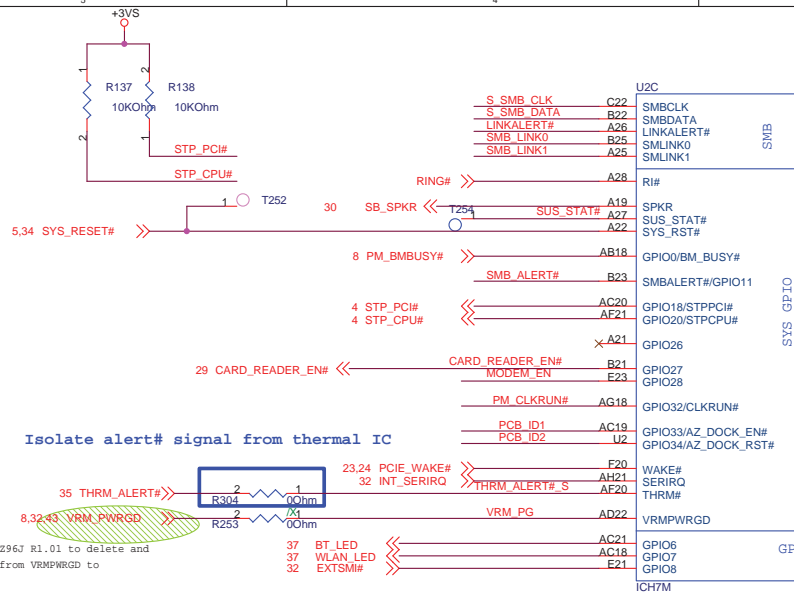
<Variant Name>

**ASUS** Title : SB-ICH7M(2)  
ASUSTeK COMPUTER INC. Engineer: Satan He

Size	Project Name	Rev
Custom	1002	1.3G

Date: Friday, October 03, 2008 Sheet 16 of 50





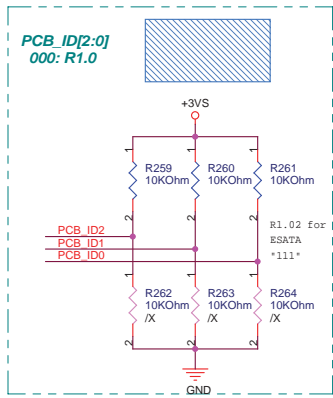
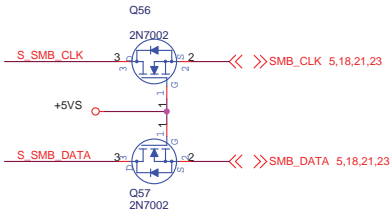
Isolate alert# signal from thermal IC



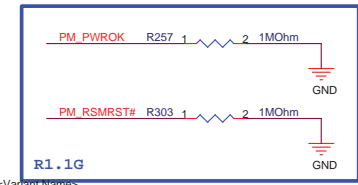
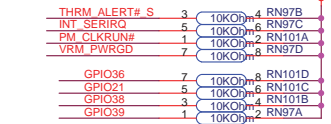
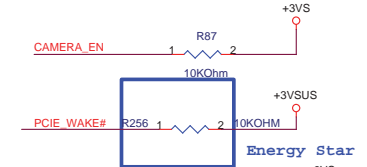
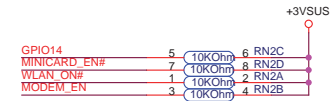
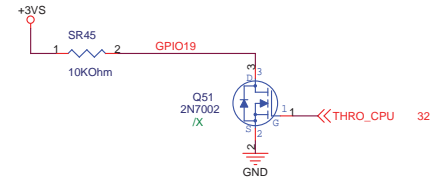
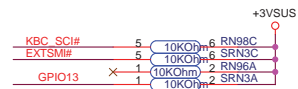
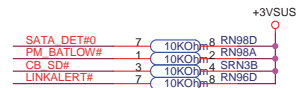
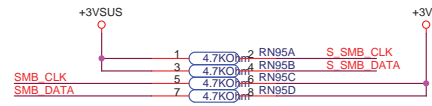
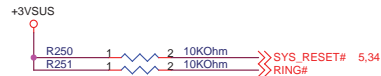
05/12/30, refer Z967 R1.01 to delete and change net name from VRMPWRGD to VRM\_PWRGD.

**GPIO25 Internal PU 20K For +1.5V DIMM Power**

	WLAN_LED	WLAN	BT
High	v	v	v
High	v	v	x
High	x	x	v
Low	x	x	x

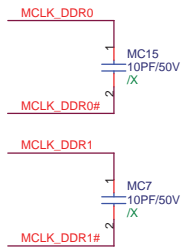


PCB\_VID3 : PROJECT CODE



<Variant Names

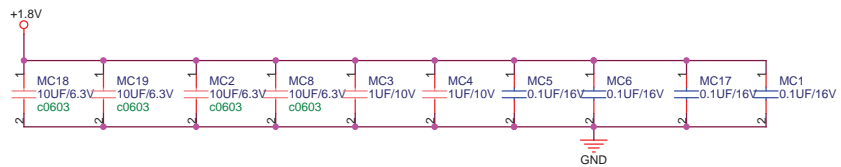
<b>ASUS</b>		Title : SB-ICH7M(3)	
ASUSTek COMPUTER INC		Engineer: Satin He	
Size	Project Name	Rev	
Custom	1002	1.3G	
Date: Friday, October 03, 2008		Sheet 17 of 50	



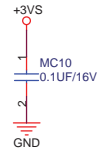
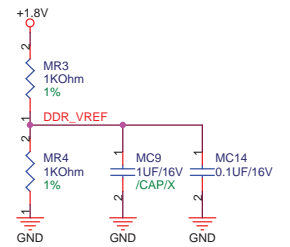
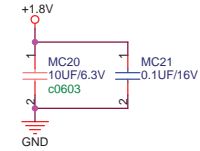
STD Type

- MA\_DQ[63:0] 10
- MA\_DQS[7:0] 10
- MA\_DQS# [7:0] 10
- MA\_DM[7:0] 10
- MA\_MA[13:0] 10,19
- MA\_BA[2:0] 10,19

DDR2 Conn. Height=4.0mm



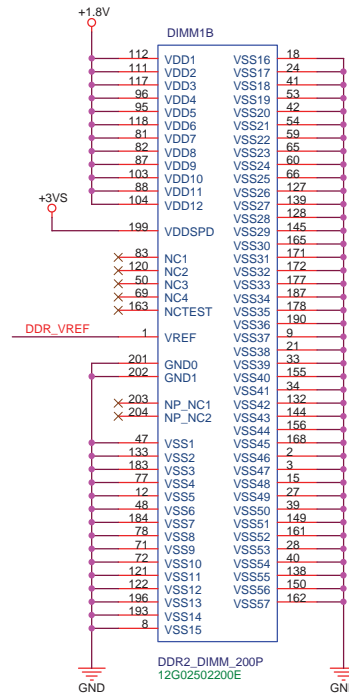
R1.1G MC3 MC4 change to 0603 1uF



DIMM1A		DIMM1B	
MA_MA0	102	A0	DQ0
MA_MA1	101	A1	DQ1
MA_MA2	100	A2	DQ2
MA_MA3	99	A3	DQ3
MA_MA4	98	A4	DQ4
MA_MA5	97	A5	DQ5
MA_MA6	94	A6	DQ6
MA_MA7	92	A7	DQ7
MA_MA8	93	A8	DQ8
MA_MA9	91	A9	DQ9
MA_MA10	105	A10/AP	DQ10
MA_MA11	90	A11	DQ11
MA_MA12	89	A12	DQ12
MA_MA13	116	A13	DQ13
	86	A14	DQ14
	84	A15	DQ15
	85	A16_BA2	DQ16
MA_BA2	85	BA0	DQ17
MA_BA0	107	BA1	DQ18
MA_BA1	106	BA2	DQ19
	110	S0#	DQ20
	115	S1#	DQ21
	30	CK0	DQ22
	32	CK0#	DQ23
	164	CK1	DQ24
	166	CK1#	DQ25
	79	CKE0	DQ26
	80	CKE1	DQ27
	113	CAS#	DQ28
	108	RAS#	DQ29
	109	WE#	DQ30
	198	SA0	DQ31
	200	SA1	DQ32
	197	SCL	DQ33
	195	SDA	DQ34
	114	ODT0	DQ35
	119	ODT1	DQ36
	10	DM0	DQ37
	26	DM1	DQ38
	52	DM2	DQ39
	67	DM3	DQ40
	130	DM4	DQ41
	147	DM5	DQ42
	170	DM6	DQ43
	185	DM7	DQ44
	13	DQS0	DQ45
	31	DQS1	DQ46
	51	DQS2	DQ47
	70	DQS3	DQ48
	131	DQS4	DQ49
	148	DQS5	DQ50
	169	DQS6	DQ51
	188	DQS7	DQ52
	11	DQS#0	DQ53
	29	DQS#1	DQ54
	49	DQS#2	DQ55
	68	DQS#3	DQ56
	129	DQS#4	DQ57
	146	DQS#5	DQ58
	167	DQS#6	DQ59
	186	DQS#7	DQ60
		DQ61	DQ61
		DQ62	DQ62
		DQ63	DQ63

DDR2\_DIMM\_200P  
12G02502200E



GROUP1  
GROUP2  
SWAP

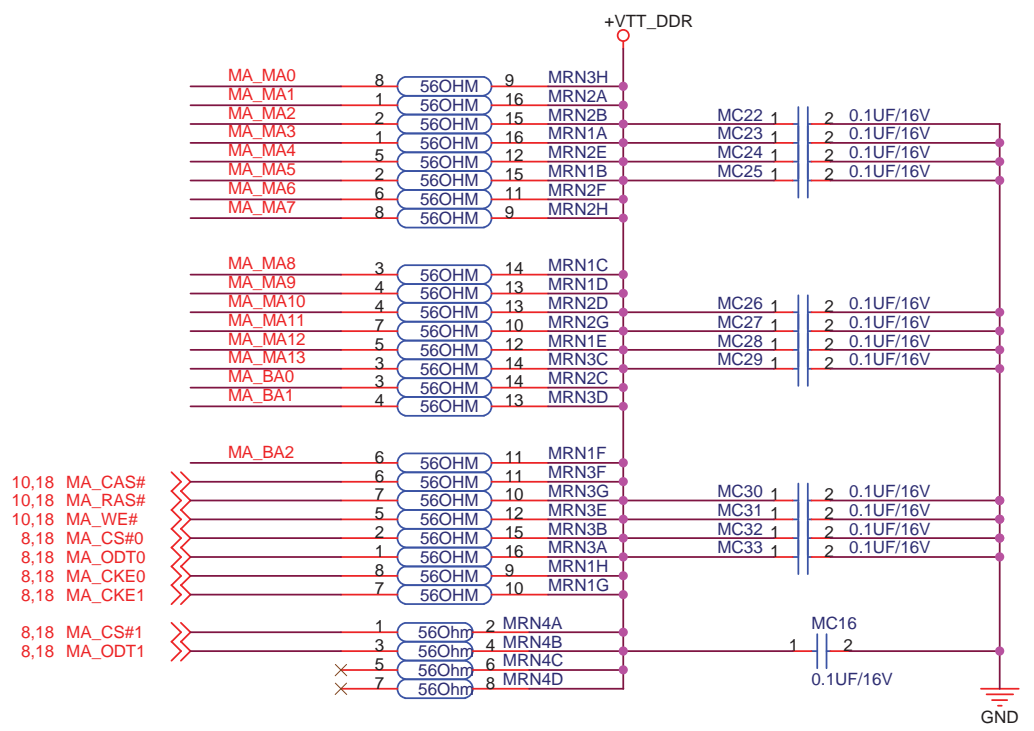


DDR2\_DIMM\_200P  
12G02502200E


<Variant Name>

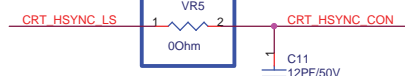
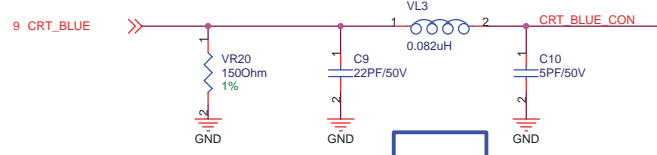
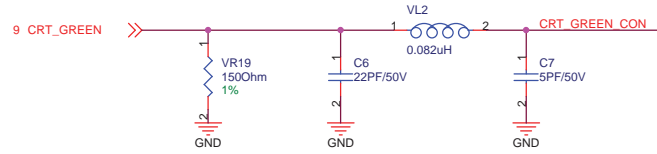
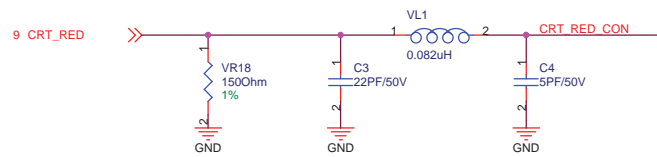
<b>ASUS</b>		<b>Title : DDR2 SODIMM</b>	
ASUSTek Computer INC.		Engineer: <i>Kel_Huang</i>	
Size	Project Name	Rev	
A3	1002	1.3G	
Date: Friday, October 03, 2008	Sheet	18	of 50

 << MA\_MA[13:0] 10,18  
 << MA\_BA[2:0] 10,18



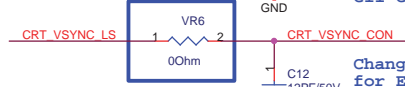
<Variant Name>

		<b>Title :</b> DDR2_Termination	
ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size	Project Name	Rev	
A4	<b>1002</b>	1.3G	
Date: Friday, October 03, 2008	Sheet	19	of 50

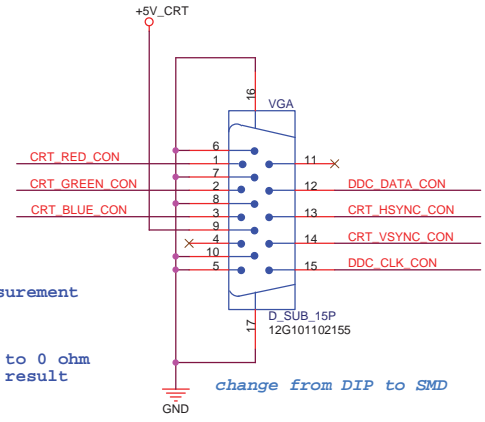
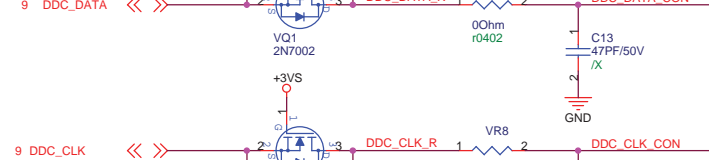
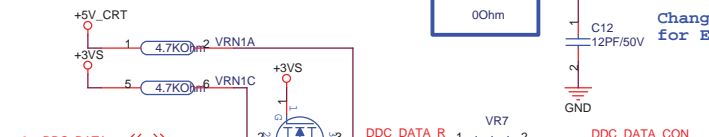


U25上:VR5 & VR6-->22 OHM  
 U25 /X :VR5 & VR6 -->0 OHM

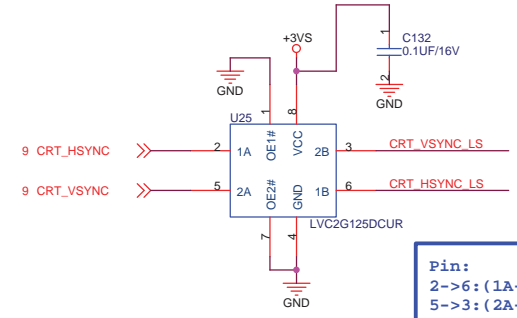
C11 C12 for EA measurement



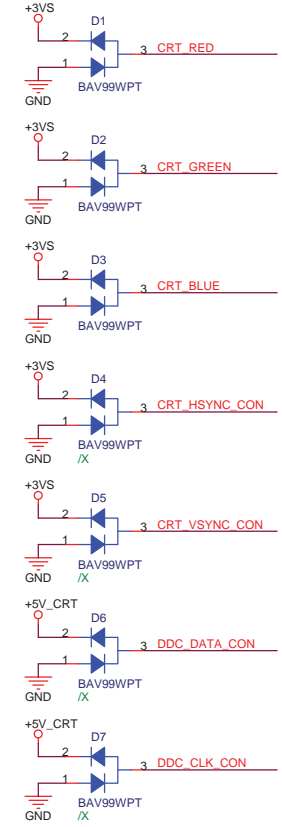
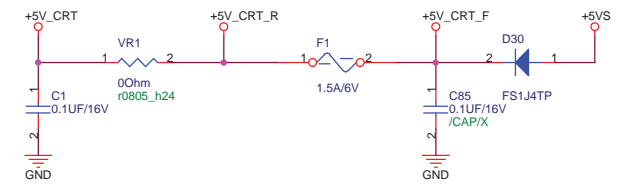
Change VR5 and VR6 to 0 ohm for EA measurement result

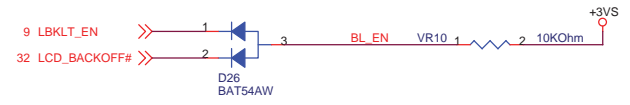
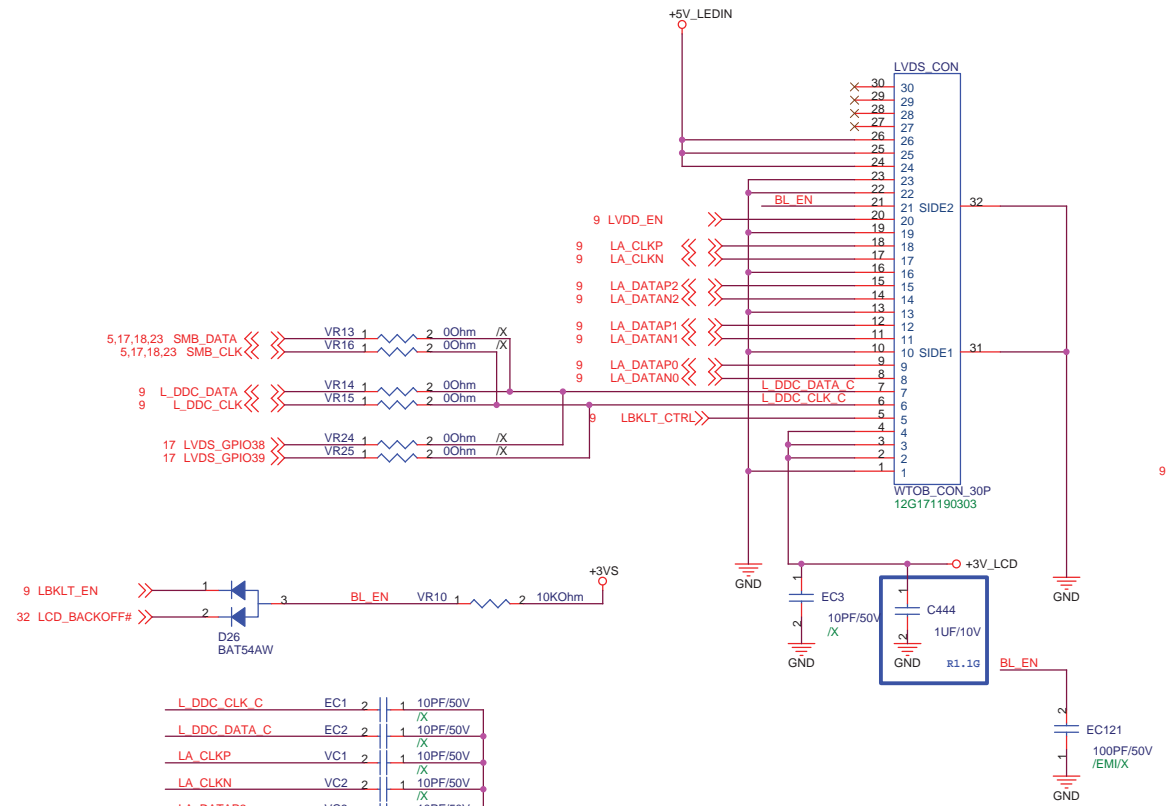


VGA use 12G10110015W  
 VGA use 12G101102155, but use 12G10110015W footprint



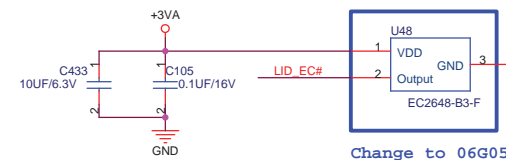
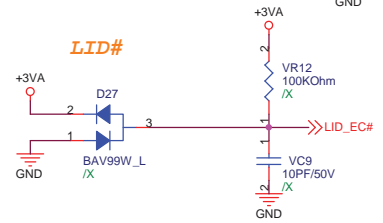
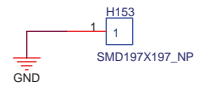
Pin:  
 2->6: (1A->1B)  
 5->3: (2A->2B)



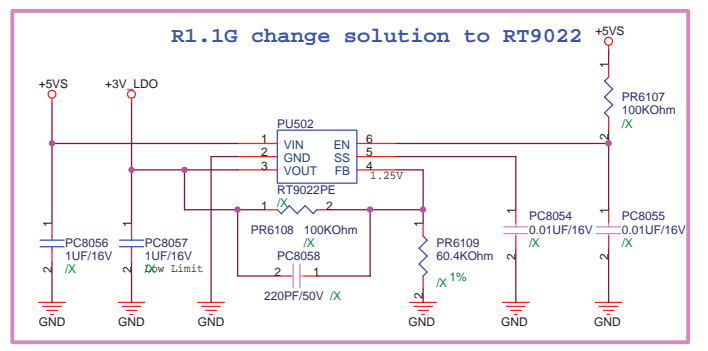
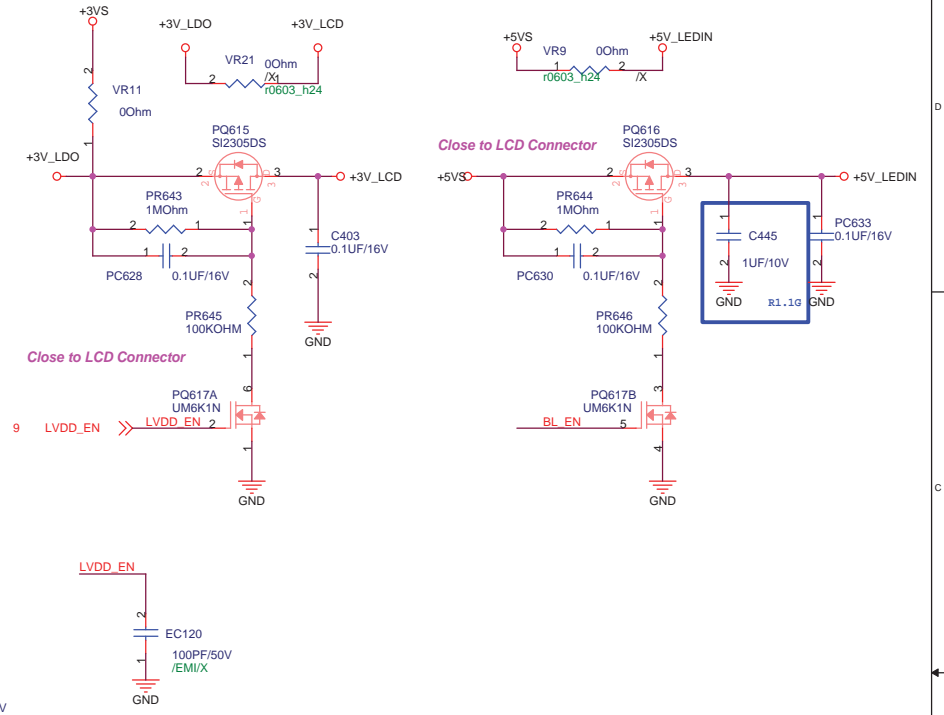
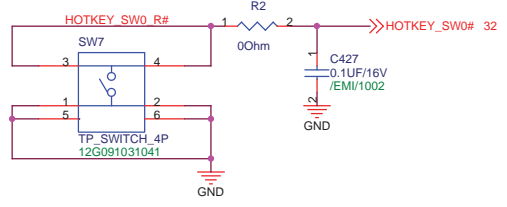


L_DDC_CLK C	EC1	2	1	10PF/50V
L_DDC_DATA C	EC2	2	1	10PF/50V
LA_CLKP	VC1	2	1	10PF/50V
LA_CLKN	VC2	2	1	10PF/50V
LA_DATAP2	VC3	2	1	10PF/50V
LA_DATAN2	VC4	2	1	10PF/50V
LA_DATAP1	VC5	2	1	10PF/50V
LA_DATAN1	VC6	2	1	10PF/50V
LA_DATAP0	VC7	2	1	10PF/50V
LA_DATAN0	VC8	2	1	10PF/50V

H153 : Pad for EMI




Change to 06G051007011 for cost issue

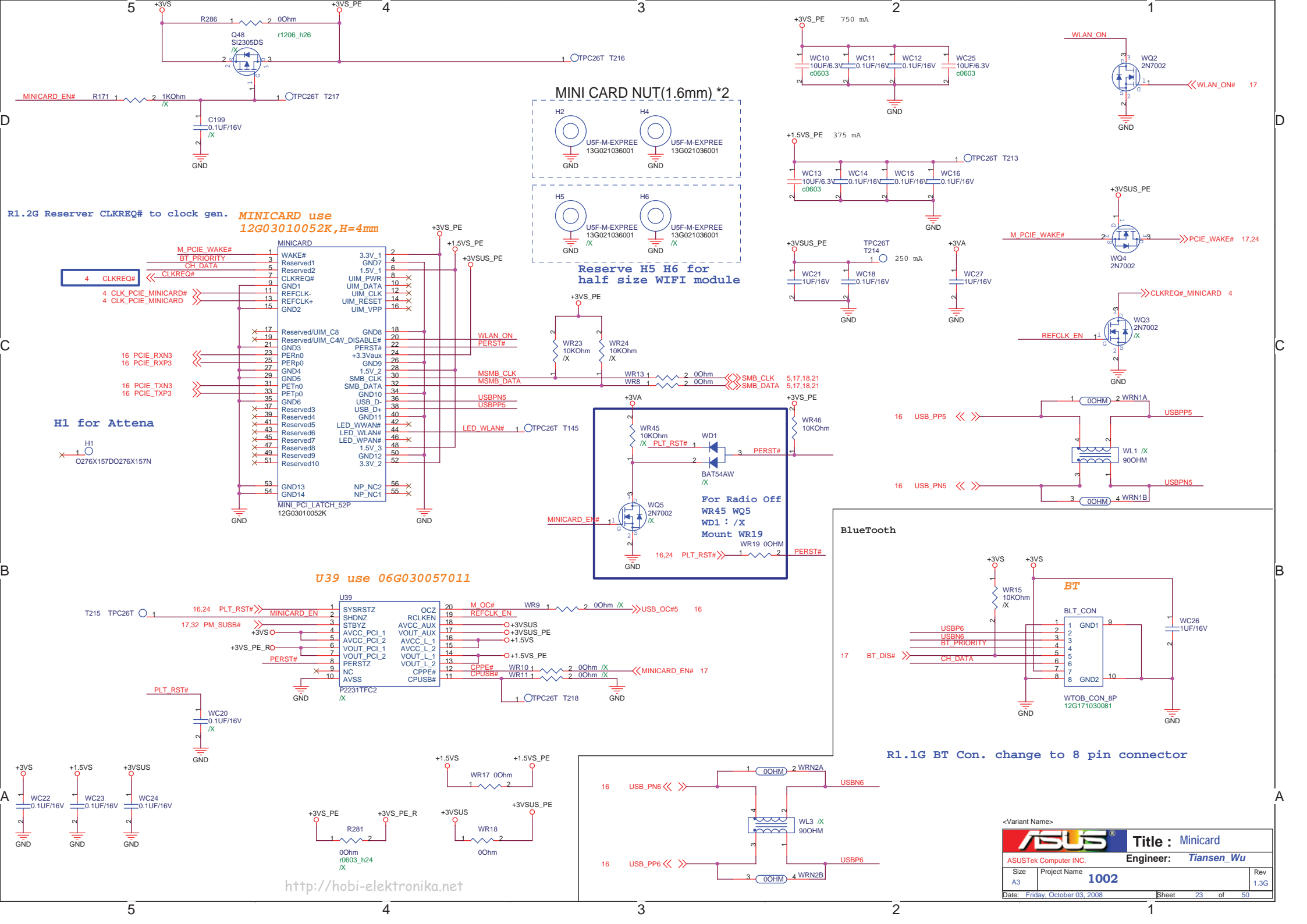


<Variant Name>

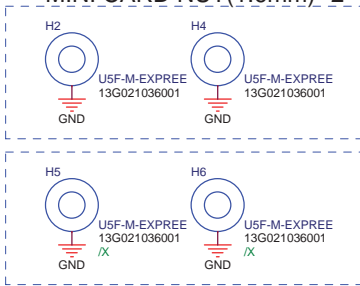
<b>ASUS</b>		<b>Title : LVDS Conn_LID</b>	
ASUSTek Computer INC.		Engineer: <b>Kel_Huang</b>	
Size	Project Name		Rev
A3	1002		1.3G
Date: Friday, October 03, 2008		Sheet	21 of 50

<Variant Name>

		<b>Title :</b> Blank	
ASUSTek Computer INC.		<b>Engineer:</b> <i>Kell_Huang</i>	
Size	Project Name		Rev
A3	<b>1002</b>		1.3G
Date: Friday, October 03, 2008		Sheet	22 of 50

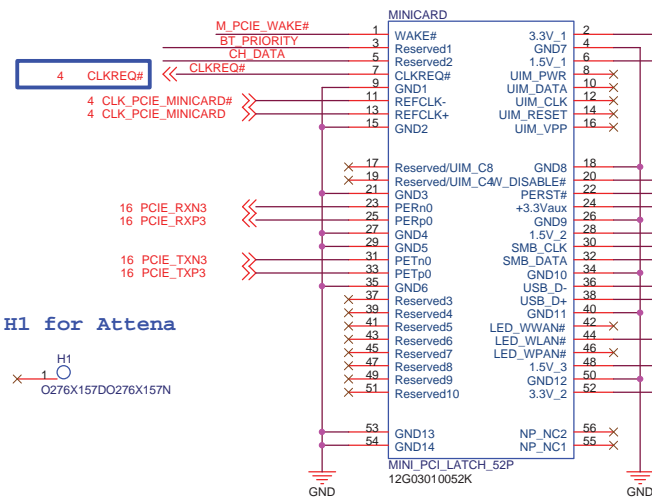


**MINI CARD NUT(1.6mm) \*2**

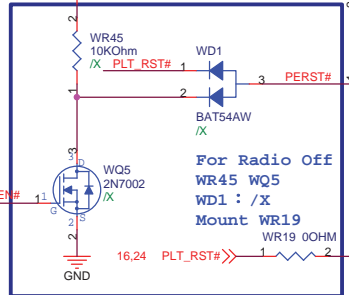
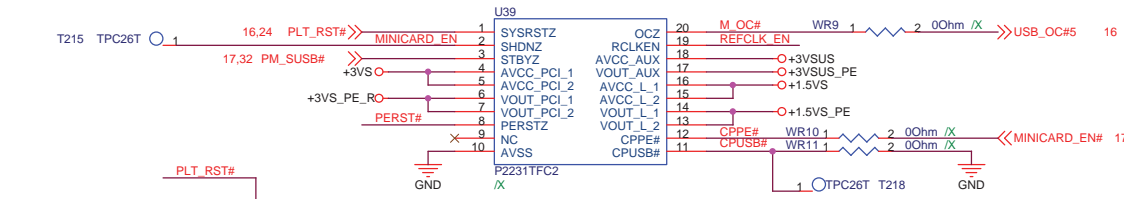


Reserve H5 H6 for half size WIFI module

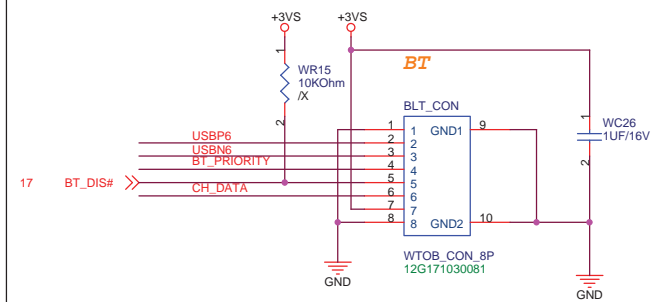
R1.2G Reserver CLKREQ# to clock gen. **MINICARD use 12G03010052K, H=4mm**



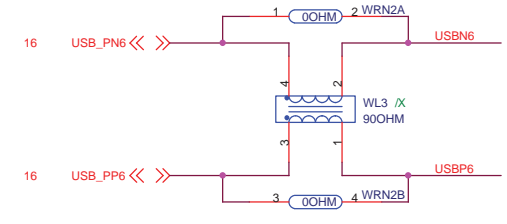
**U39 use 06G030057011**



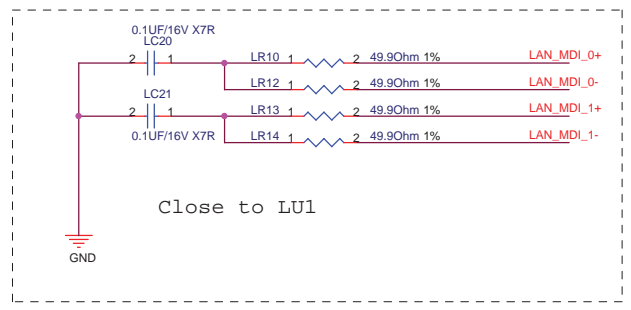
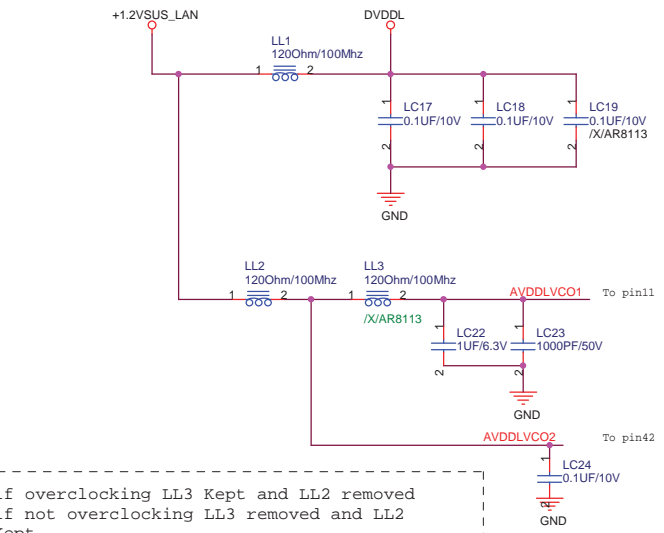
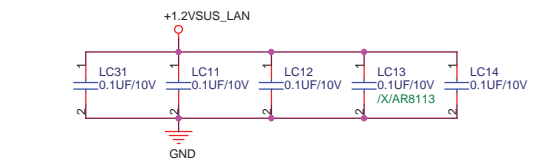
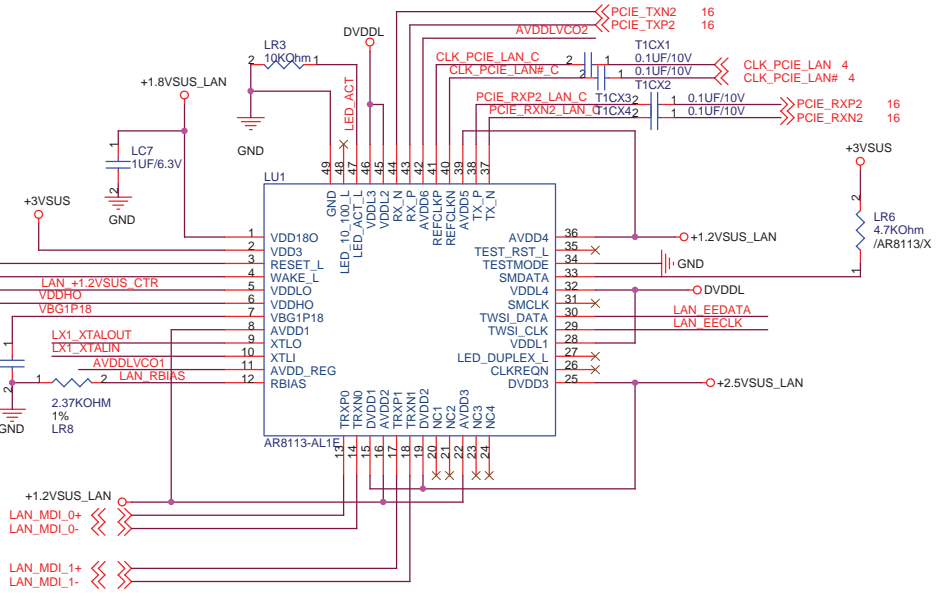
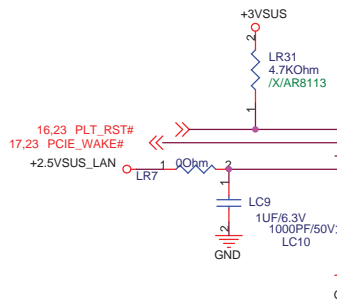
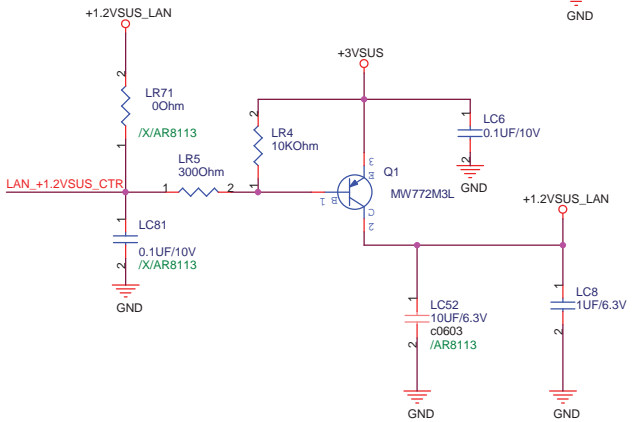
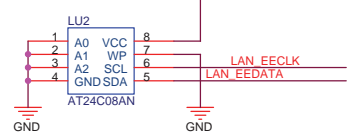
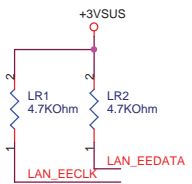
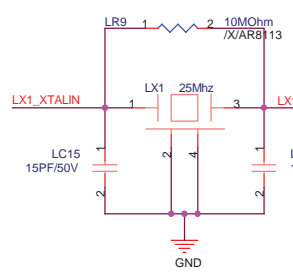
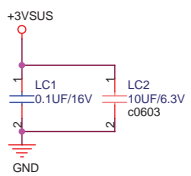
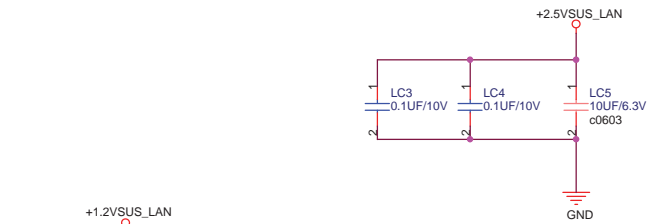
**BlueTooth**



R1.1G BT Con. change to 8 pin connector



<b>ASUS</b>		<b>Title : Minicard</b>	
ASUSTek Computer INC.		Engineer: <b>Tiansen_Wu</b>	
Size A3	Project Name <b>1002</b>	Rev 1.3G	
Date: Friday, October 03, 2008	Sheet		23 of 50

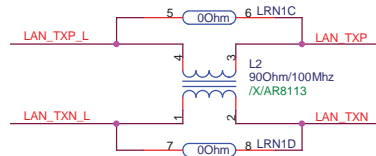
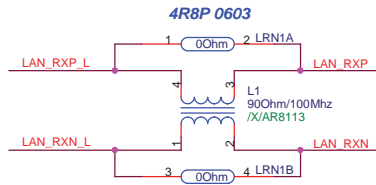


Close to LU1

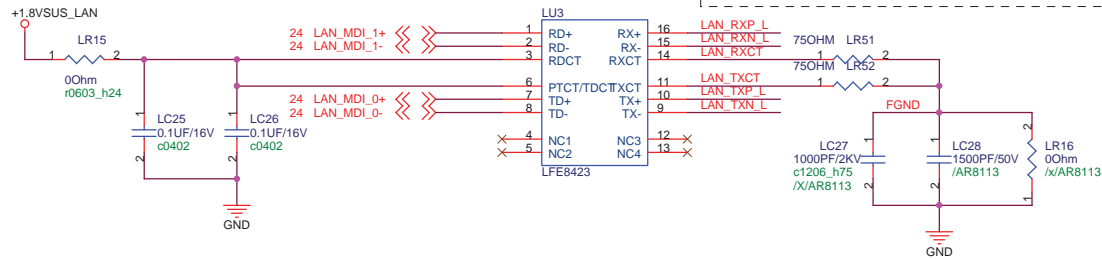
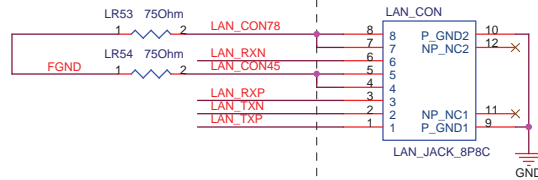
if overclocking LL3 Kept and LL2 removed  
if not overclocking LL3 removed and LL2 Kept

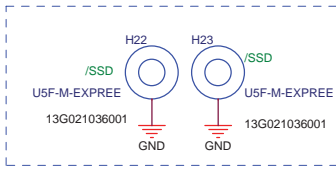
ASUS		Title : AR8113	
ASUSTek Computer INC		Engineer: Jenen_wang	
Size	Project Name	Rev	1.3G
A3	1002		
Date: Friday, October 03, 2008	Sheet	24	of 50



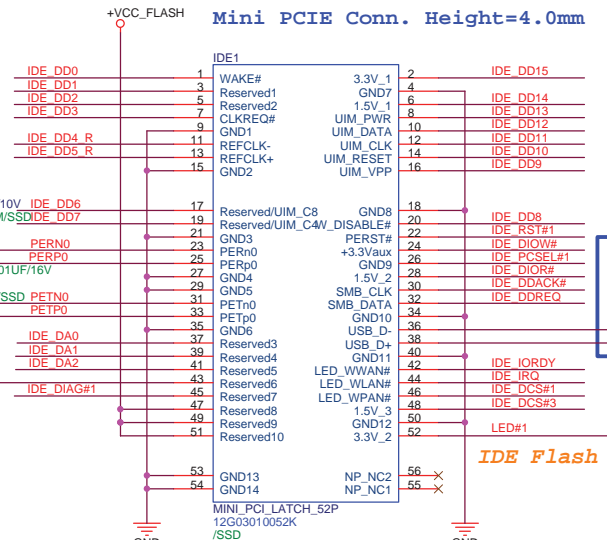
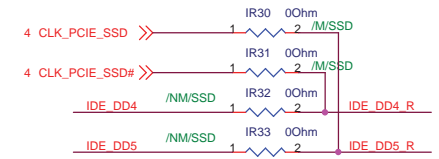
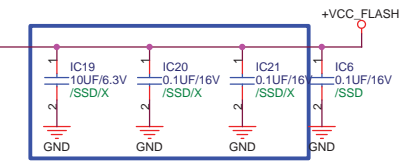


LAN connector: 12G148101086  
SMT type

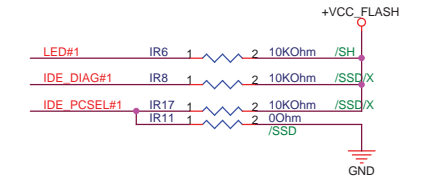
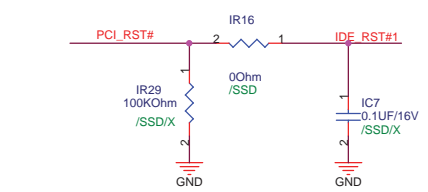




R1.2G Change from 0603 to 0805

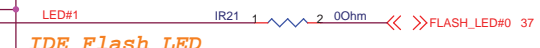
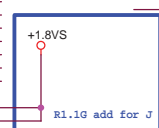


- << IDE\_DD[15:0] 15
- << IDE\_DA[2:0] 15
- << IDE\_DDACK# 15
- << IDE\_DDREQ 15
- << IDE\_DIOR# 15
- << IDE\_DIOW# 15
- << IDE\_IORDY 15
- << IDE\_DCS#1 15
- << IDE\_DCS#3 15
- << IDE\_IRQ 15
- << PCI\_RST# 16,32



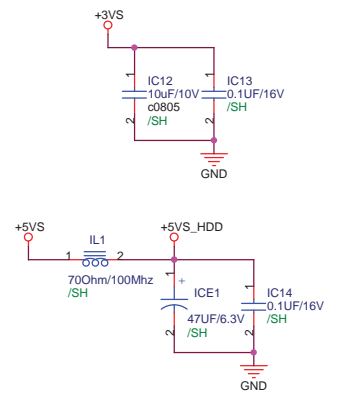
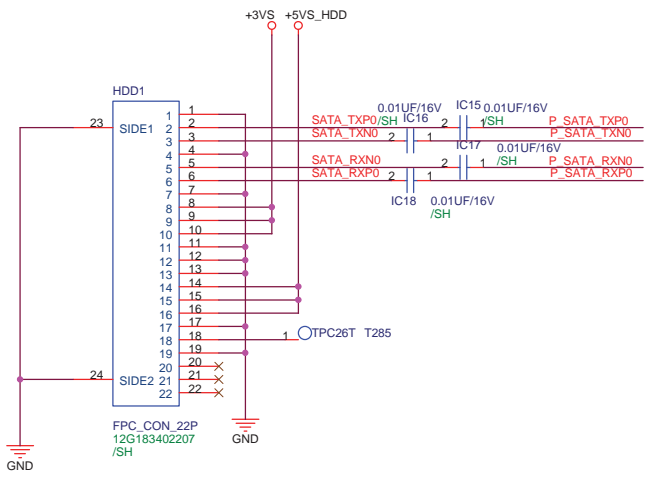
HD Master/Slave:  
Master:Low  
Slave :NC or High

SATA Interface for J  
PCIe Interface for M



## SATA HDD Connector

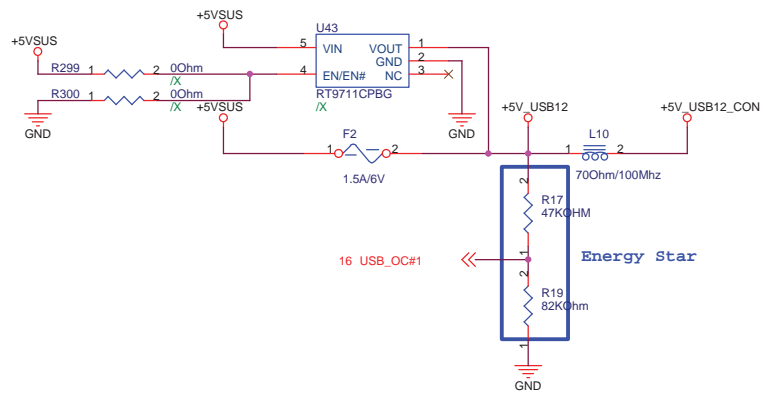
FPC Connector with Mylar /SH for SATA HDD



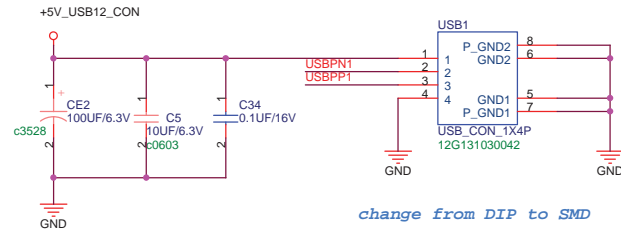
**Naming Rule:**  
IC: IU?  
R: IR?  
C: IC?  
L: IL?

<Variant Names>

<b>ASUS</b>		<b>Title : HD + Flash Conn</b>	
ASUSTek Computer INC.		Engineer: <b>Kell_Huang</b>	
Size A3	Project Name <b>1002</b>	Rev 1.3G	
Date: Friday, October 03, 2008		Sheet 26 of 50	

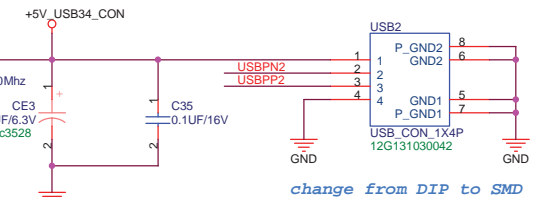
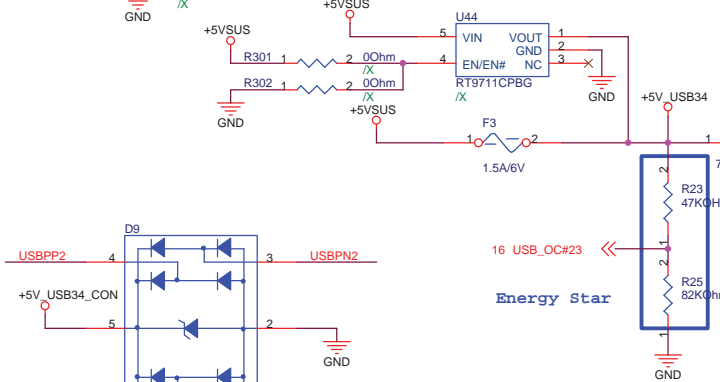
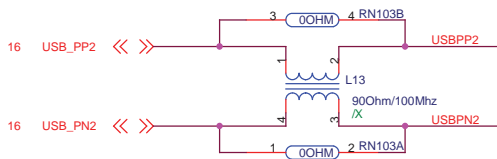
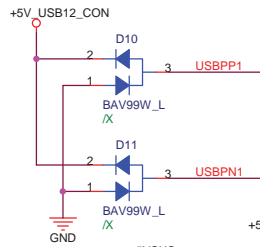
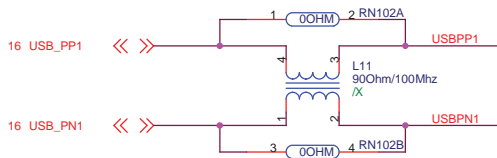


1.1G change USB con. to 12G131030042

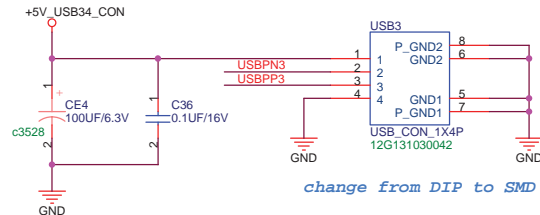
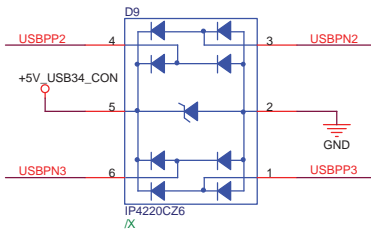
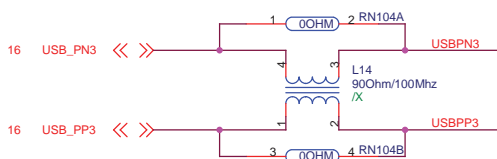


change from DIP to SMD

1.1G change CE2 CE3 CE4 to POSCAP, 100uF/6.3V

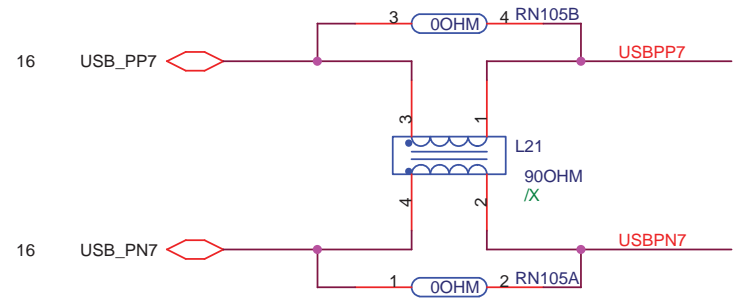
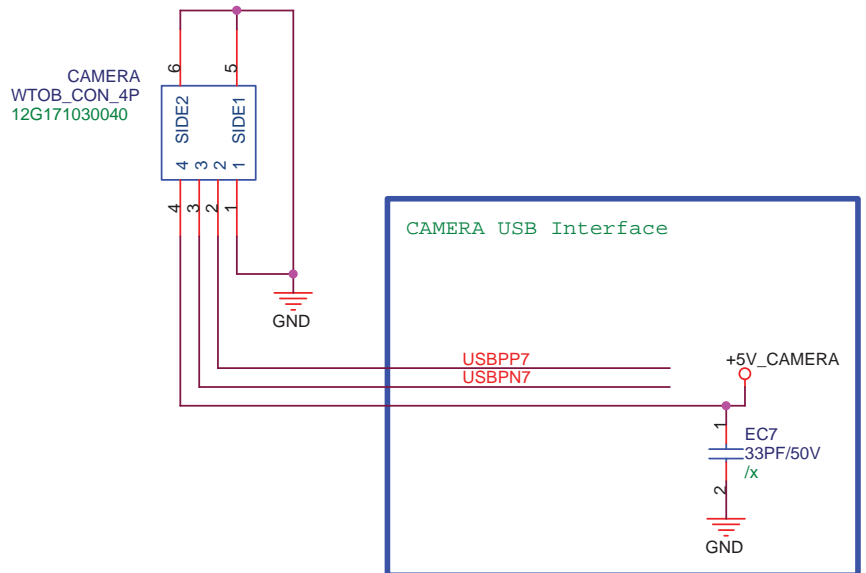
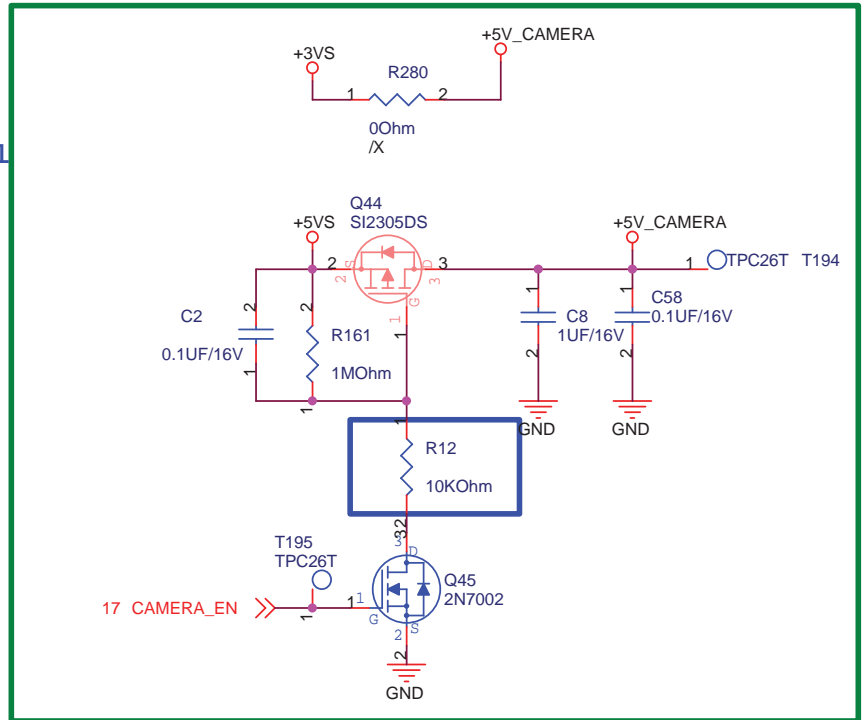


change from DIP to SMD



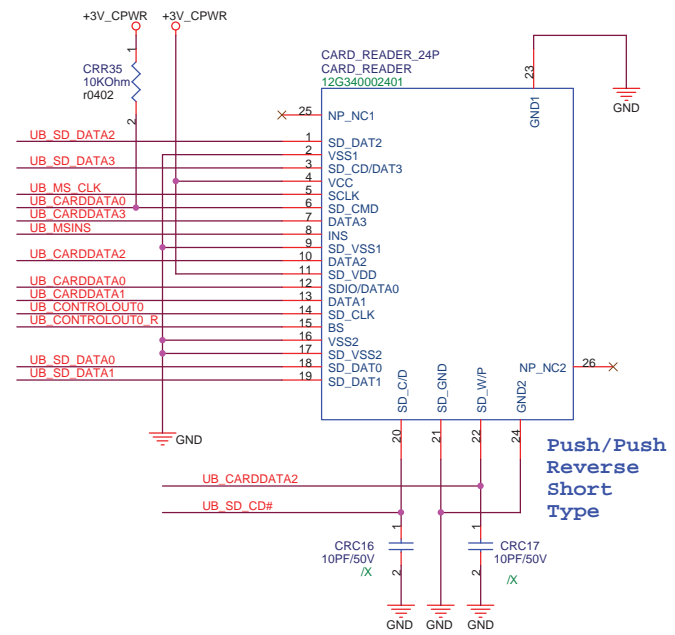
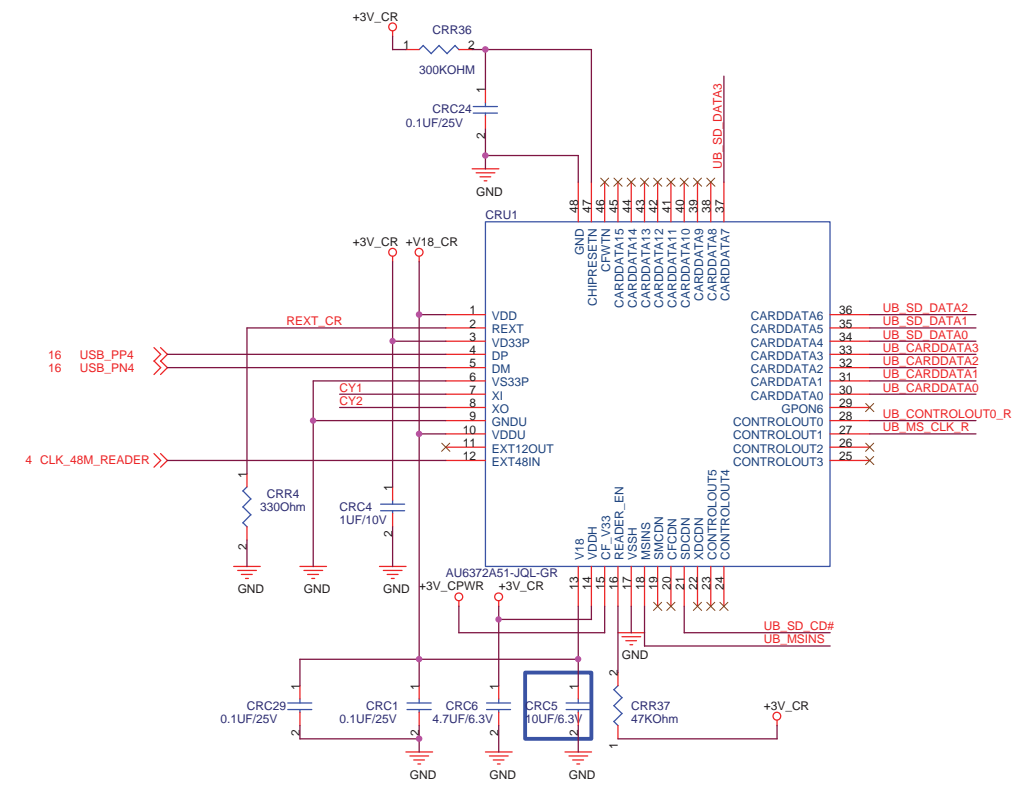
change from DIP to SMD

Power Control



<Variant Name>

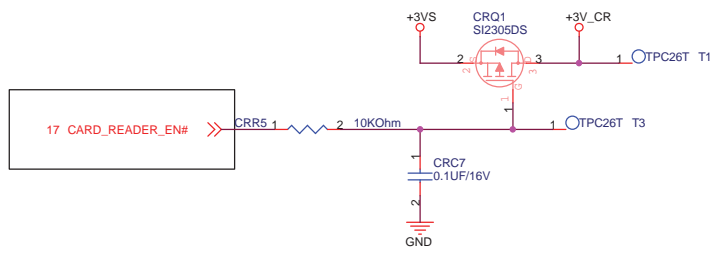
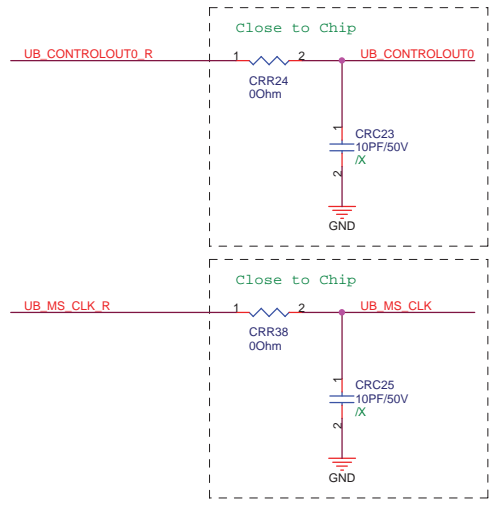
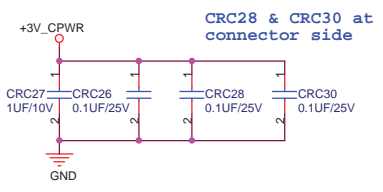
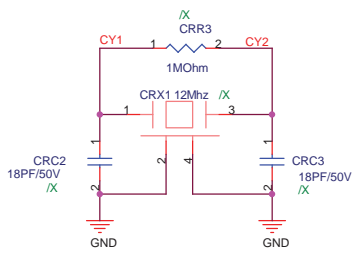
<b>ASUS</b>		<b>Title : Camera Power</b>	
ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size A4	Project Name <b>1002</b>	Rev 1.3G	
Date: Friday, October 03, 2008		Sheet 28 of 50	

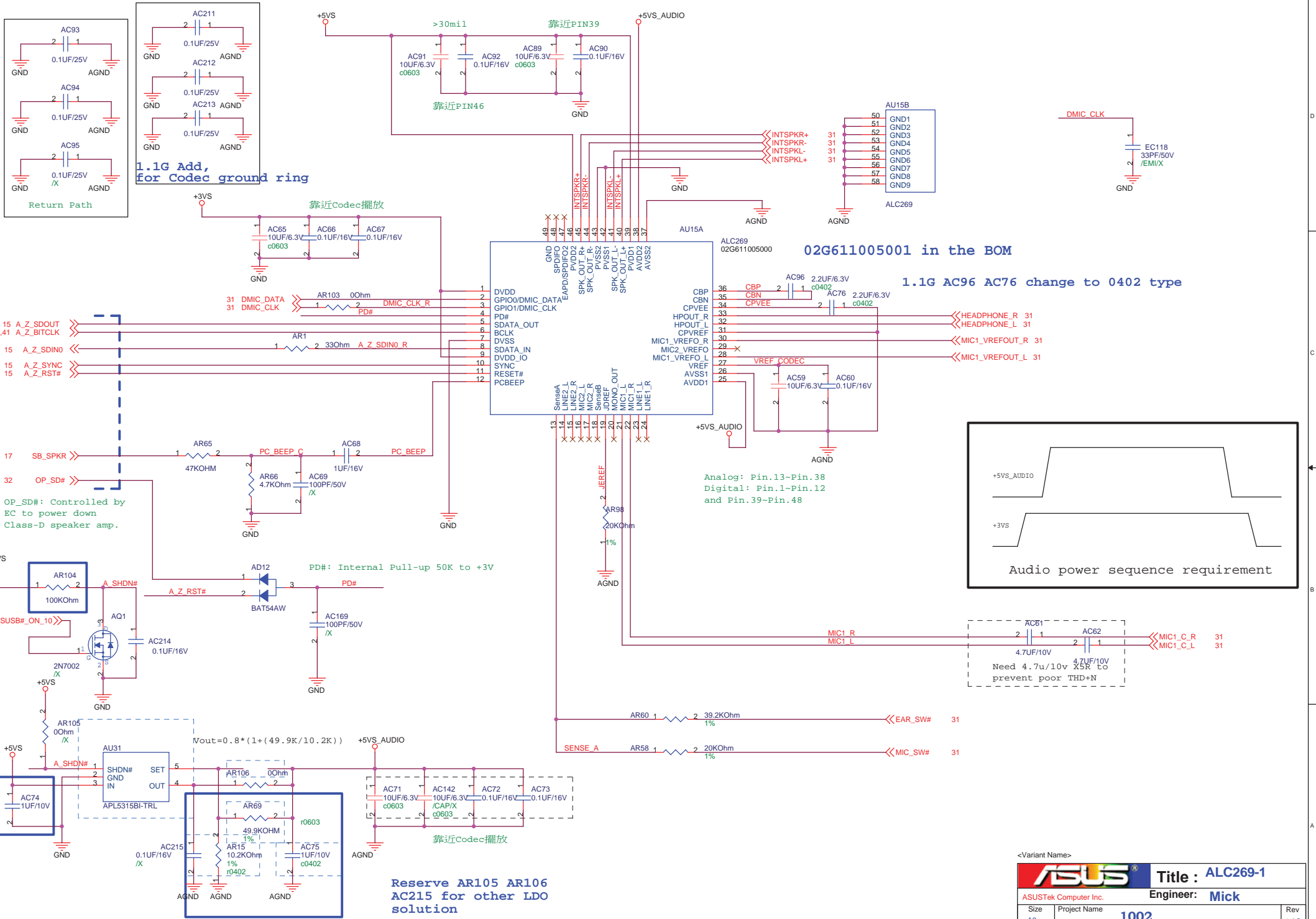


Push/Push Reverse Short Type

SDWP: Internal Pull-up  
 SDCDN: Internal Pull-up  
 SDWP = 1 Write protect  
 SDWP = 0 Write-able  
 SDCDN = 1 No card  
 SDCDN = 0 Card inserted

Card Insert: Pin.20 and Pin.21 are Shorted.  
 Card not Insert: Pin.20 and Pin.21 are Opened.  
 Write Protect: Pin.22 and Pin.21 are Opened.  
 Write Enable: Pin.22 and Pin.21 are Shorted.



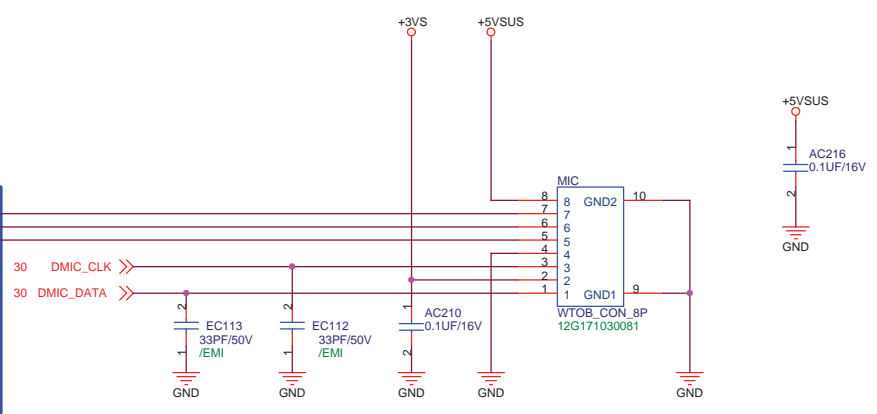
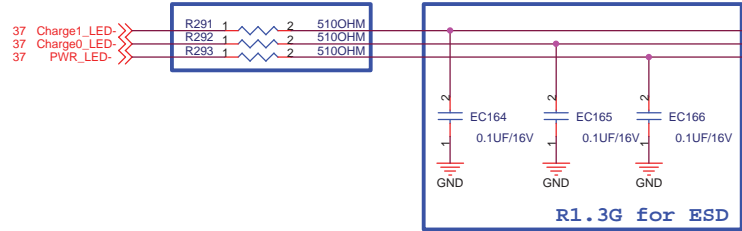


ASUS		Title : ALC269-1	
ASUSTek Computer Inc.		Engineer: Mick	
Size A3	Project Name 1002	Rev 1.3G	
Date: Friday, October 03, 2008	Sheet 30 of 50		

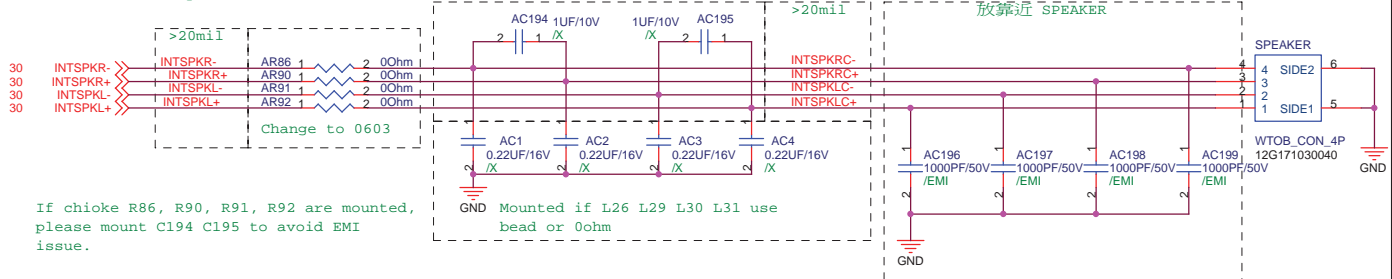
**1.1G add PWR LED and Charge LED**

DMIC Cable length should be less 30cm

Change R291 R292 R293 to 510 Ohm

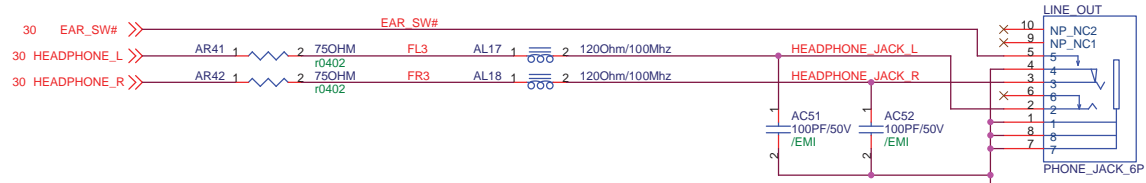


Total length from speakerR+- L+- (pin40 41 44 45) to internal speaker please as short as possible (<20cm is better)



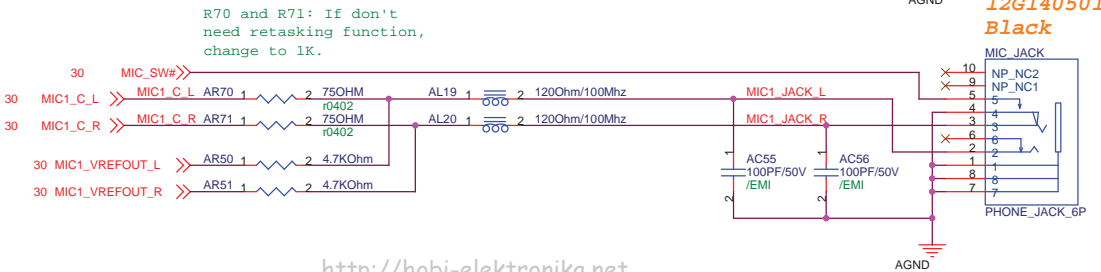
If choke R86, R90, R91, R92 are mounted, please mount C194 C195 to avoid EMI issue.

**LINE\_OUT use**  
**12G14050106P(SINGATRON)**  
**Black**



**1.1G Change audio con. to black**  
 change from DIP to SMD

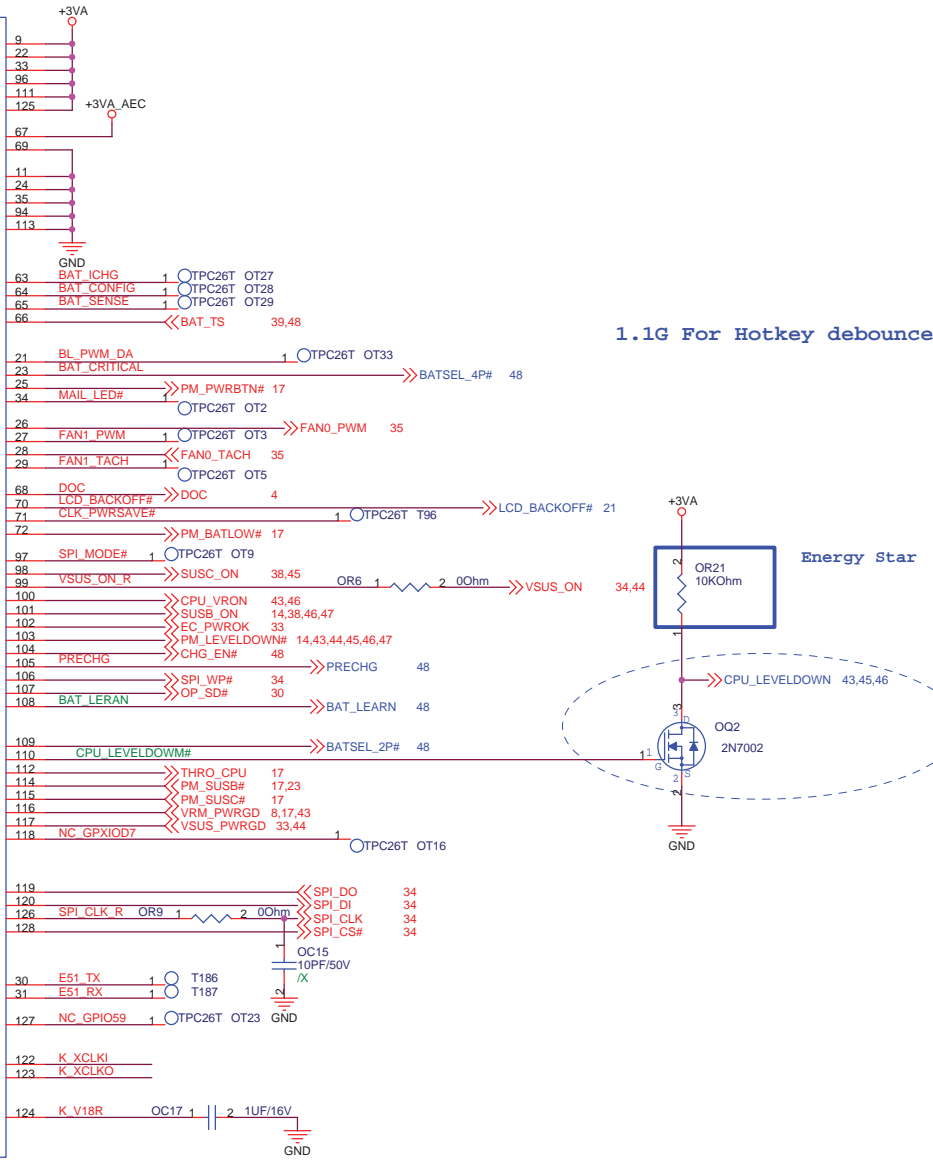
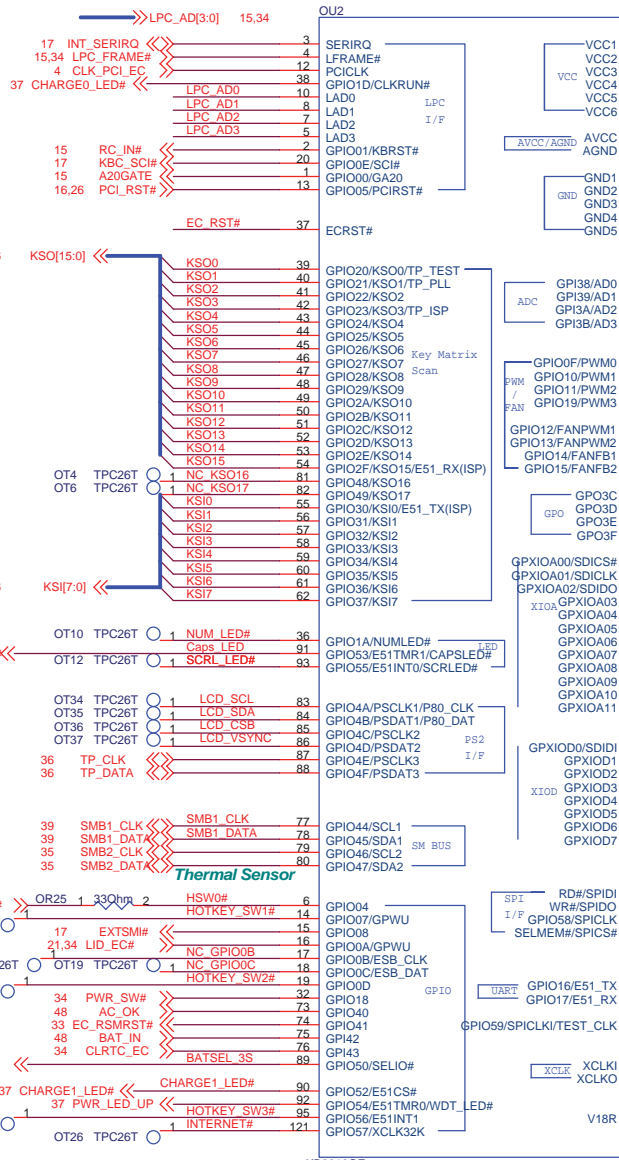
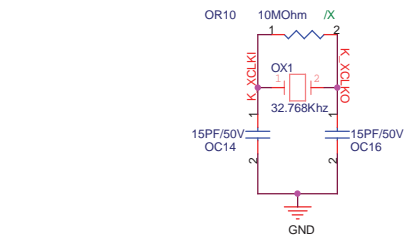
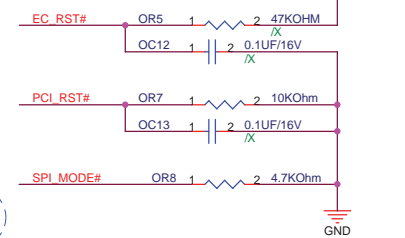
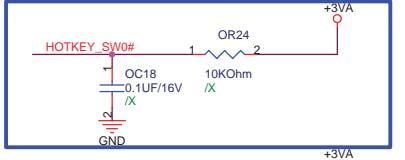
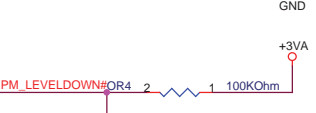
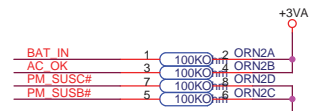
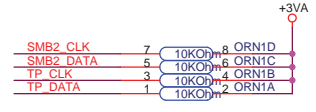
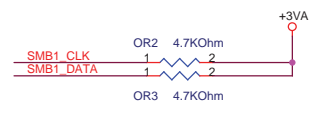
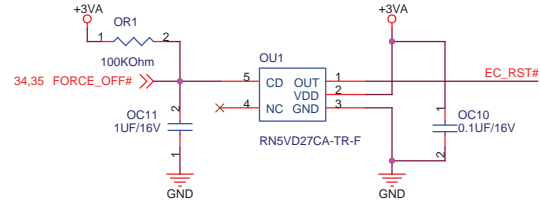
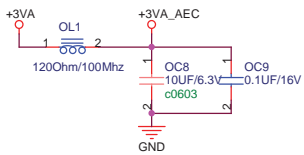
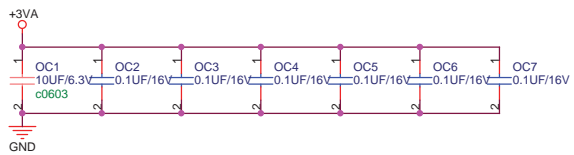
**MIC JACK use**  
**12G14050106P(SINGATRON)**  
**Black**



**1.1G Change audio con. to black**  
 change from DIP to SMD

R70 and R71: If don't need retasking function, change to 1K.

ASUS		Title : ALC269-2	
ASUSTek Computer Inc.		Engineer: MICK	
Size A3	Project Name 1002	Rev 1.3G	
Date: Friday, October 03, 2008	Sheet 31	of 50	



1.1G For Hotkey debounce

Energy Star

OR25 1.1G For Hotkey debounce  
HOTKEY\_SW0# - HOTKEY\_SW3# internal PU

<http://hobi-elektronika.net>

<Variant Name>

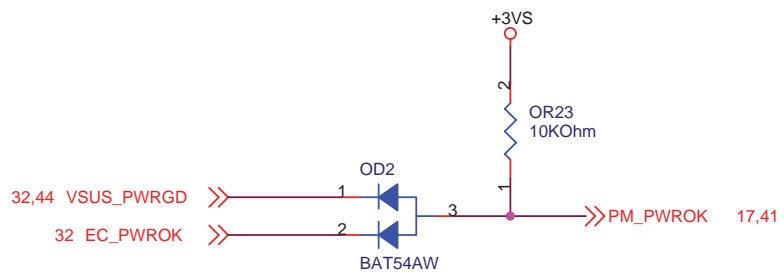
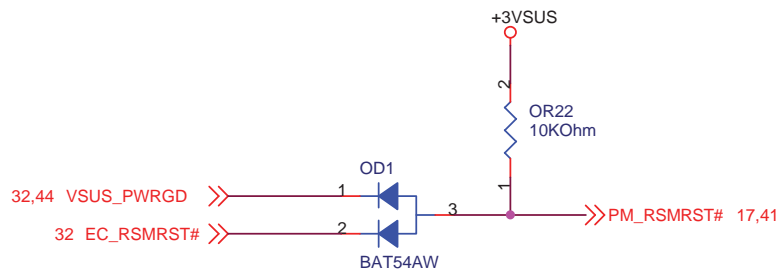
**ASUS** Title : EC\_ENE KB3310

ASUSTek Computer INC. Engineer: Keil\_Huang


Size	Project Name	Rev
A3	1002	1.3G

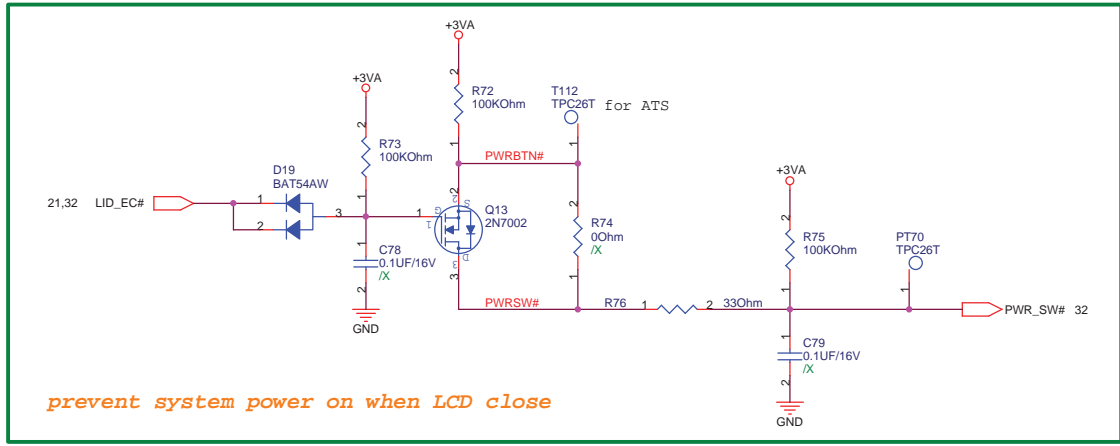
Date: Friday, October 03, 2008 Sheet 32 of 50



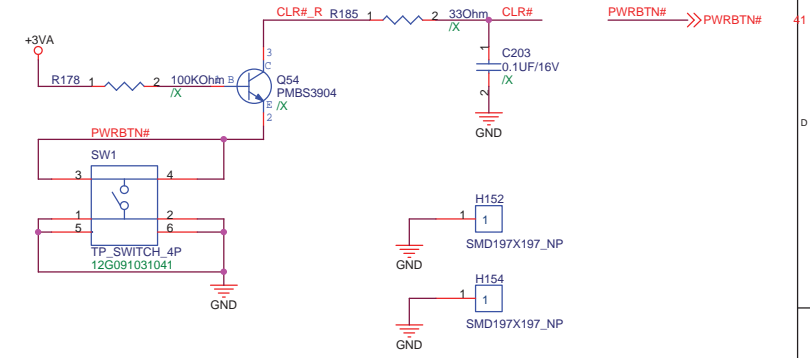


<Variant Name>

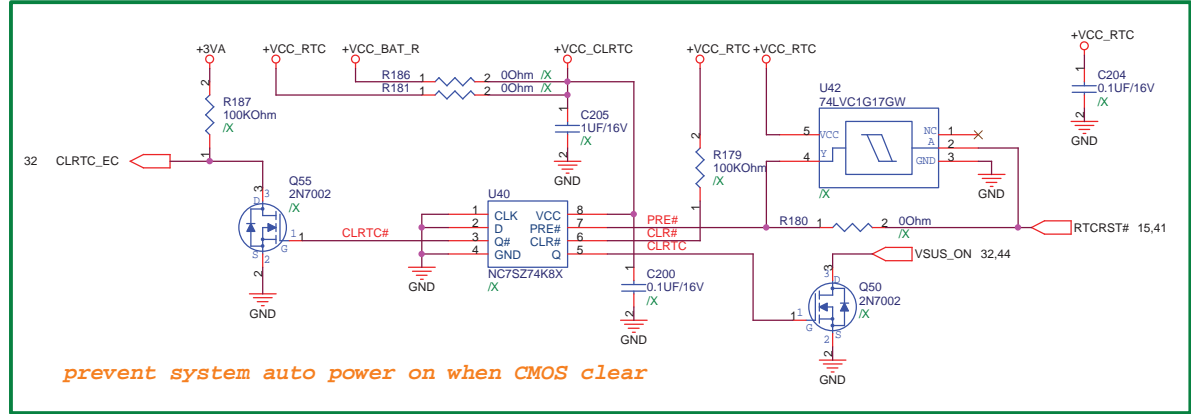
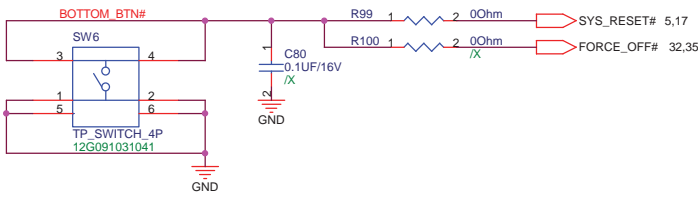
		<b>Title :</b> EC_UART_KC3820	
ASUSTek Computer INC.		<b>Engineer:</b> Kell_Huang	
Size A4	Project Name <b>1002</b>		Rev 1.3G
Date: Friday, October 03, 2008		Sheet 33 of 50	



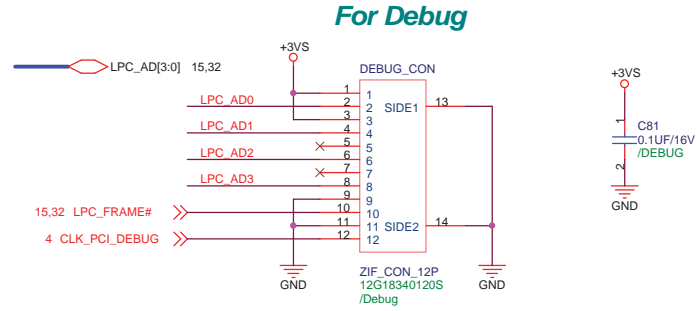
prevent system power on when LCD close



H152&H154 : Pad for EMI



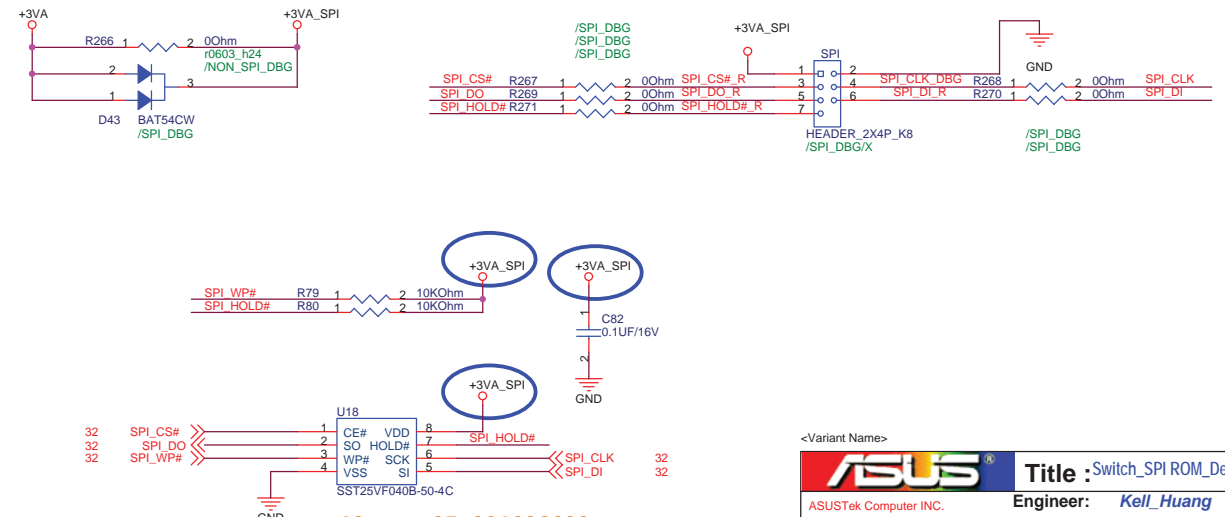
prevent system auto power on when CMOS clear



For Debug

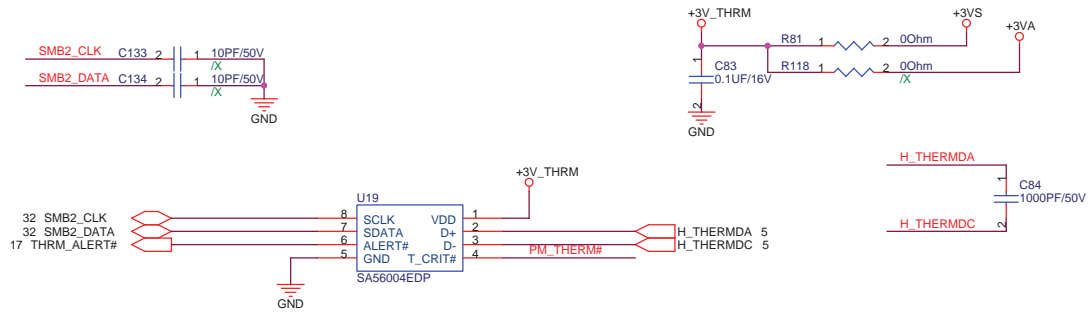
Debug Card cable use Z96 Touch Pad cable, P/N: 14G124110126, 14G124110120, 14G124110121, 14G124110124, 14G124110125

<http://hobi-elektronika.net>

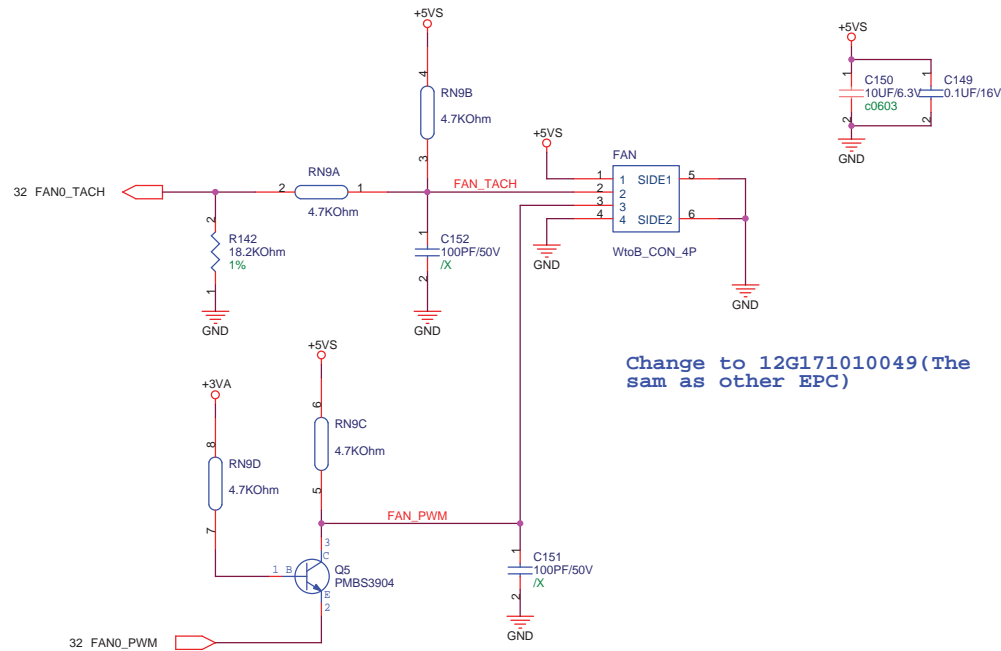
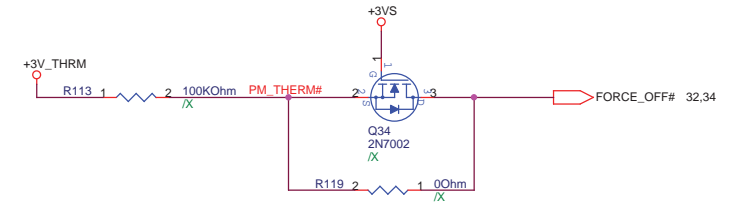


U18 use 05G001002900 & 05G00100F130

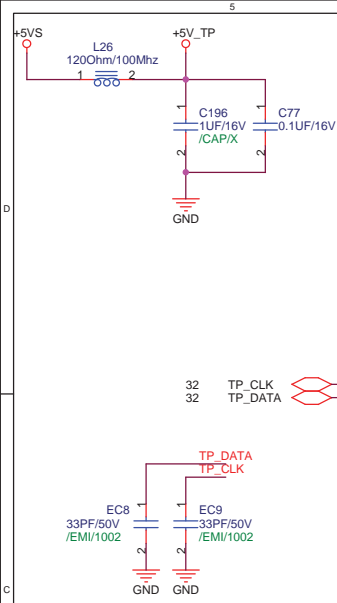
ASUS		Title : Switch_SPI ROM_Debug	
ASUSTek Computer INC.		Engineer: Kel_Huang	
Size	A3	Project Name	1002
Date:	Friday, October 03, 2008	Sheet	34 of 50
Rev	1.3G		



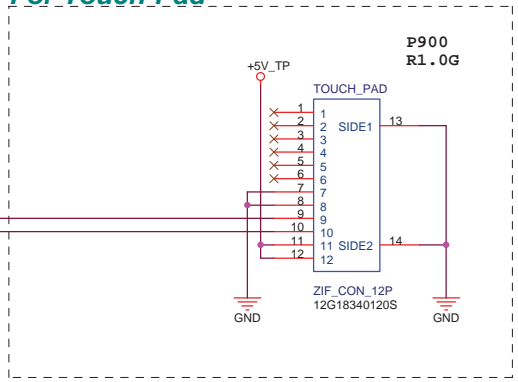
U19 use 06G023044020,  
second source 06G023055010



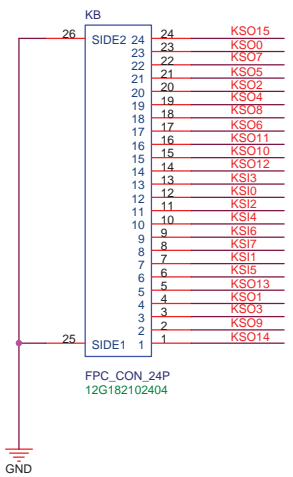
Change to 12G171010049(The  
sam as other EPC)



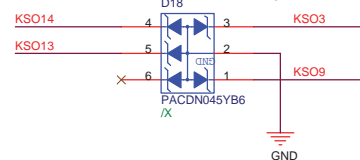
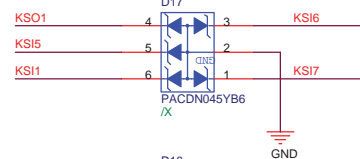
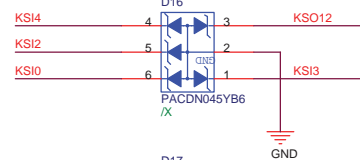
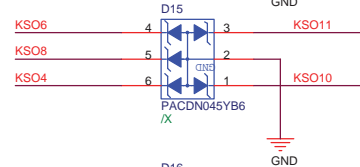
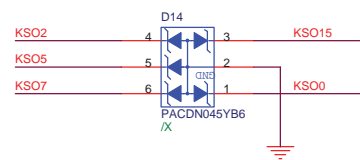
**For Touch-Pad**



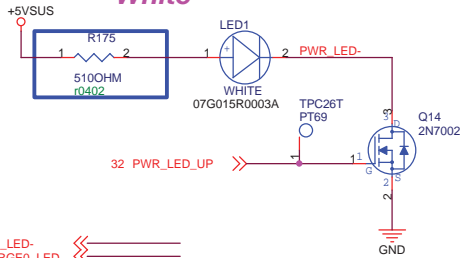
**For Keyboard Connector**



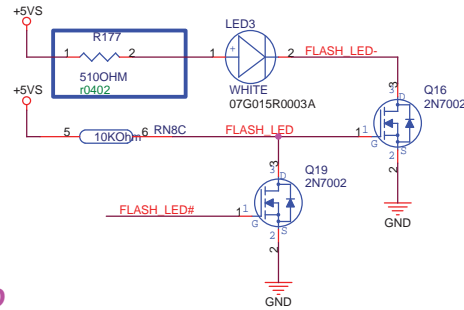
For assembly direction, KB pin1 to KB conn. pin24



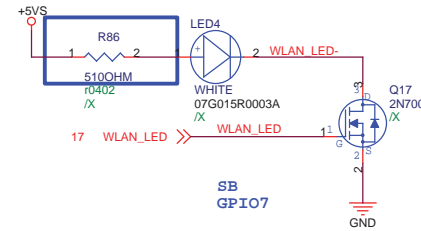
**for POWER LED White**



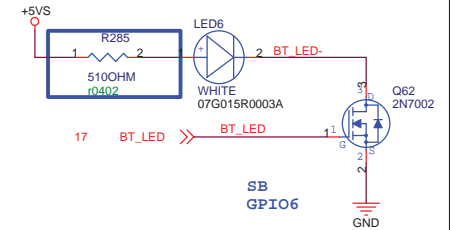
**for FLASH LED White**



**White /X**

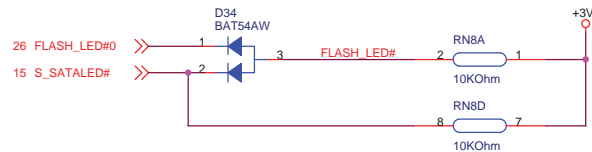


**for WIFI/BlueTooth LED White**



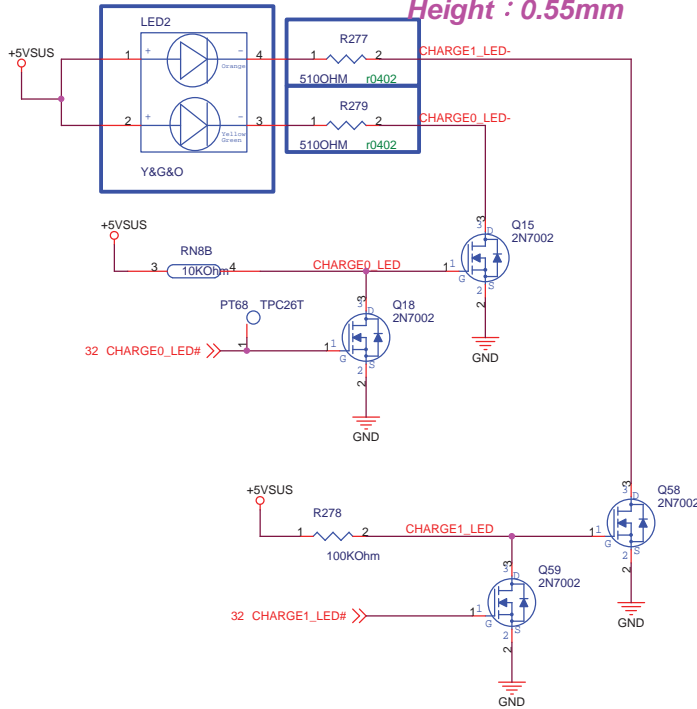
31 PWR\_LED-  
31 CHARGE0\_LED-  
31 CHARGE1\_LED-

Change LED resistor to 510 Ohm, about 4mA

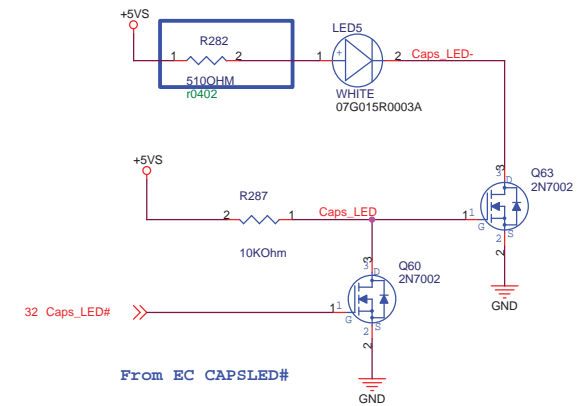


**1.1G change to EVERLIGHT**

**for CHARGE LED Height : 0.55mm**



**for Caps Lock LED White**



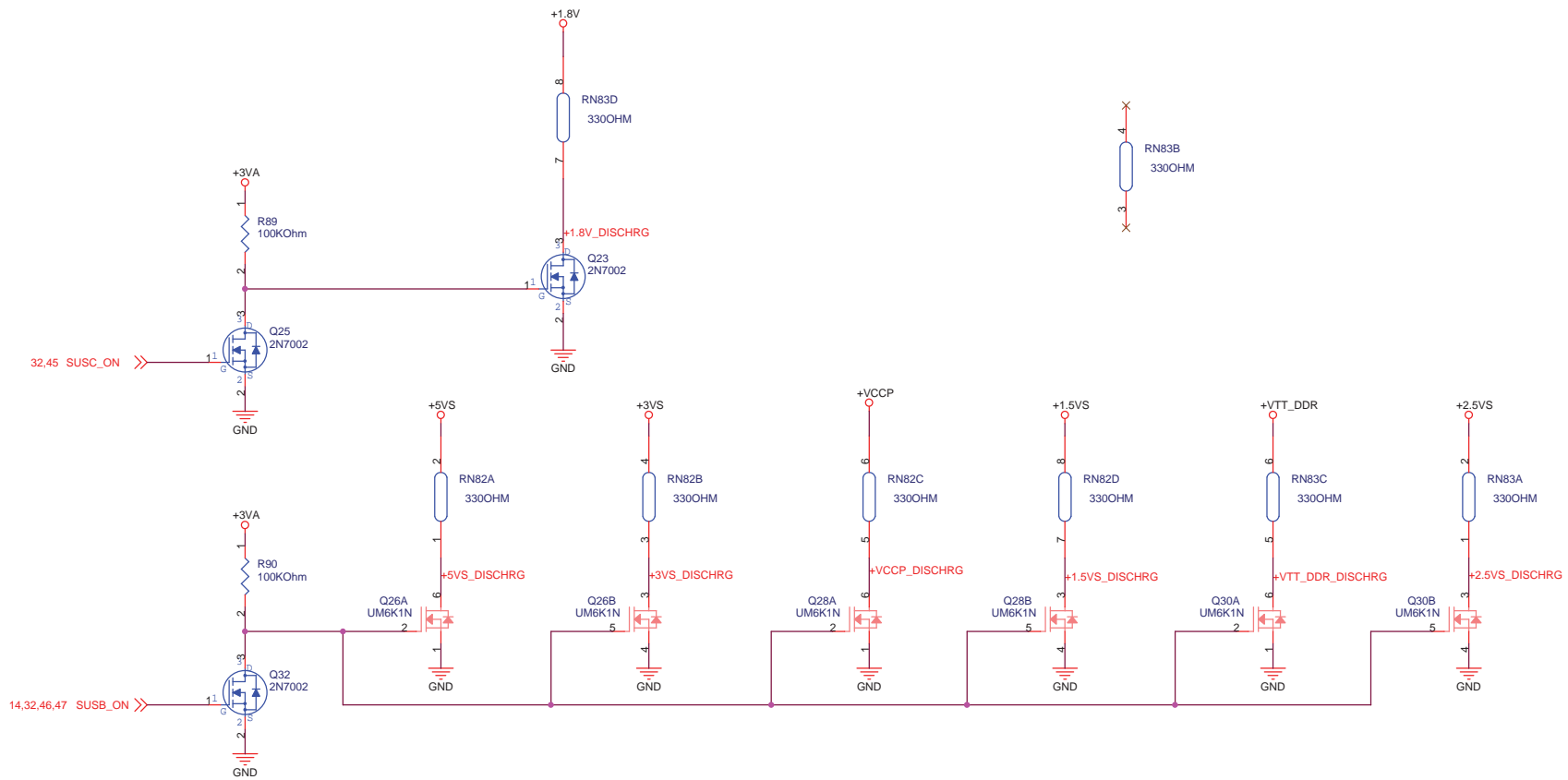
The battery charge indicator (LED) shows the status of the battery's power as follows:

scenario	Adapter mode	Battery mode
Battery power is between 100%~80%	<b>Orange ON</b>	<b>Green ON</b>
Battery power is between 80%~10%	<b>Orange Blinking Slowly</b>	<b>Green Blinking Slowly</b>
Battery power is less than 10%	<b>Orange Blinking Quickly</b>	<b>Green Blinking Quickly</b>
S3/S5 Mode	<b>Scenario the same as above</b>	<b>Off</b>

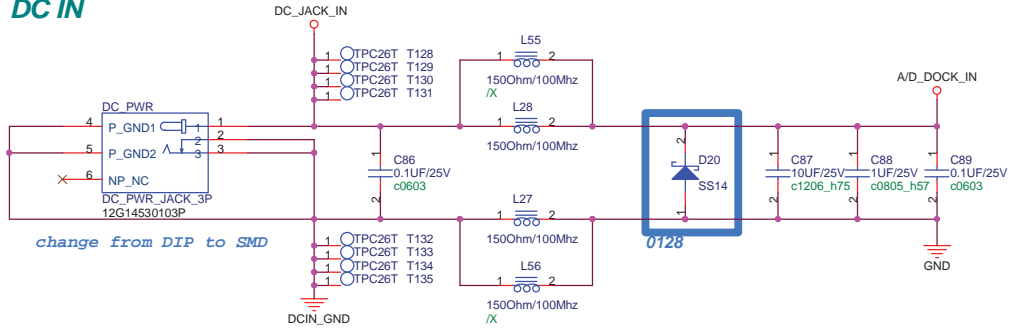
Note: The BATTERY LED should be off when the machine has no battery attached

<Variant Name>

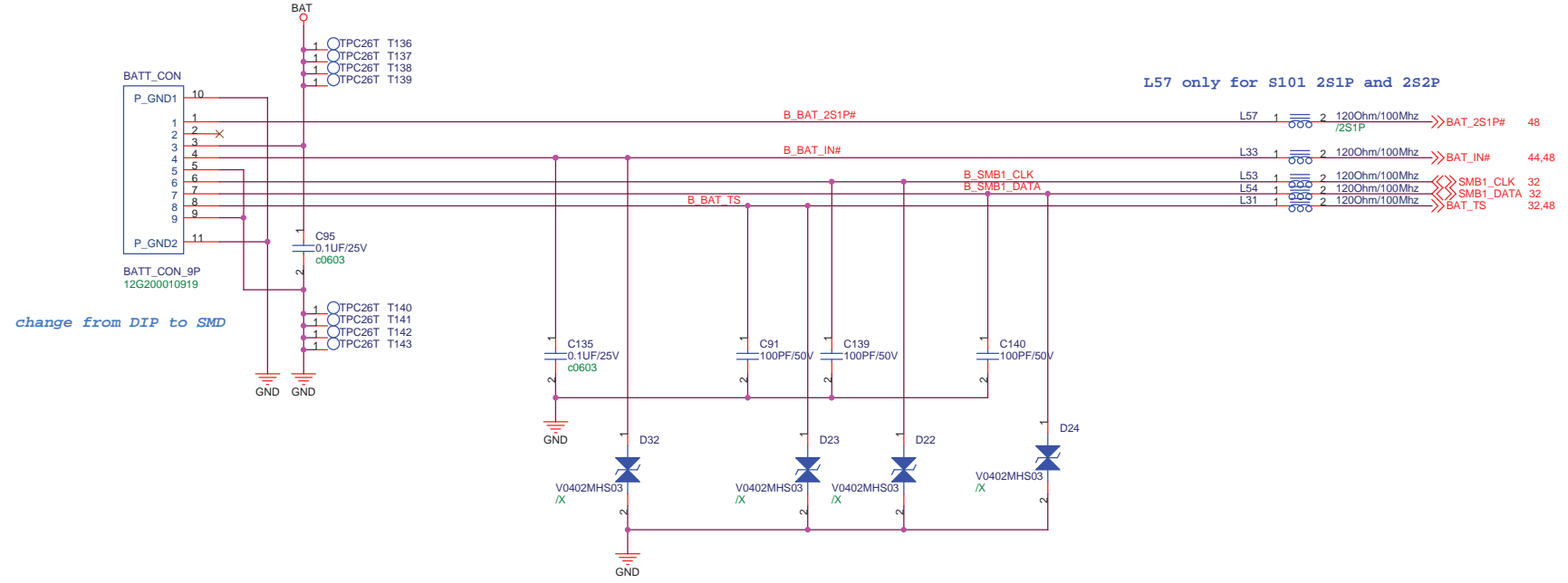
<b>ASUS</b>		<b>Title : LED</b>	
ASUSTek Computer INC.		Engineer: <b>Kell_Huang</b>	
Size	Project Name	Rev	
A3	<b>1002</b>	1.3G	
Date: Friday, October 03, 2008	Sheet	37	of 50



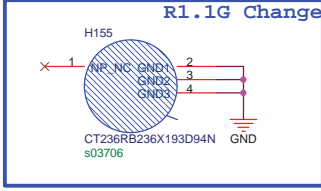
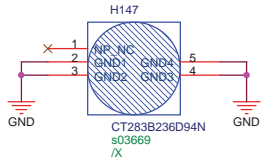
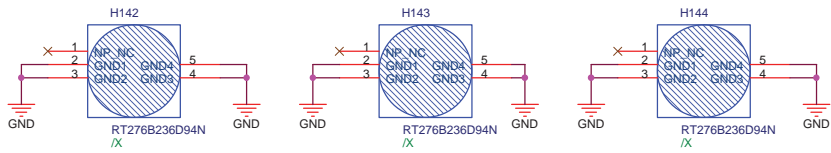
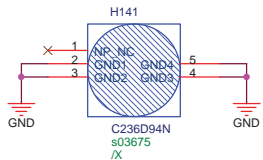
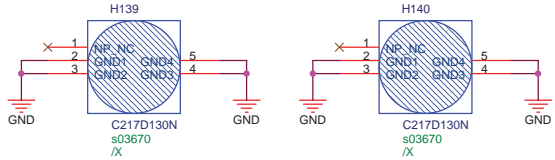
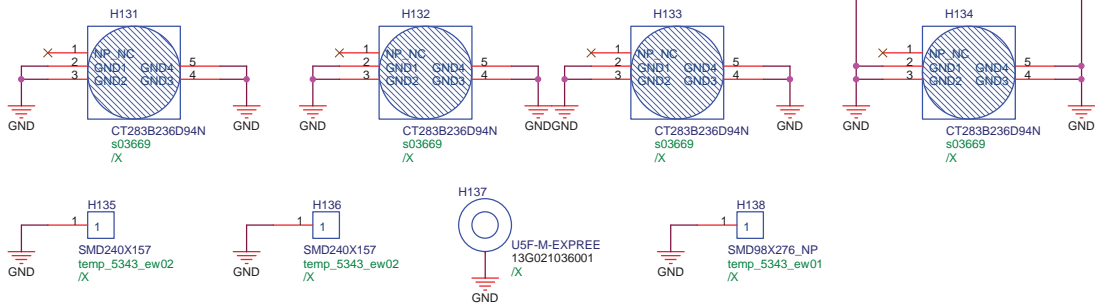
**DC IN**



**BAT IN**

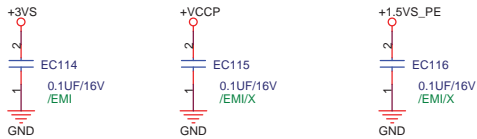


ASUS		Title : PWR Jack	
ASUSTek Computer INC.		Engineer: Keli_Huang	
Size	Project Name	Rev	
A3	1002	1.3G	
Date: Friday, October 03, 2008	Sheet	39	of 50

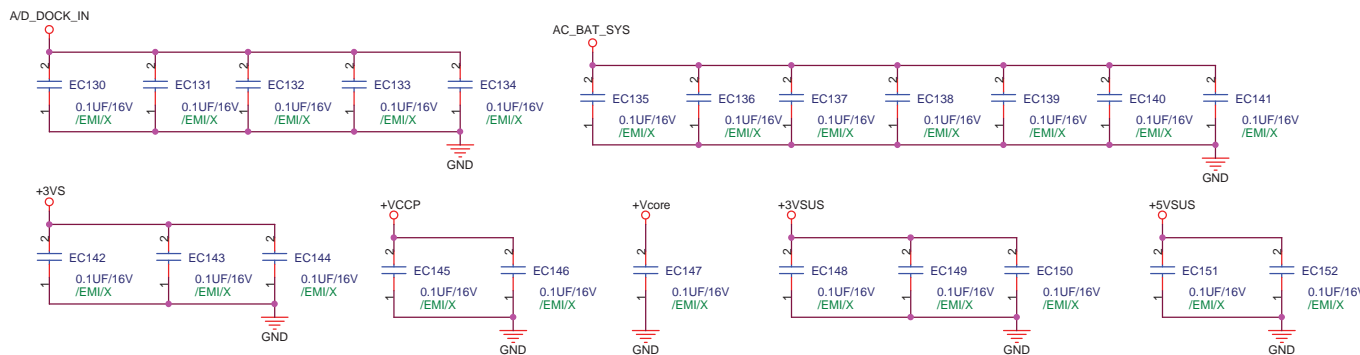
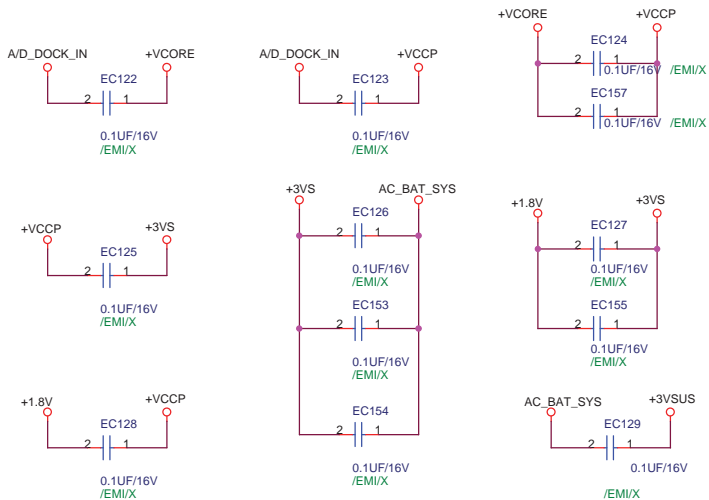
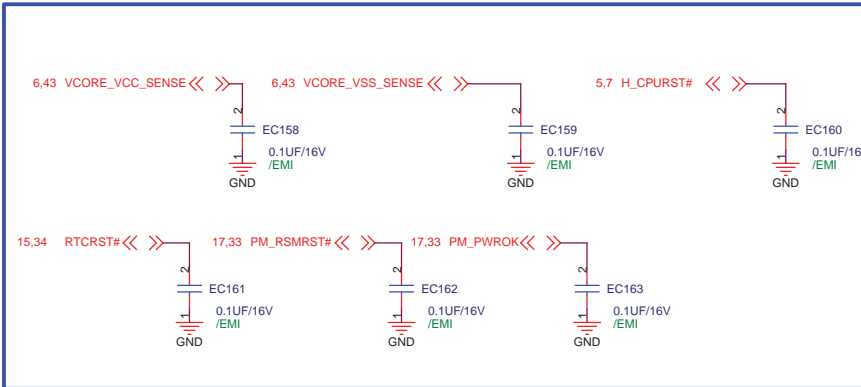


<Variant Name>		ASUS®		Title : Srew Hole	
ASUSTek Computer INC.		Engineer: <i>Kel_Huang</i>			
Size	Project Name	Rev			
A3	1002	1.3G			
Date: Friday, October 03, 2008	Sheet	40	of	50	

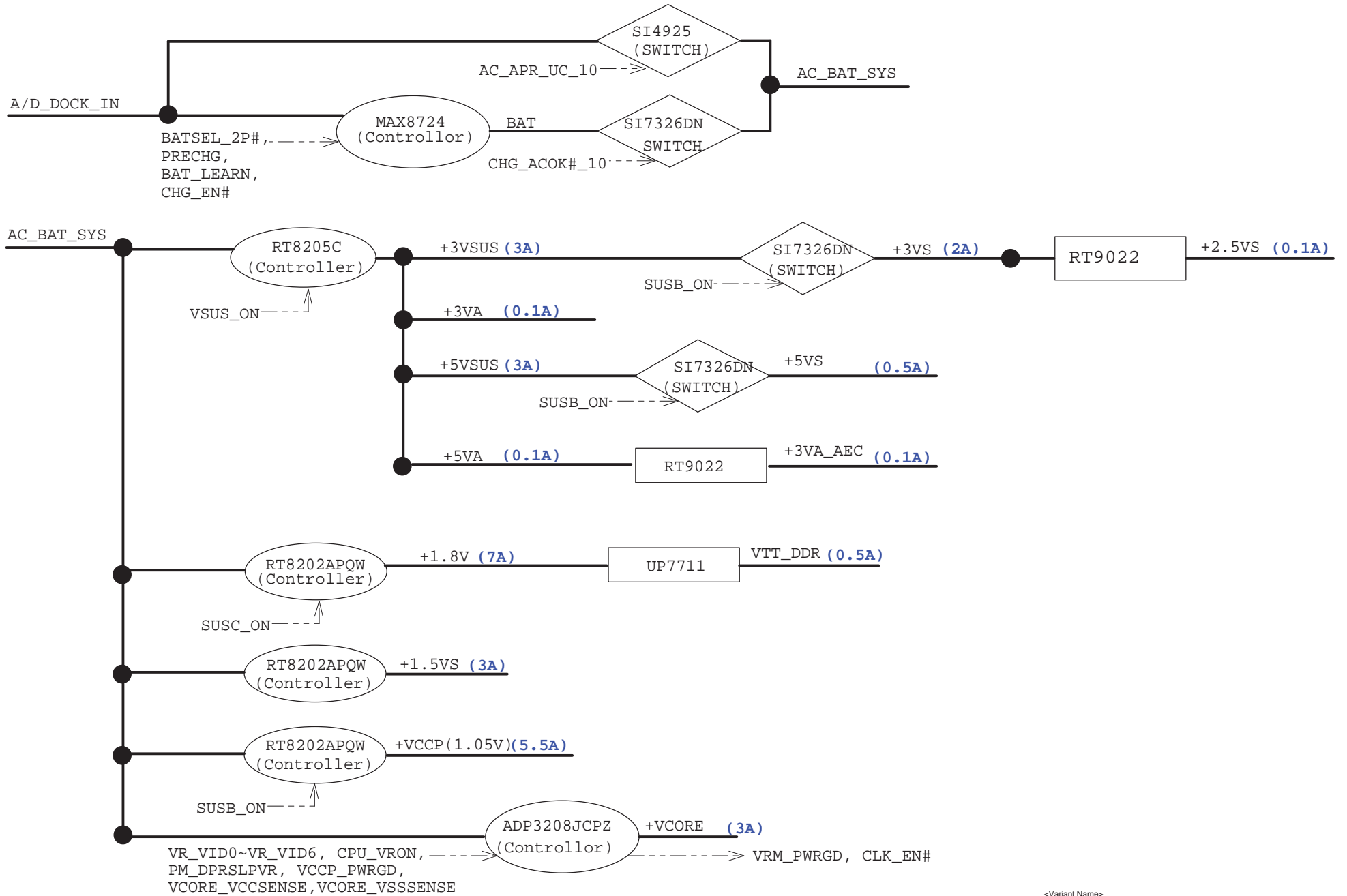


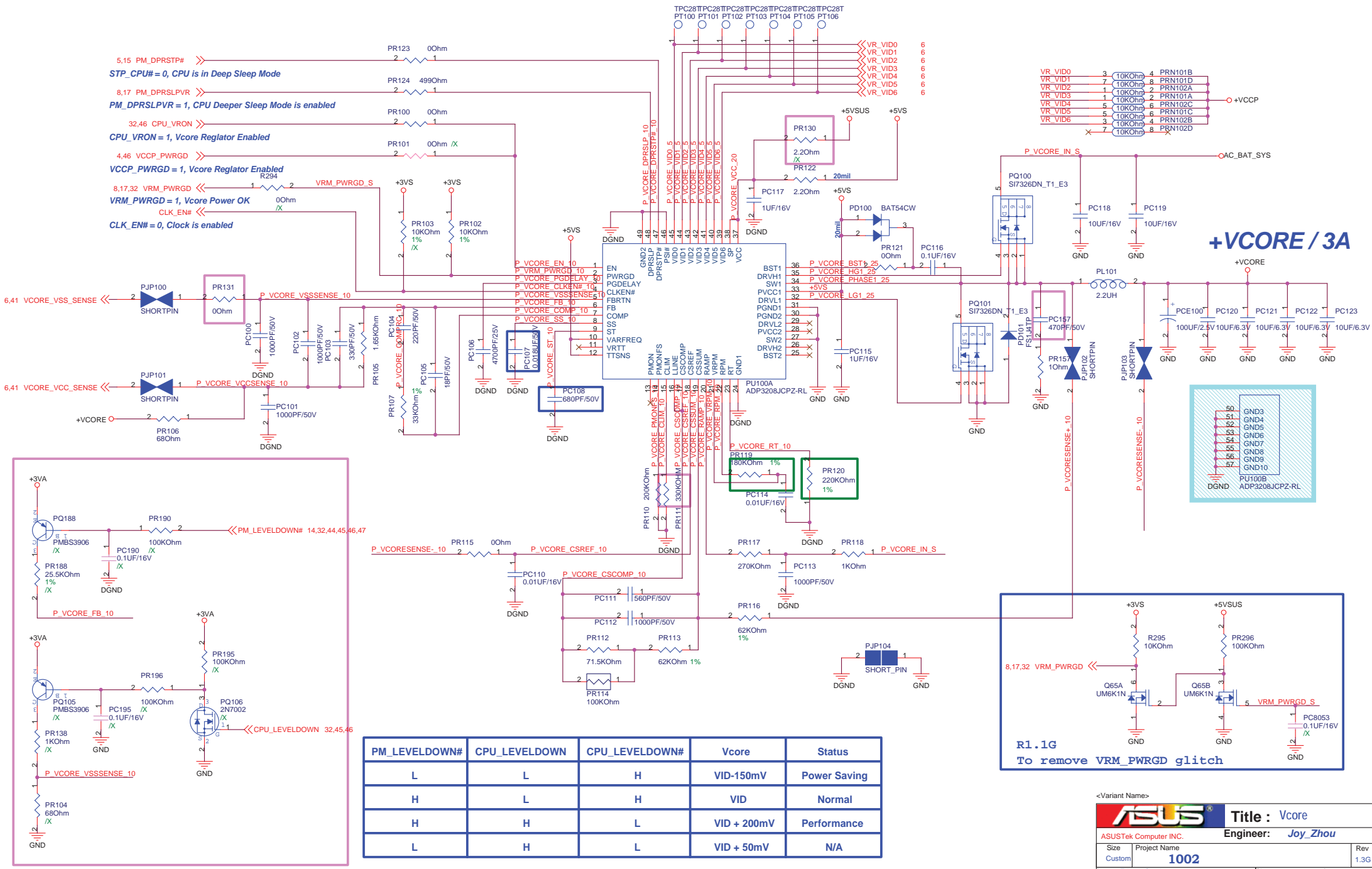


### 1.3G For ESD



<Variant Name>		<b>ASUS</b>		<b>Title : EMI</b>	
ASUSTek Computer INC.		ASUSTek Computer INC.		Engineer: <i>Kell_Huang</i>	
Size	Project Name	Date: Friday, October 03, 2008		Sheet	41 of 50
A3	1002			Rev	1.3G

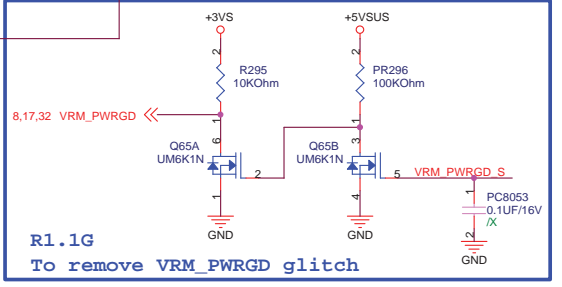




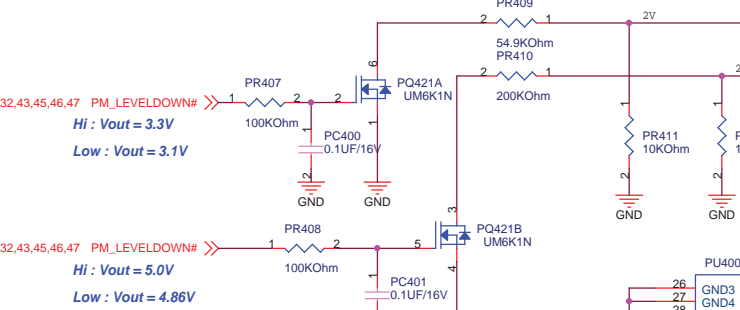
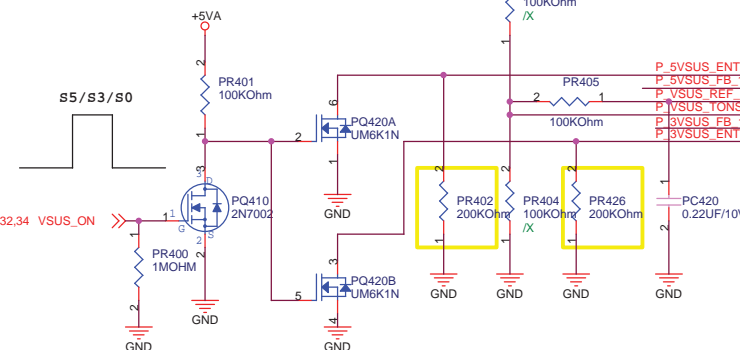
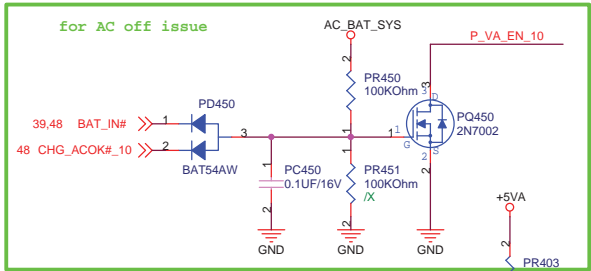
5.15 PM\_DPRSTP# >>>  
**STP\_CPU# = 0, CPU is in Deep Sleep Mode**  
 8.17 PM\_DPRSLPVR >>>  
**PM\_DPRSLPVR = 1, CPU Deeper Sleep Mode is enabled**  
 32.46 CPU\_VRON >>>  
**CPU\_VRON = 1, Vcore Regulator Enabled**  
 4.46 VCCP\_PWGRD >>>  
**VCCP\_PWGRD = 1, Vcore Regulator Enabled**  
 8.17.32 VRM\_PWGRD <<<  
**VRM\_PWGRD = 1, Vcore Power OK**  
 CLK\_EN# <<<  
**CLK\_EN# = 0, Clock is enabled**

### +Vcore / 3A

PM_LVLDOWN#	CPU_LVLDOWN	CPU_LVLDOWN#	Vcore	Status
L	L	H	VID-150mV	Power Saving
H	L	H	VID	Normal
H	H	L	VID + 200mV	Performance
L	H	L	VID + 50mV	N/A

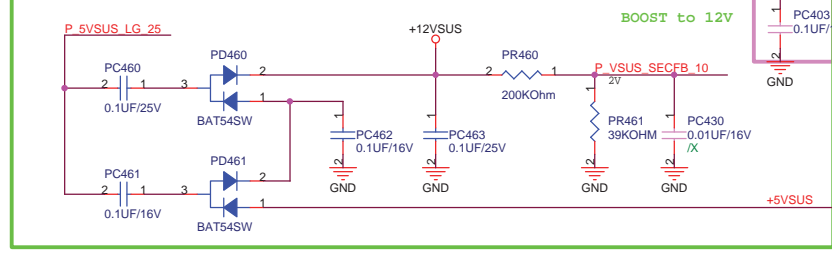
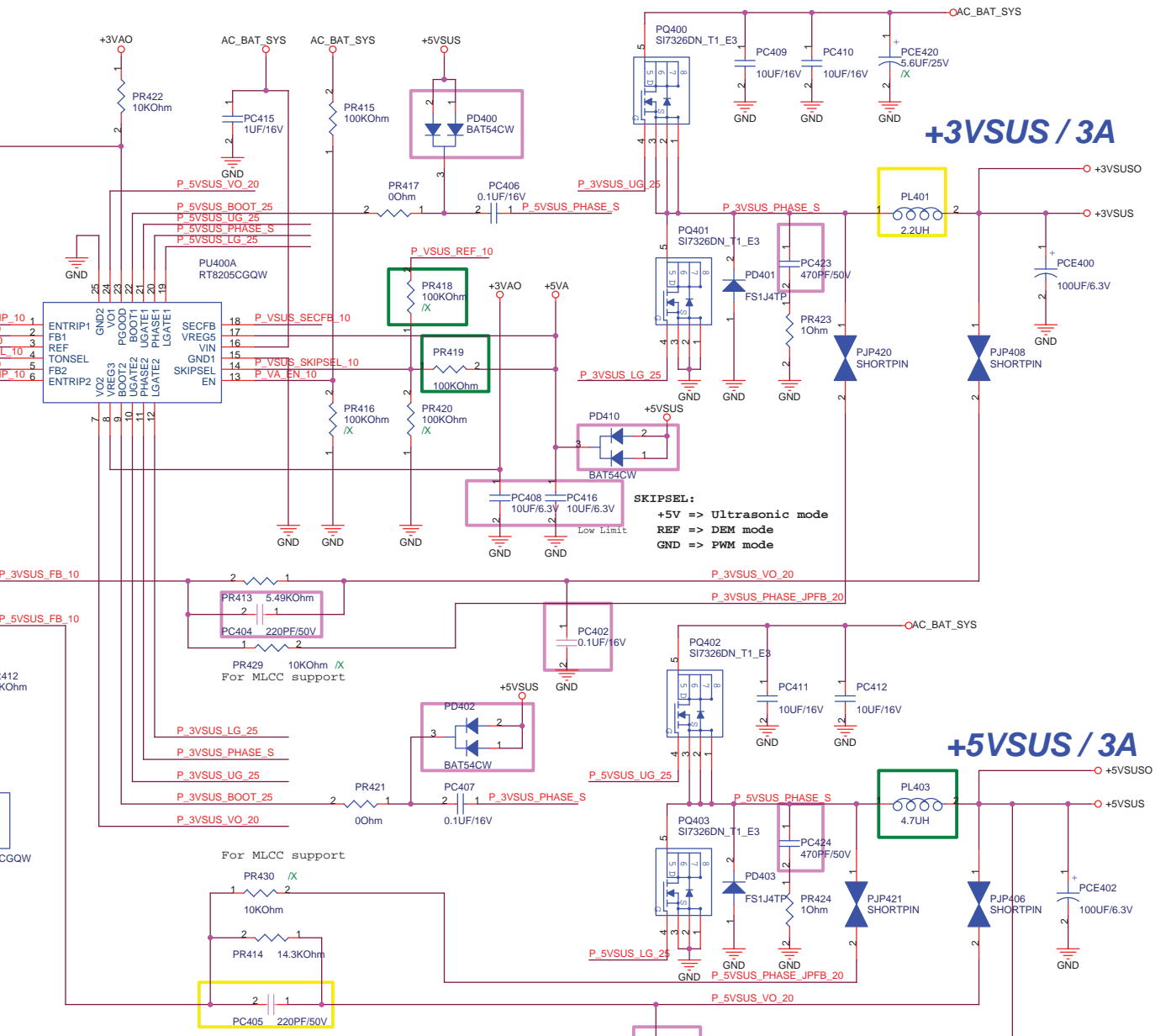


<Variant Name>  
**Title : Vcore**  
 ASUSTek Computer INC. **Engineer: Joy\_Zhou**  
 Size Project Name  
 Custom **1002** Rev 1.3G  
 Date: Friday, October 03, 2008 Sheet 43 of 50



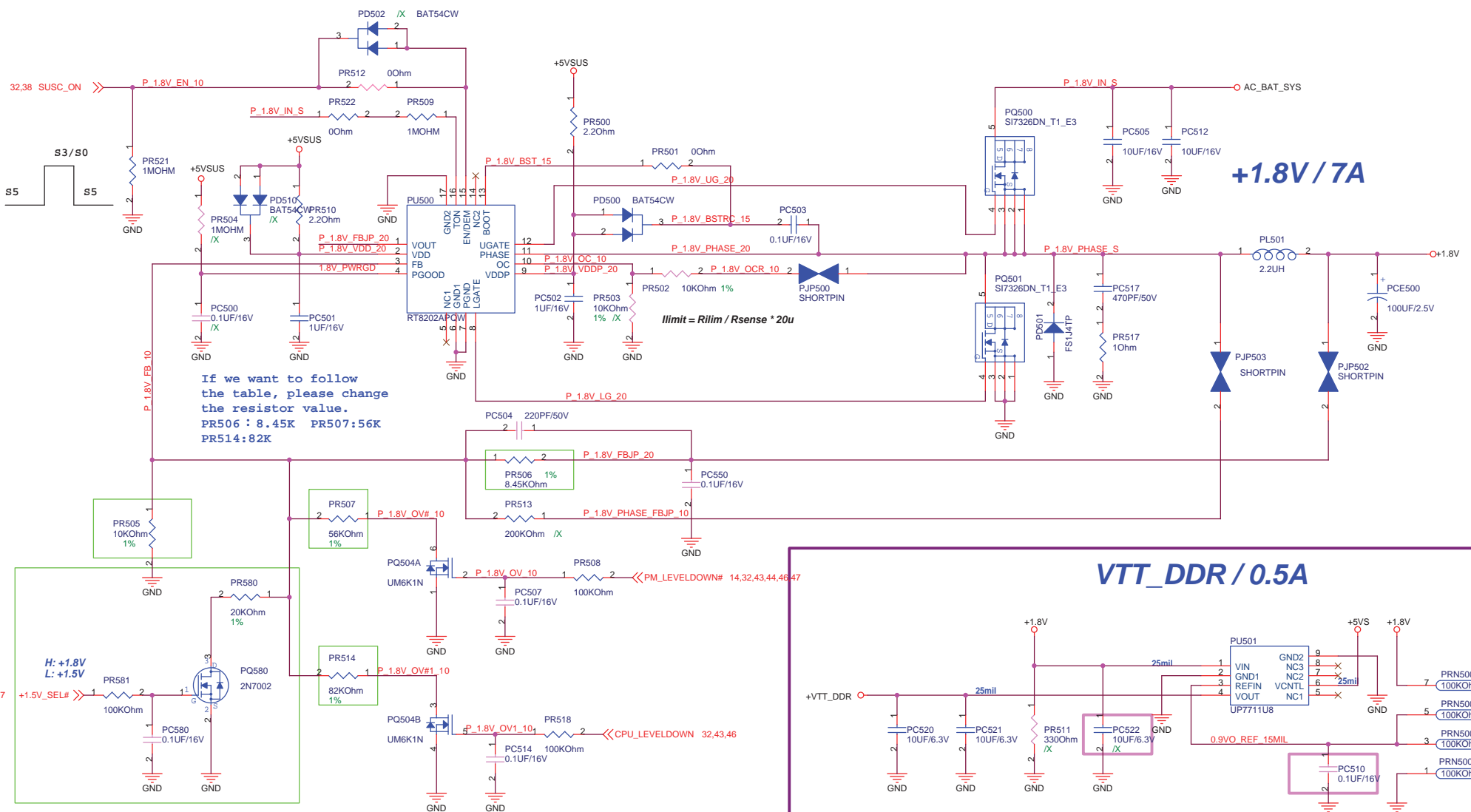
ENTRIP:  
 GND => Disable  
 OCF =>  $(10\mu A \times R) / 10 / R_{dson}$

TONSEL:  
 +5V => 400KHz / 500KHz  
 REF => 300KHz / 375KHz  
 GND => 200KHz / 250KHz



<Variant Name>

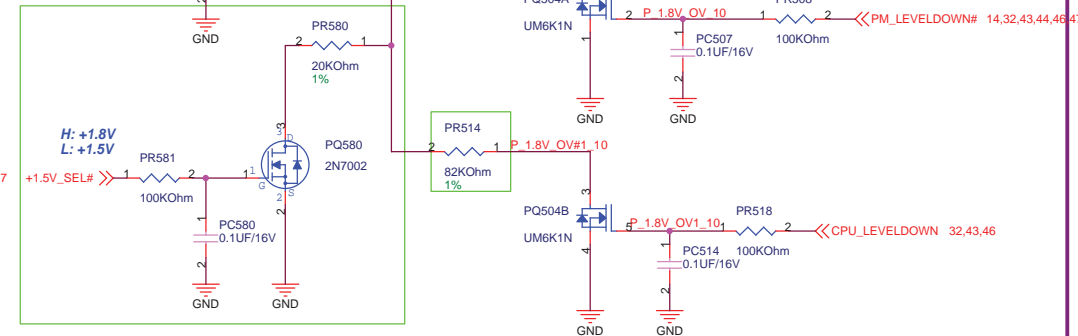
<b>ASUS</b>		<b>Title: +3VSUS &amp; +5VSUS &amp; +3VA</b>	
ASUSTeK COMPUTER INC		Engineer: N/A	
Size A3	Project Name <b>1002</b>	Rev 1.3G	
Date: Friday, October 03, 2008		Sheet 44 of 50	



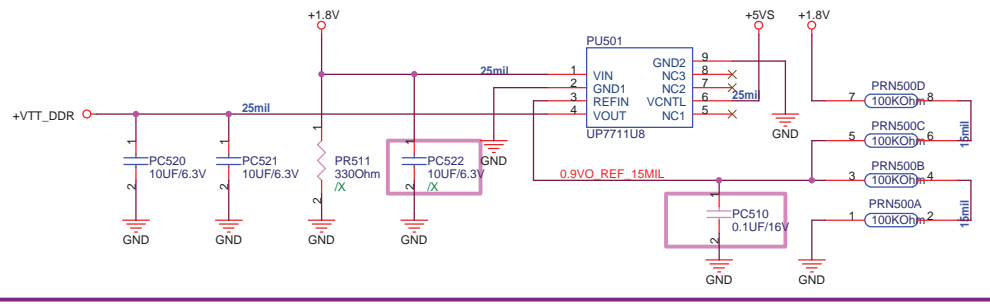
**+1.8V / 7A**

If we want to follow the table, please change the resistor value.  
 PR506: 8.45K PR507: 56K  
 PR514: 82K

$I_{limit} = \frac{R_{lim}}{R_{sense}} * 20\mu$

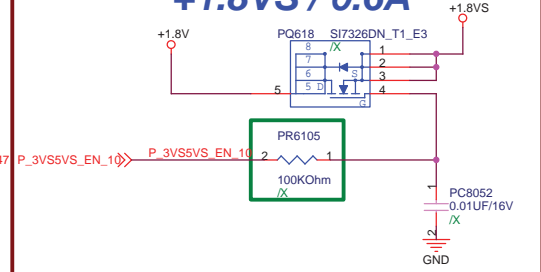


**VTT\_DDR / 0.5A**

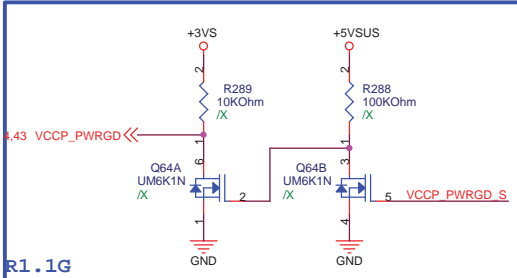
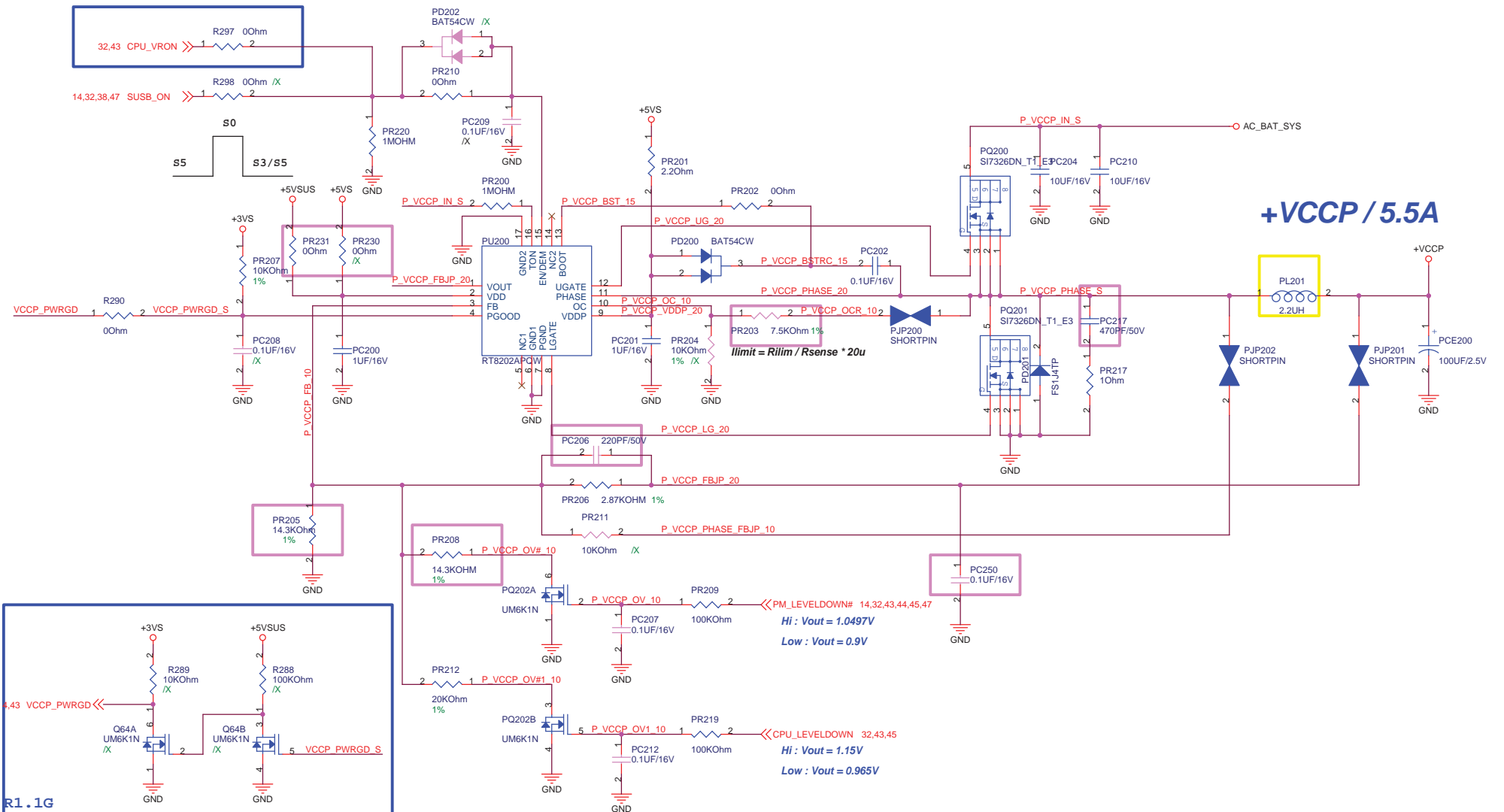


+1.5V_SEL#	PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
H	L	L	H	1.72V	Power Saving
H	H	L	H	1.82V	Normal
H	H	H	L	1.9V	Performance
H	L	H	L	1.782V	N/A
L	L	L	H	1.4V	Power Saving
L	H	L	H	1.5V	Normal
L	H	H	L	1.58V	Performance

**+1.8VS / 0.6A**



1.1G change Enable signal from CPU\_VRON



To remove VCCP\_PWRGD glitch

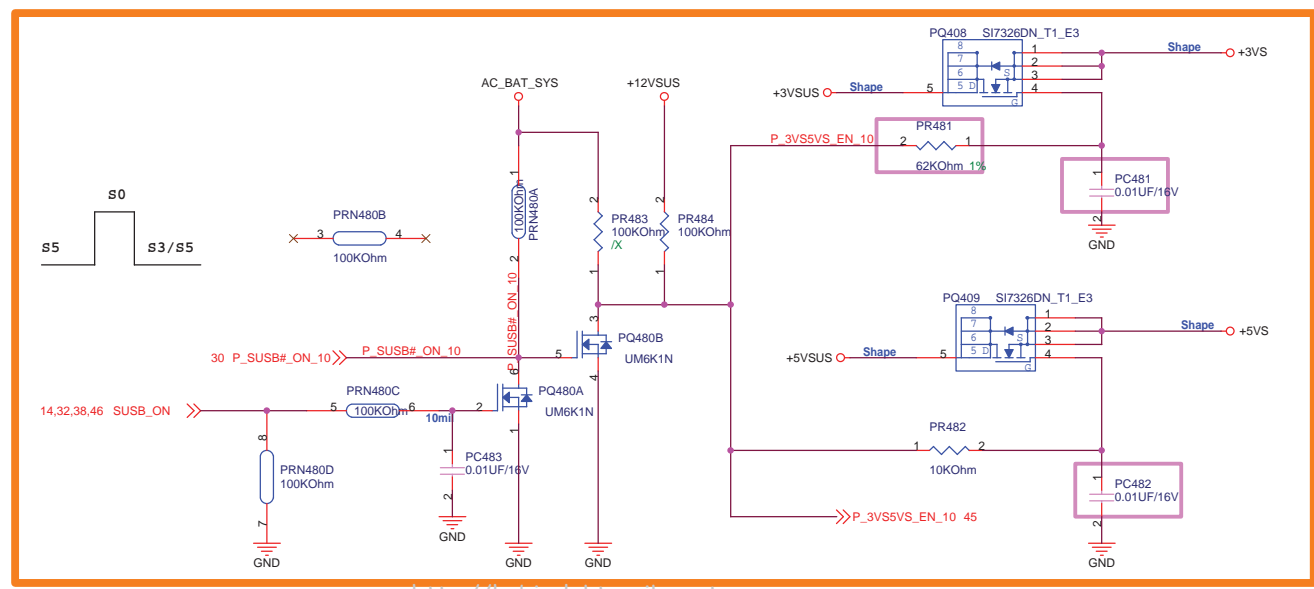
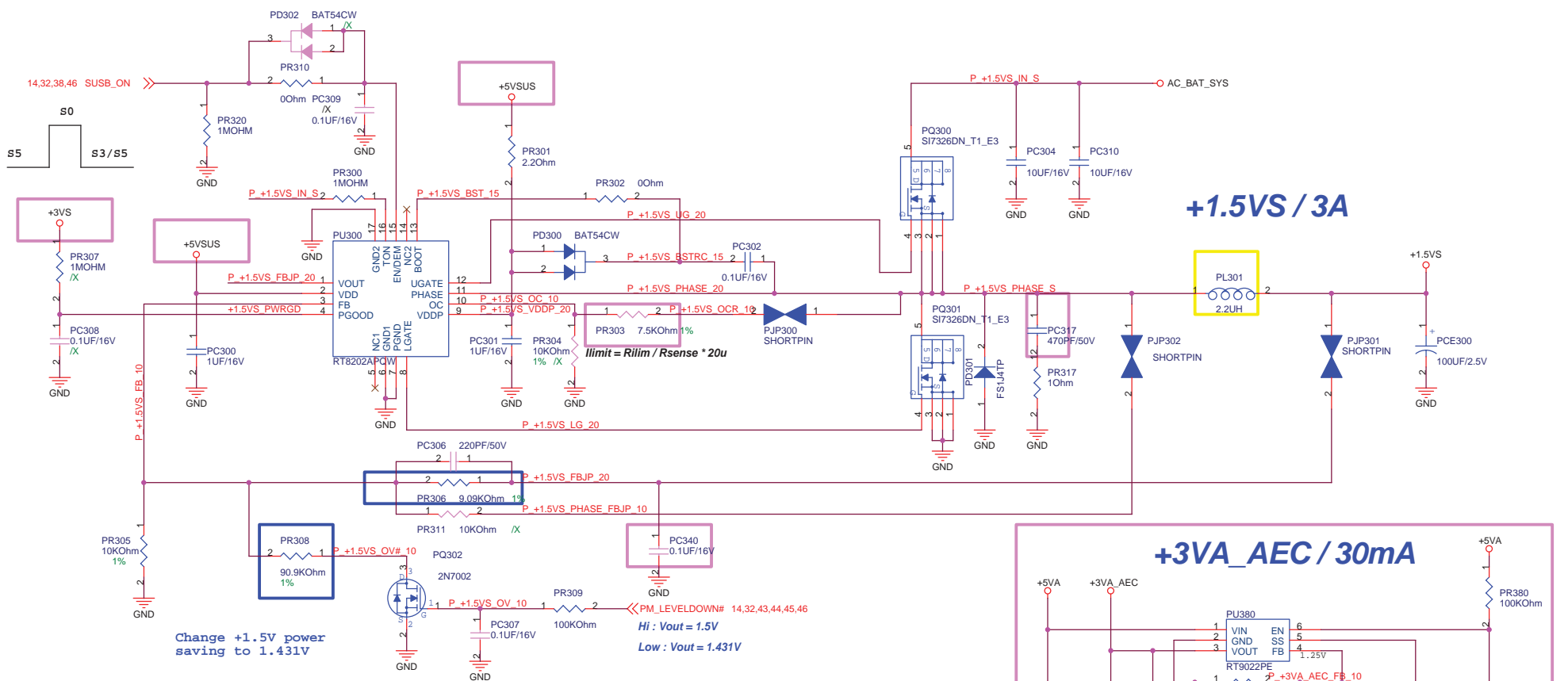
PM_LEVELDOWN#	CPU_LEVELDOWN	CPU_LEVELDOWN#	Voltage	Status
L	L	H	0.9V	Power Saving
H	L	H	1.048V	Normal
H	H	L	1.157V	Performance
L	H	L	1.072V	N/A

<Variant Name>

**ASUS** Title : VCCP

ASUSTek Computer INC. Engineer: Joy\_Zhou

Size	Project Name	Rev
A3	1002	1.3G
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<b>ASUS</b>		<b>Title : +1.5VS &amp; +2.5VS</b>	
ASUSTek Computer INC.		Engineer: <b>Joy_Zhou</b>	
Size	A3	Project Name	<b>1002</b>
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**Prevent Input from 19V :**  
 Adaptor > 14.5V, P\_19VDETEC\_10 High  
 Adaptor < 13.5V, P\_19VDETEC\_10 Low

**Prevent Input from 9.5V :**  
 Adaptor > 10.5V, P\_9VDETEC\_10 High  
 Adaptor < 10V, P\_9VDETEC\_10 Low

VREFIN = 3.396V  
 MAX8724\_REF = 4.096V  
 MAX8724\_LDO = 5.4V

**Pre-Charging Mode :**  
 Precharging current = 148mA  
 V<sub>ictl</sub> = 100mV

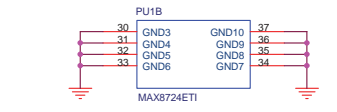
**Adaptor Max. Current :**  
 PR10 = 130K; I<sub>limit</sub> = 2.174A; 20.65W (9.5V/22W)  
 PR10/PR38 = 75.5K; I<sub>limit</sub> = 2.85A; 34.2W (12V/36W)

**ACIN Threshold = 2.048V**  
 Adaptor > 10.47V, System Powered by Adaptor  
 Adaptor < 10.47V, System Powered by Battery

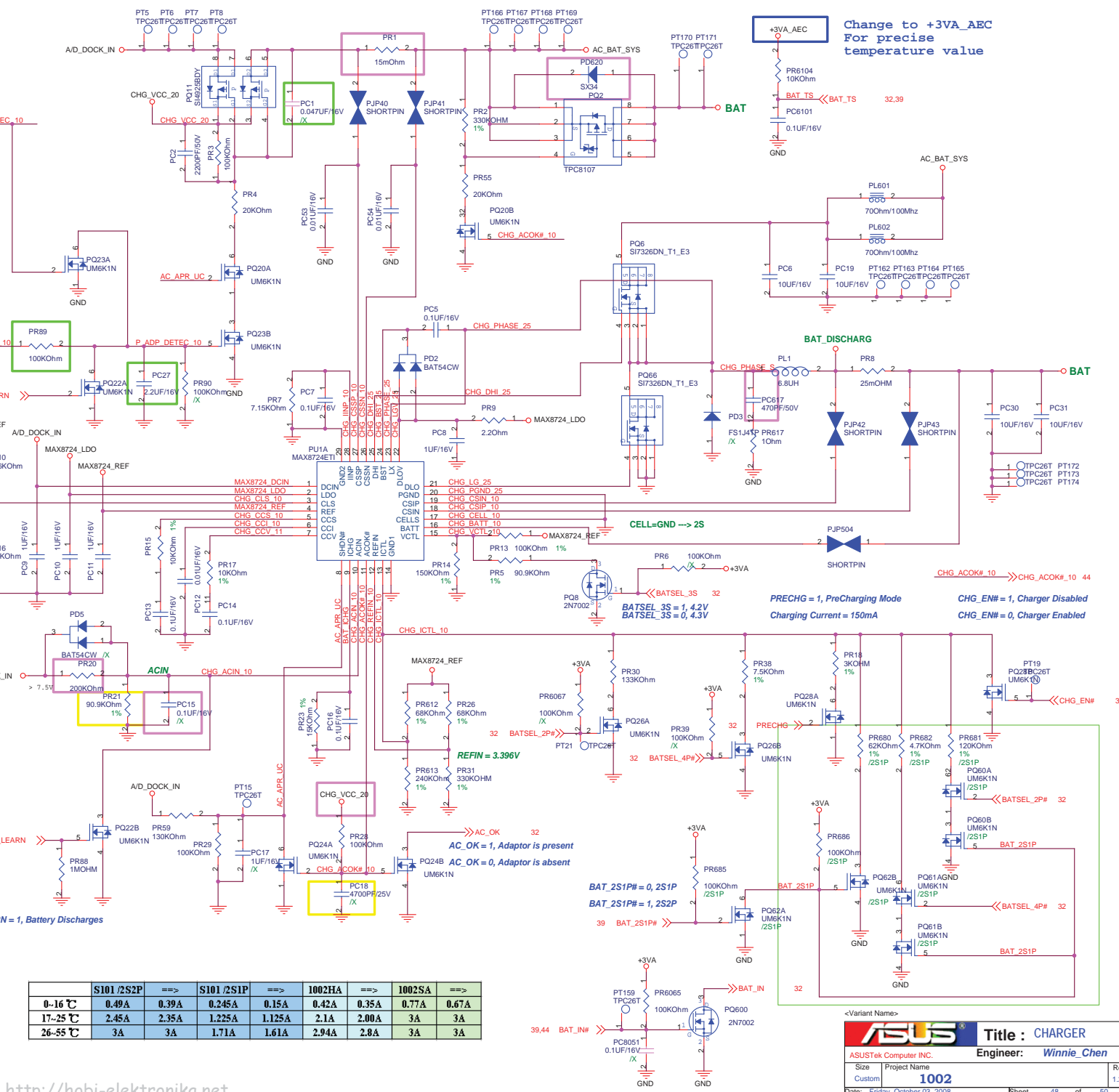
**Battery Charging Voltage :**  
 $BAT = Cell \times (4 + [0.4 * (V_{vctl} / V_{refin})])$

**Battery Charging Current :**  
 $I_{charge} = (0.075 / PR8) \times (V_{ictl} / V_{refin})$

**Input Adaptor Max. Current Limit :**  
 $I_{limit\_current} = (0.075 / PR1) \times (V_{cls} / 4.096)$



	S101 /2S2P	S101 /2S1P	1002HA	1002SA
0-16 °C	0.49A	0.39A	0.245A	0.15A
17-25 °C	2.45A	2.35A	1.225A	1.125A
26-55 °C	3A	3A	1.71A	1.61A
			2.94A	2.8A
			3A	3A



**Change to +3VA\_AEC**  
 For precise temperature value

<Variant Name>

**ASUS** Title : CHARGER

ASUSTek Computer INC. Engineer: Winnie\_Chen

Size	Project Name	Rev
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## EC KB3310 GPIO SETTING

Pin	Pin Name	Signal Name	Type	Note
1	GPIO00/GA20	A20GATE	O	
2	GPIO01/KBRST#	RC_IN#	O	
6	GPIO04	HOTKEY_SW0#	I	Internal pull high
13	GPIO05/PCIRST#	PCI_RST#	I	
14	GPIO07	HOTKEY_SW1#	I	Internal Pull Up
15	GPIO08	EXTSMH#	OD	10K ohm Pull Up to +3VSU
16	GPIO0A	LID_EC#	I	Internal pull high
17	GPIO0B/ESB_CLK	NC	O	
18	GPIO0C/ESB_DAT	NC	O	
19	GPIO0D	HOTKEY_SW2#	I	Internal pull high
20	GPIO0E/SC#	KBC_SC#	OD	10K ohm Pull Up to +3VSUS
21	GPIO0F/PWM0	BL_PWM_DA	O	
23	GPIO10/PWM1	BATSEL_4P#	O	Battery charging current setting
25	GPIO11/PWM2	PM_PWRBTN#	OD	Internal pull high in ICH
26	GPIO12/FANPWM1	FAN0_PWM	O	CPU Fan
27	GPIO13/FANPWM2	FAN1_PWM	O	VGA Fan
28	GPIO14/FANFB1	FAN0_TACH	I	CPU FanTach
29	GPIO15/FANFB2	FAN1_TACH	I	VGA FanTach
30	GPIO16/E51_TX	E51_TX	O	RS232 debug port
31	GPIO17/E51_RX	E51_RX	O	RS232 debug port
32	GPIO18	PWR_SW#	I	Internal pull high
34	GPIO19/PWM3	MAIL_LED#	O	
36	GPIO1A/NUMLED	NUM_LED#	O	
38	GPIO1D/CLKRUN#	NC	O	
39	GPIO20/KSO0/TP_TEST	KSO0	O	
40	GPIO21/KSO1/TP_PLL	KSO1	O	
41	GPIO22/KSO2	KSO2	O	
42	GPIO23/KSO3	KSO3	O	
43	GPIO24/KSO4	KSO4	O	
44	GPIO25/KSO5	KSO5	O	
45	GPIO26/KSO6	KSO6	O	
46	GPIO27/KSO7	KSO7	O	
47	GPIO28/KSO8	KSO8	O	
48	GPIO29/KSO9	KSO9	O	
49	GPIO2A/KSO10	KSO10	O	
50	GPIO2B/KSO11	KSO11	O	
51	GPIO2C/KSO12	KSO12	O	
52	GPIO2D/KSO13	KSO13	O	
53	GPIO2E/KSO14	KSO14	O	
54	GPIO2F/KSO15	KSO15	O	
55	GPIO30/KSI0	KSI0	I	Internal pull high
56	GPIO31/KSI1	KSI1	I	Internal pull high
57	GPIO32/KSI2	KSI2	I	Internal pull high
58	GPIO33/KSI3	KSI3	I	Internal pull high
59	GPIO34/KSI4	KSI4	I	Internal pull high
60	GPIO35/KSI5	KSI5	I	Internal pull high
61	GPIO36/KSI6	KSI6	I	Internal pull high
62	GPIO37/KSI7	KSI7	I	Internal pull high
63	GPI38/AD0	BAT_ICHG	I	
64	GPI39/AD1	BAT_CONFIG	I	Battery configuration
65	GPIO3A/AD2	BAT_SENSE	I	Battery Voltage Sensor
66	GPIO3B/AD3	BAT_TS	I	Battery Thermal Sensor
68	GPO3C/DA0	DOC	O	Trigger Clock Gen

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## EC KB3310 Other Pin SETTING

Pin	Pin Name	Signal Name	Type	Note
70	GPO3D/DA1	LCD_BACKOFF#	O	
71	GPO3E/DA2	CLK_PWRSERVE#	O	
72	GPO3F/DA3	BAT_LL#	O	Battery Low Low
73	GPIO40	AC_OK	I	AC Adaptor Plug in
74	GPIO41	PM_RSMRST#	O	10K pull down to GND
75	GPI42	BAT_IN	I	
76	GPI43	CLRTC_EC	I	
77	GPIO44/SCL1	SMB0_CLK	I/O	4.7K pull high to +3VA_EC
78	GPIO45/SDA1	SMB0_DAT	I/O	4.7K pull high to +3VA_EC
79	GPIO46/SCL2	SMB1_CLK	I/O	10K pull high to +3V
80	GPIO47/SDA2	SMB1_DAT	I/O	10K pull high to +3V
81	GPIO48/KSO16	KB pin 28	I	for KB type detection
82	GPIO49/KSO17	KB pin 27	I	for KB type detection
83	GPIO4A/PSCLK1	AUO_SCL	O	for AUO, default H at S0
84	GPIO4B/PSDAT1	AUO_SDA	O	for AUO, default L at S0
85	GPIO4C/PSCLK2	AUO_CSB	O	for AUO, default H at S0
86	GPIO4D/PSDAT2	LVDD_EN	I	for AUO 7" Panel
87	GPIO4E/PSCLK3	TP_CLK	I/O	10K pull high to +3V
88	GPIO4F/PSDAT3	TP_DAT	I/O	10K pull high to +3V
89	GPIO50/SELIO#	BATSEL_3S	O	Battery series, H:3S, L:4S
90	GPIO52/E51_CS#	CHG_LED_UP#	O	
91	GPIO53/CAPLED	CAP_LED#	O	
92	GPIO54	PWR_LED_UP	O	
93	GPIO55/SCRLED	SCR_LED#	O	
95	GPIO56	PWR4G_SW#	I	Internal pull high
97	GPX0A00/SDICS#	SPI_MODE#	O	4.7K pull down to GND
98	GPX0A01/SDICLK	SUSC_ON	O	
99	GPX0A02/SDIDO	VSUS_ON	O	
100	GPX0A03	CPU_VRON	O	
101	GPX0A04	SUSB_ON	O	
102	GPX0A05	ICH_PWROK	O	
103	GPX0A06	VOLT_CTRL	O	
104	GPX0A07	CHG_EN#	O	Battery charging enabled
105	GPX0A08	PRECHG	O	
106	GPX0A09	SPI_WP#	O	
107	GPX0A10	OP_SD#	O	Audio OP
108	GPX0A11	BAT_LEARN	O	
109	GPXID0/SDIDI	BATSEL_2P#	O	Battery parallel, H:1P, L:2P~3P
110	GPXID1	NC	O	
112	GPXID2	THRO_CPU	O	Active if CPU temperature over spec
114	GPXID3	SUSB#	I	100K pull down to GND
115	GPXID4	SUSC#	I	100K pull down to GND
116	GPXID5	CPUPWR_GD	I	Pull high to +3V
117	GPXID6	VSUS_GD	I	
118	GPXID7	NC	O	
121	GPIO57	INTERNET#	I	Internal pull high
126	GPIO57/SPICLK	SPI_CLK	O	
127	GPIO59/TEST_CLK	NC	O	


Pin	Pin Name	Signal Name	Type	Note
3	SERIRQ	INT_SERIRQ	I/O	10K pull high to +3V
4	LFRAME#	LPC_FRAME#	I	
5	LAD3	LPC_AD3	I/O	
7	LAD2	LPC_AD2	I/O	
8	LAD1	LPC_AD1	I/O	
9	VCC	+3VA_EC	P	
10	LAD0	LPC_AD0	I/O	
11	GND	GND	P	
12	PCICLK	CLK_PCI_EC	I	
22	VCC	+3VA_EC	P	
24	GND	GND	P	
33	VCC	+3VA_EC	P	
35	GND	GND	P	
37	ECRST#	EC_RST#	I	100K pull high to +3VA_EC
67	AVCC	+3VACC	P	
69	AGND	AGND	P	
94	GND	GND	P	
96	VCC	+3VA_EC	P	
111	VCC	+3VA_EC	P	
113	GND	GND	P	
119	RD#/SPIDI	SPL_SO	I	
120	WR#/SPIDO	SPL_SI	O	
112	XCLKI	32KXCLKI	I	
123	XCLKO	32KXCLKO	O	
124	V18R	V18R	P	Reserved 1uF to GND
125	VCC	+3VA_EC	P	
128	SPICS#/SELMEM#	SPI_CE#	O	

<Variant Name>

		<b>Title : EC Pin Define</b>	
ASUSTek Computer INC.		Engineer: <b>Satan He</b>	
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		<b>Title :</b> History	
ASUSTek Computer INC.		<b>Engineer:</b> <i>Satan He</i>	
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